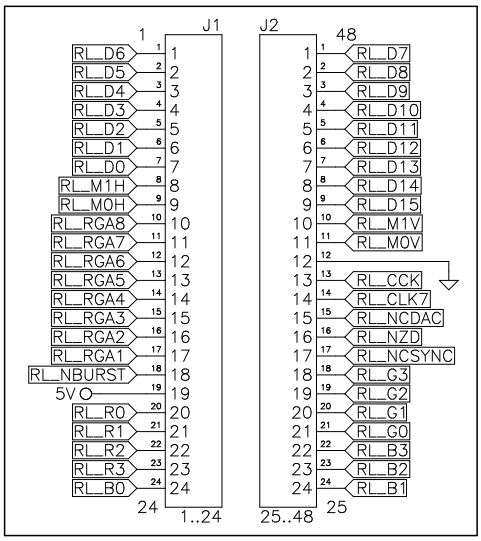
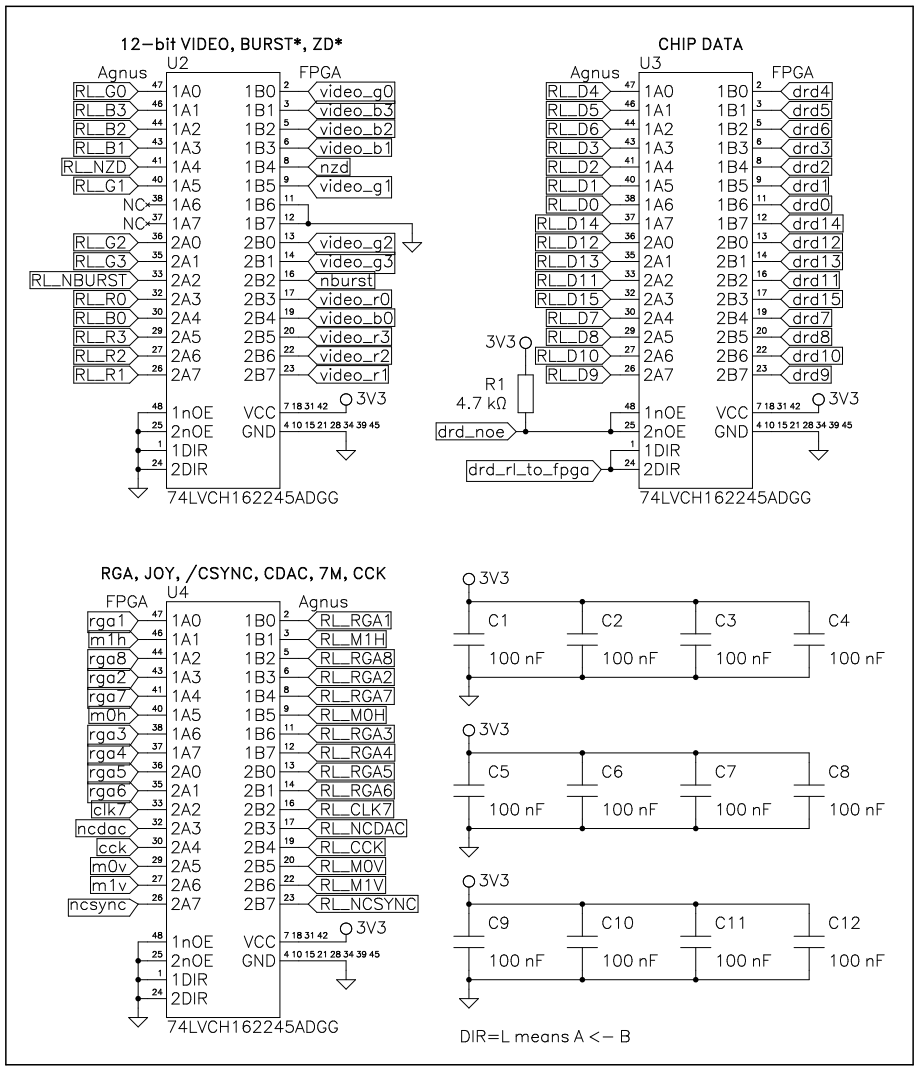
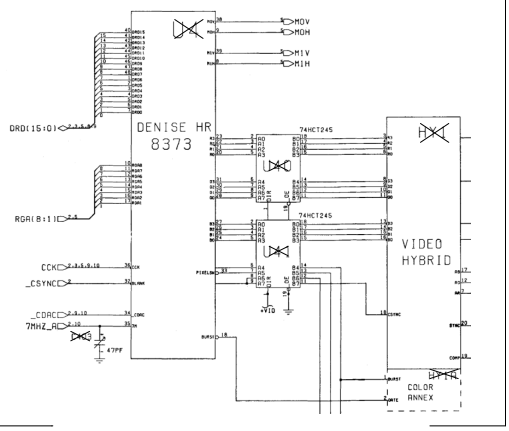
DENISE external interface



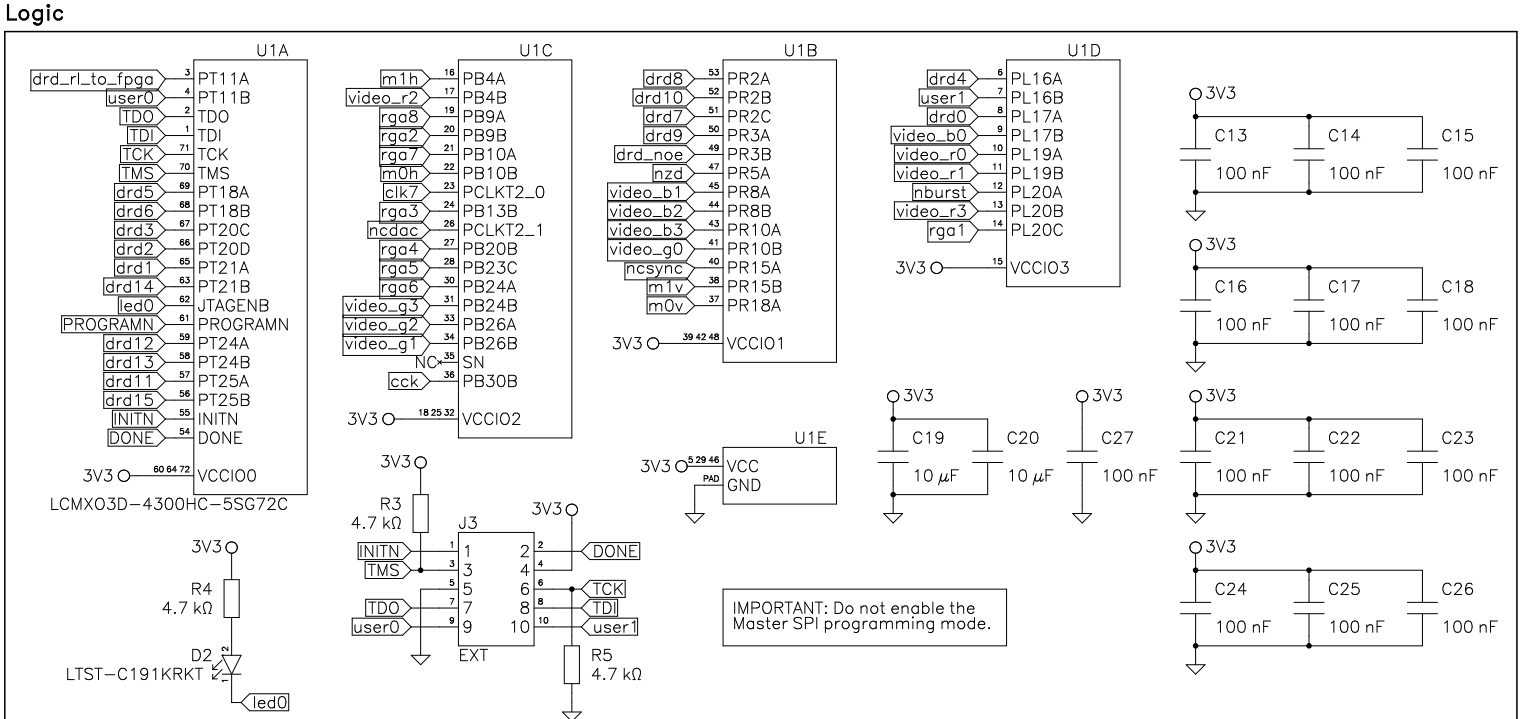
Level translation



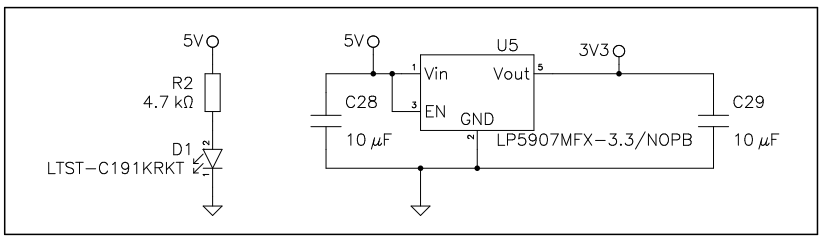
A500 reference. Not part of schematic.



Logic



Power



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Project: Deniser board	Revision: 1
Drawn by: Author	
Date: 2021−03−16	