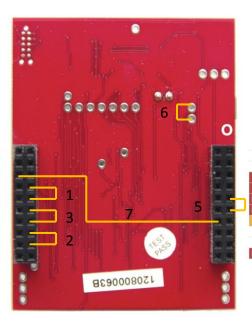


Flash 256 KB Serial hardware

## J2 J4 20 40 GROUND PF\_2 SCK (1) CS (1) PB\_2 19 39 PF\_3 RX (7) MISO (1) 18 38 PB\_3 A3 PF\_0 17 37 PC\_4 16 36 PC\_5 PB\_7 15 35 MOSI (2) PC\_6 PB\_6 14 34 MISO (2) PC\_7 MISO (0) 13 33 PD\_6 12 32 CS (0) PA\_3 PD\_7 11 31 SCK (0)

## StellarPad with LM4F120H5QR

Revision 1



	Pin number
0	ther pin number
	Serial UART
	SPI
	analogRead()
digitalF	Read() and digitalWrite()
digitalR	ead(), digitalWrite() and analogWrite()

			J3	JI				
	VBUS		21	1		+3.3V		
	GND		22	2	PB_5	A11		CS (2)
SCK (3)	A7	PD_0	23	3	PB_0			RX (1)
CS (3)	A6	PD_1	24	4	PB_1			TX (1)
MISO (3)	A5	PD_2	25	5	PE_4	A9	SCL (2)	RX (5)
MOSI (3)	A4	PD_3	26	6	PE_5	A8	SDA (2)	TX (5)
TX (7)	A2	PE_1	27	7	PB_4	A10		SCK (2)
	A1	PE_2	28	8	PA_5			MOSI (0)
	A0	PE_3	29	9	PA_6			
MOSI (1)	RED_LED	PF_1	30	10	PA_7			

PA_0	RX (0)
PA_1	TX (0)
PD_4	RX (6)
PD_5	TX (6)

Rei Vilo, 2012

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