

Diagram of RJ12-6P6C J2 connector wiring. The connector is shown on the left with pins 1 through 6. Pin 1 is connected to +5V. Pin 2 is labeled DATA_REQ. Pin 3 is connected to pin 1. Pin 4 is crossed out with an X. Pin 5 is labeled RX_P1. Pin 6 is connected to GND. The connector is labeled RJ12-6P6C J2. To the right, a 6-pin connector labeled J4 Conn_01x06 is shown with pins 1 through 6. Pins 1, 2, 3, 5, and 6 are connected to the corresponding pins of J2. Pin 4 is crossed out with an X. The text "Additional SMD pads for bottoms side" is written below the J4 connector.

[illegible]

Current limit & Voltage regulation

The circuit diagram illustrates a system for current limiting and voltage regulation. It features a +5V input connected to a network of resistors (R1, R2, R3, R9) and two BC807 transistors (Q1, Q2). A 1k resistor (R1) is connected to ground. The transistors are configured to sense current and regulate the output. A diode D2 (SS14) is used for protection. The core component is the AP7365-33WG-7 voltage regulator (U1), which takes input from the +5V line and provides a regulated +3.3V output. The regulator's pins are labeled VIN, EN, VOUT, and GND. Various capacitors (C1, C2, C3, C6) are used for filtering and stability. The output voltage is +3.3V, and the input voltage is +5V. The circuit is labeled with component values and types, such as R1 1k, Q1 BC807, Q2 BC807, D2 SS14, U1 AP7365-33WG-7, C1 1uF, C2 1uF, C3 100nF, and C6 470uF.

For 5V programmer support

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