

Liab ApS

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Title: **NANOLIAB, OVERVIEW**

Schematic Name: W:\PROJECTS\NANOLIAB\NANOLIAB1A-1.0.3_0.DSP

Date: Tuesday, October 03, 2006

Sheet: 1 of 8

U201C

AT91RM9200 MICROPROCESSOR

PORT C AND PORT D

PC1/BFRDY/SMOE
PC2/BFAVD
PC3/BFBAA/SMWE
PC4/BFOE
PC5/BFWE
PC6/NWAIT

PC10/NCS4/CFCS
PC11/NCS5/CFCE1
PC12/NCS6/CFCE2
PC13/NCS7
PC14
PC15

PD0/ETX0
PD1/ETX1
PD2/ETX2
PD3/ETX3
PD4/ETXEN
PD5/ETXER
PD6/DTXD
PD7/PCK0
PD8/PCK1
PD9/PCK2
PD10/PCK3
PD11
PD12
PD13
PD14
PD15/TD0
PD16/TD1
PD17/TD2
PD18/NPCS1
PD19/NPCS2
PD20/NPCS3
PD21/RTS0
PD22/RTS1
PD23/RTS2
PD24/RTS3
PD25/DTR1
PD26
PD27

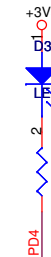
HDPA
HDMA

HDPB
HDMB

DDP
DDM

AT91RM9200BGA

ETHINT
ETHPD



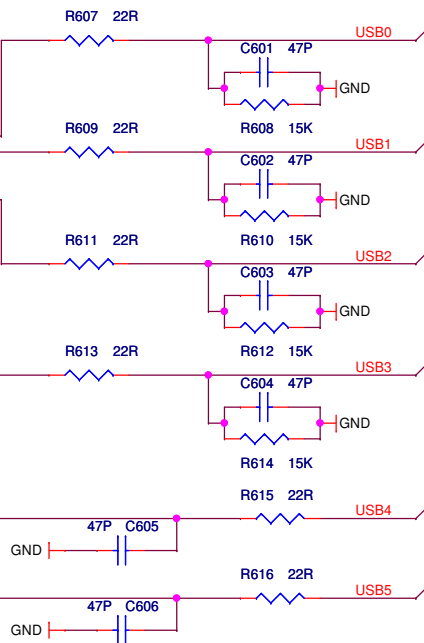
PD[0..5]

RTS0

DTR1

USB[0..5]

USB[0..5]



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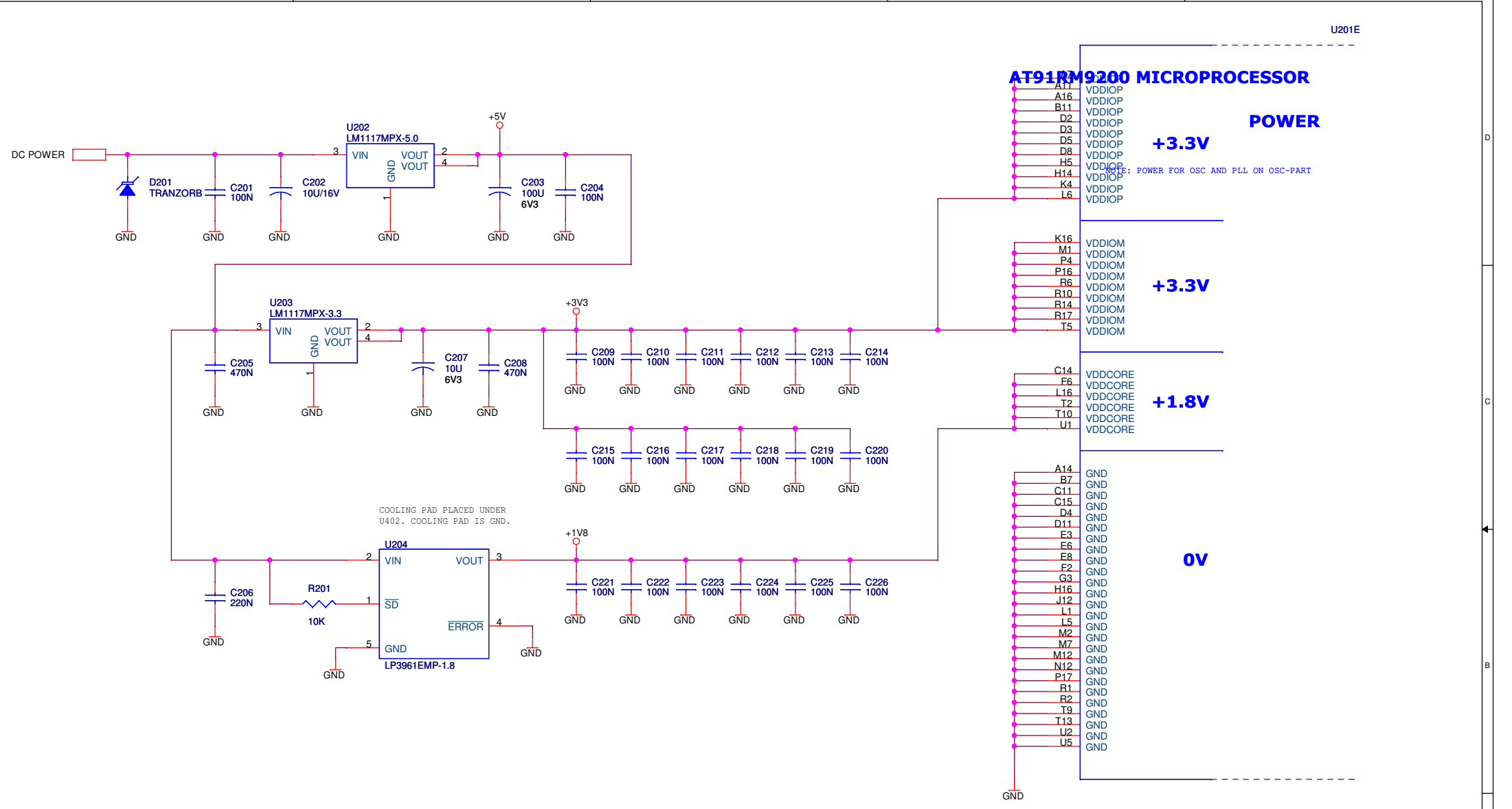
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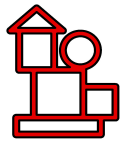
Title: **NANOLIAB, PORT C, D AND USB**

Schematic Name: W:\PROJECTS\NANOLIAB\NANOLIAB1A-1.0.3_0.DS Rev: 1.0

Date: Monday, October 02, 2006

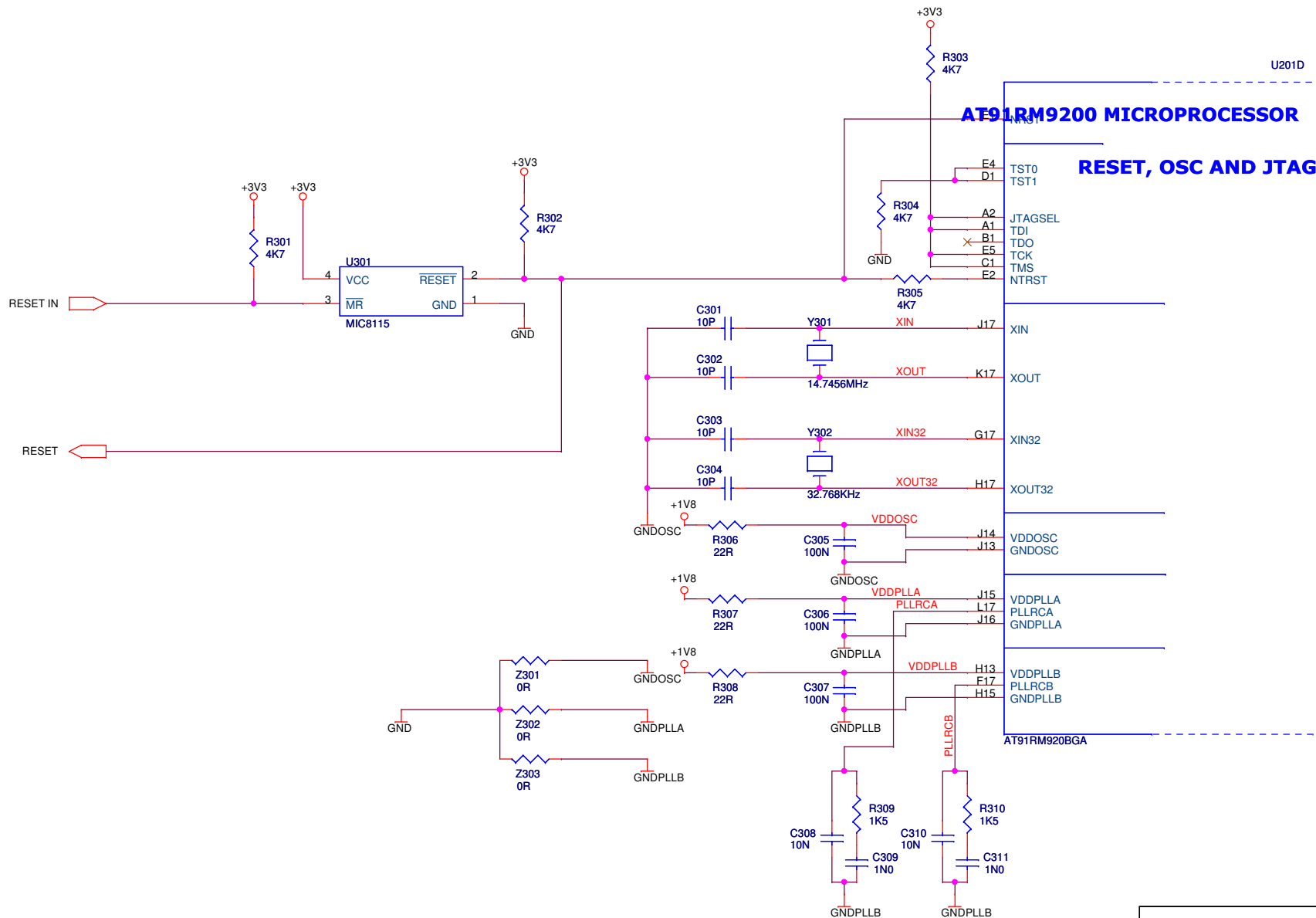
Sheet: 6 of 8





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AT91RM9200

| | |
|---|---------------|
| Title: NANOLIAB, POWER SUPPLY | |
| Schematic Name: W:\PROJECTS\NANOLIAB\NANOLIAB1A-1.0.3_0.DSN | |
| Date: Wednesday, October 04, 2006 | Sheet: 2 of 8 |



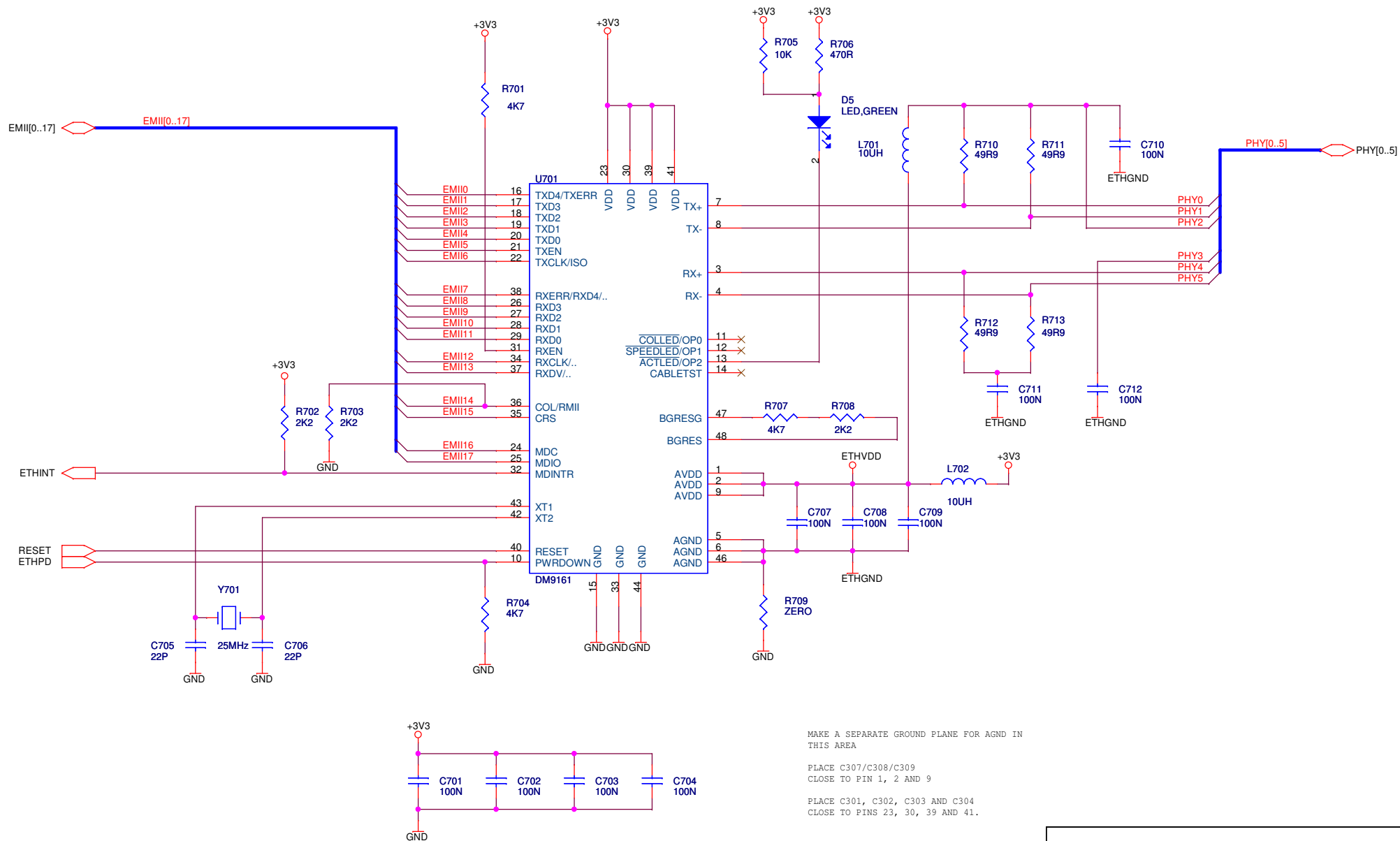
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Title: **NANOLIAB, OSCILLATORS AND RESET**

Schematic Name: W:\PROJECTS\NANOLIAB\NANOLIAB1A-1.0.3_0.DS Rev: 1.0

Date: Tuesday, October 03, 2006 Sheet: 3 of 8



MAKE A SEPARATE GROUND PLANE FOR AGND IN THIS AREA

PLACE C307/C308/C309
CLOSE TO PIN 1, 2 AND 9

PLACE C301, C302, C303 AND C304
CLOSE TO PINS 23, 30, 39 AND 41.



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Title: **NANOLIAB, ETHERNET**

Schematic Name: W:\PROJECTS\NANOLIAB\NANOLIAB1A-1.0.3_0.DS Rev: 1.0

Date: Monday, October 02, 2006

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