A Simple Von Neumann-Based Processor Implementation on FPGA

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*Abstract* — Write the summary of the study here.

Keywords — FPGA, CPU

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# Introductıon

Write text in this field about what the project is and its purpose.

# System Archıtecture

Provide information about the development tools used, simulators and the architecture of the design.

# Software Used

Describe the reference testing software used in this area.

# Results

The operations supported by the developed processor, the knowledge and gains obtained with this project will be explained.

##### Project team

Write short CVs of the project team in this field.

##### Reference Files

In this area, the YouTube video link of the study and the links to the github files will be written.

##### References

1. Smith, JO and Abel, JS, ``Bark and ERB Bilinear Trans ­forms'', *IEEE Trans. Speech and Audio Proc* ., 7(6):697-708, 1999.
2. Lee, K.-F., *Automatic Speech Recognition: The Development of the SPHINX SYSTEM,* Kluwer Academic Publishers, Boston, 1989.
3. Rudnicky, AI, Polifroni, Thayer, E H., and Brennan, RA "Interactive problem solving with speech", *J. Acoust. Soc. Amer* ., *Vol. 84, 1988, p S213(A).*