Jonas Markussen

R&D Systems Software Engineer



Software Architect Senior Software Engineer 2019 —

2018 — 2019 L

Dolphin Interconnect Solutions

- Implemented an NVMe device driver and block device interface on top of SmartIO, allowing multiple hosts in a PCIe cluster to share and simultaneously access NVMe devices without requiring virtualization.
- Responsible for turning the SmartIO framework implementation from my PhD into a standard Dolphin product.
- Developed multiple shared-memory and RDMA-based benchmarking tools in order to measure the performance of data transfers.
- Contributed to two research papers on offloading a DPDK network card driver onto an FPGA and on a GPU respectively.

External PhD Student PhD Student

2018 - 2022

2015 — 2018

Simula Research Laboratory

- Was a full-time PhD student until my scholarship ran out, and then completed the PhD on my own time while I worked for Dolphin.
- Designed, implemented, and evaluated the SmartIO framework for highperformance, distributed I/O by making it possible for host machines in a Dolphin PCIe shared-memory cluster to disaggregate and share their internal I/O resources (PCIe devices and memory resources).
- Extended SmartIO with Linux KVM hypervisor support using VFIO mediated device drivers, allowing physical devices to be assigned to VMs running on remote hosts in the cluster.
- Developed a CUDA/C++ library for SmartIO with GPUDirect support, and, as proof of concept, implemented a userspace NVMe driver that enable GPUs to initiate reading and writing from storage independently of CPUs (driver running on GPU itself), and moving data from remote NVMe devices anywhere in the cluster directly into GPU memory.
- Published 5 academic papers about SmartIO, including a paper in ACM TOCS, a flagship journal on distributed computer systems.

Software Developer

2014 - 2015

Bridgetech

- Contributed to a project using libpcap for live capturing IPTV traffic over RTP or MPEG-DASH and parsing out MPEG transport streams.
- Implemented a parser for MPEG video streams in order to extract and validate closed captioning data and provide real-time event notifications in case of missing or corrupted data.

Software Development Engineer (web back-end) 2013 — 2014 Front-end Web Developer 2011 — 2013

Fotoware

Java Programmer (part-time) 2010 — 2011

Redimi

Teaching Assistant 2009 — 2011

University of Oslo

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EDUCATION

PhD, Informatics 2015 — 2022 University of Oslo

& Simula Research Laboratory

Doctoral degree in computer science.

MSc, Informatics 2010 — 2014 University of Oslo & Simula Research Laboratory

Master's degree in computer science.

BSc, Informatics 2006 — 2010 University of Oslo

Bachelor's degree in computer science.

SKILLS & EXPERTISE

Software Engineering
C, Python, C++, JavaScript, Bash, git, docker, CI/CD, PHP, Java.

Systems & Embedded Programming Linux kernel hacking, PCIe device drivers, microcontrollers, virtual machines, memory and resource virtualization, PCIe NTBs, Linux KVM/VFIO, memory architectures, NVMe, CUDA/GPUDirect.

Distributed & Parallel Computing
Distributed systems, distributed sharedmemory applications, cluster computing,
high-performance computing, RDMA, GPU
programming, ultra low-latency networking.

器 Network Programming

Transport layer protocols, IP routing protocols, WLAN & MANET protocols, PIM-SM multicasting, QoS, AQM & traffic engineering, libpcap, REST API design, HTTP.

■ SELECTED PUBLICATIONS

- J. Markussen. "SmartIO: Device sharing and memory disaggregation in PCIe cluster using non-transparent bridging". PhD thesis. 2022. DOI: 10852/97351
- J. Markussen, L.B. Kristiansen, P. Halvorsen, H. Kielland-Gyrud, H.K. Stensland, C. Griwodz. "SmartIO: Zero-overhead Device Sharing through PCle Networking". ACM Transactions on Computer Systems (TOCS), vol. 38, no. 1–2. 2021. DOI: 10.1145/3462545

A list of publications can be found at https://dblp.org/pid/169/0395.html