- Atmel SAM3
- Atmel SAMD10/SAMD20/SAN

Α

- ► Atmel SAML21
- ► CC26xx/CC13xx common
- ► CC26xx/CC13xx common
- **▶** ESP Common
- ▼ ESP32 SoC Series
  - ► ESP-IDF Interface API
- ► ESP-NOW netdev interface
- ► ESP-WiFi netdev interface
- ► ESP32 Bluetooth LE HCl fc
- ► ESP32 CAN controller
- ► ESP32 Ethernet netdev inte
- ► ESP32 compile configuration
  ESP32 family

## ESP32-C3 family

ESP32-S2 family

ESP32-S3 family

sdkconfig\_default\_commor

sdkconfig\_default\_esp32.h sdkconfig\_default\_esp32c3

sdkconfig\_default\_esp32s2

sdkconfig\_default\_esp32s3

esp\_log.h

adc arch.h

adc\_arch\_private.h

cpu conf esp32.h

cpu\_conf\_esp32c3.h

cpu\_conf\_esp32s2.h

GPIO2, GPIO8 and GPIO9 are bootstrapping pins which are used to boot ESP32-C3 in different modes:

GPIO2	GPIO8	GPIO9	Mode	
1	X	1	SPI Boot mode to boot the firmware from flash (default mode)	
1	1	0	Download Boot mode for flashing the firmware	

## ADC Channels

ESP32-C3 integrates two 12-bit ADCs (ADC1 and ADC2) with 6 channels in total:

- ADC1 supports 5 channels: GPIO0, GPIO1, GPIO2, GPIO3 and GPIO4
- ADC2 supports 1 channel: GPIO5 or internal signals such as vdd33

The maximum number of ADC channels ADC NUMOF MAX is 6.

## Note

- ADC2 is also used by the WiFi module. The GPIOs connected to ADC2 are therefore not available as ADC channels if the modules esp\_wifi or esp\_now are used.
- Vref can be read with function adc\_line\_vref\_to\_gpio at GPIO5.

## 12C Interfaces

ESP32-C3 has one built-in I2C interfaces.

The following table shows the default configuration of I2C interfaces used for ESP32-C3 boards. It can be overridden by application-specific configurations.

Device	Signal	Pin	Symbol	Remarks
I2C_DEV(0)			#I2C0_SPEED	default is I2C_SPEED_FAST