

**ECE541 - HWK6**

In small bus-based multiprocessors, write-through caches are sometimes used because the coherence protocol can be simplified compared to write-back caches. Show how the basic snooping protocol from the textbook, which is designed for write-back caches, can be changed for a write-through cache.

**2.**

This protocol leverages a shared state where multiple bus-connected-devices can leverage (read) data in a shared state without invalidating the data, but can invalidate when necessary to write data.