

The schematic diagram illustrates the system architecture across six vertical sections representing different stages or components:

- Section 1:** A blue box labeled "Access Manager" contains a red circle with a white dot. Below it is a green box labeled "SP-DF".
- Section 2:** Two grey boxes are stacked vertically, labeled "UG" (top) and "OI" (bottom).
- Section 3:** Similar to Section 2, with grey boxes labeled "OH" (top) and "OU" (bottom).
- Section 4:** Similar to Section 2, with grey boxes labeled "UI" (top) and "UFE" (bottom).
- Section 5:** Similar to Section 2, with grey boxes labeled "OI" (top) and "UFF" (bottom).
- Section 6:** Similar to Section 2, with grey boxes labeled "UI" (top) and "UFG" (bottom).

Horizontal connections between sections include:

- A blue arrow pointing right from the Access Manager in Section 1 to the SP-DF in Section 2.
- A blue arrow pointing left from the SP-DF in Section 2 to the Access Manager in Section 3.
- A blue arrow pointing right from the SP-DF in Section 3 to the SP-DF in Section 4.
- A blue arrow pointing left from the SP-DF in Section 4 to the SP-DF in Section 5.
- A blue arrow pointing right from the SP-DF in Section 5 to the SP-DF in Section 6.
- A blue arrow pointing left from the SP-DF in Section 6 to the SP-DF in Section 7.

Each section also features a small blue box at the top containing a red circle with a white dot, and a small blue box at the bottom containing a red circle with a white dot. These boxes are connected by horizontal lines to the main components in each section.

COUNTDOWN			
DE U	OR	OUT UU	UUU
000 GA	000 EH	000 EH	000 EH
000 GA	000 EH	000 EH	000 EH
000 GA	000 EH	000 EH	000 EH

[illegible]

ÔUÏÛT ÒÈVÙÙ

[illegible][illegible][illegible]

	PÁVIER UOÓBÓT K	ÖÜÜ	
		ÖSÜÖU ÜÄÖÖ-ÖFÄÖH	
		ÜÜRÖU ÖÜVWWMÖS EÄÖK ÖVU	PÄÖÖÖP

PUT	0000000000000000	0000000000000000	0000000000000000	0000000000000000
DATE	00000000	00000000	0000000000000000	00000000
TIME	00000000	00000000	0000000000000000	00000000