

國立臺灣大學電機資訊學院電子工程學研究所

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本論文係○○○君(○學號○)在國立臺灣大學電子工程學研究所完成之碩士學位論文，於民國 107 年 05 月 22 日承下列考試委員審查通過及口試及格，特此證明

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2018.05.24 ○○○

中文摘要

(英翻中。)

Your Thesis Title

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Graduate Institute of Electronics Engineering

National Taiwan University

Taipei, Taiwan

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Abstract





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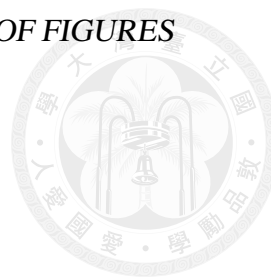
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Chapter 1

Introduction

Hello, World!

1.1 Figure

We are from Media IC & System Lab, as shown in Figure 1.1.¹

1.1.1 Table

The related information is shown in Table 1.1.

¹All the images in the dissertation are used under Creative Commons license



Figure 1.1: We are from Media IC & System Lab.

Table 1.1: Lab information.

Property	Description
Professor	Shao-Yi Chien
Labs	BL421, MD431, MD726



Chapter 2

Related Work

Prof. Chien's most cited paper [[1](#)].





Reference

- [1] S.-Y. Chien, S.-Y. Ma, and L.-G. Chen, "Efficient Moving Object Segmentation Algorithm Using Background Registration Technique," *IEEE Transactions on Circuits and Systems for Video Technology (TCSVT)*, vol. 12, no. 7, pp. 577–586, 2002. [3](#)