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Professor Anderson

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Lab 2 – Part 1: Updating the Virtual Format

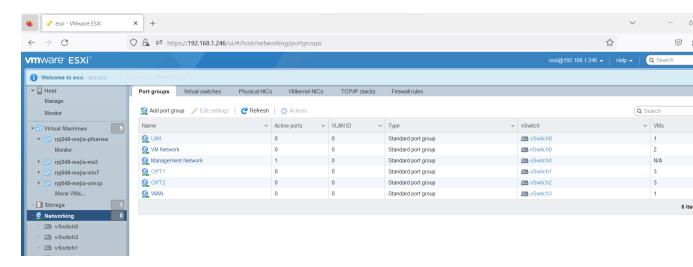


Figure 5 (Screen shot 1): Port group assignments for Lab 2 and sample turn-in artifact.

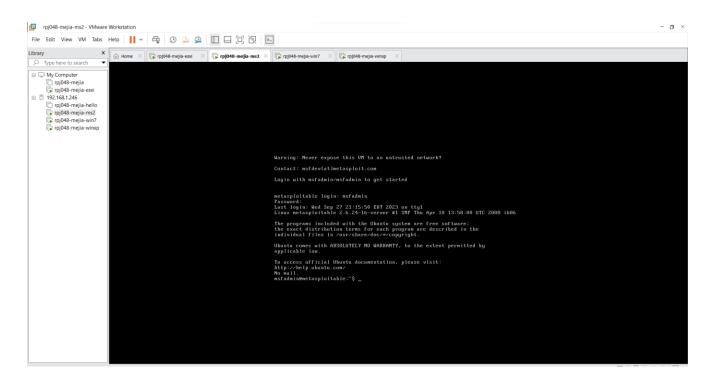


Figure 10 (Screen shot 2): Sample MS2 image with required information.

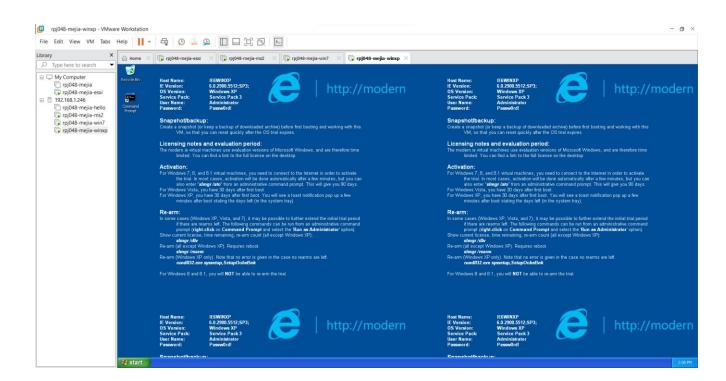


Figure 11 (Screen shot 3): Sample Windows XP image with required information.

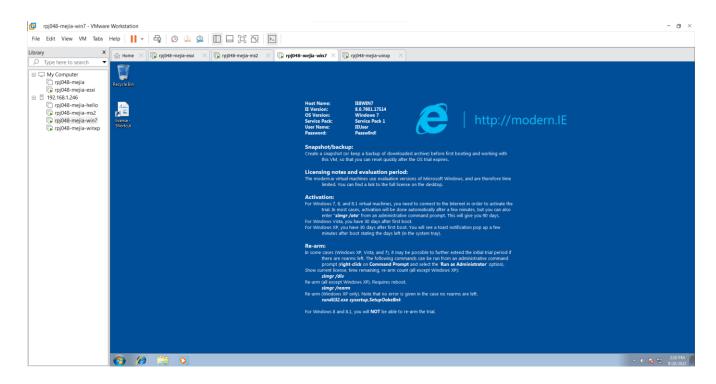


Figure 12 (Screen shot 4): Sample Windows 7 image with required information.

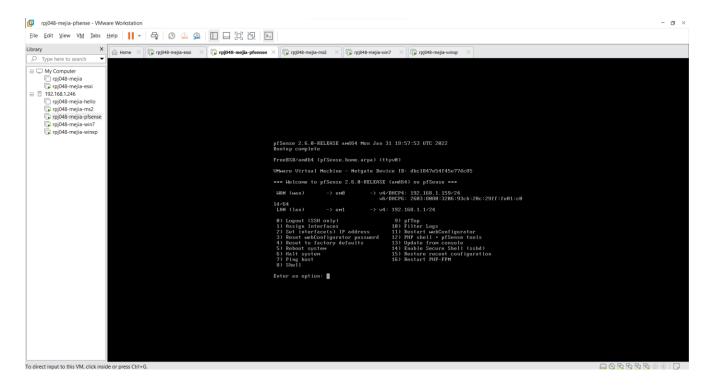


Figure 14 (Screen shot 5): Sample pfSense screen shot.

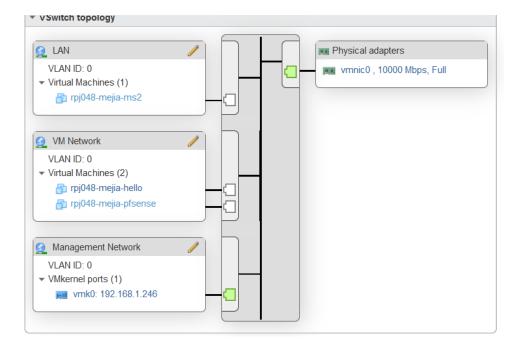


Figure 15 (Screen shot 6): vSwitch topology for vSwitch0.

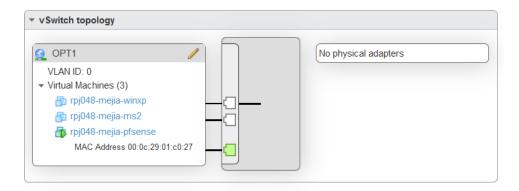


Figure 16 (Screen shot 7): vSwitch topology for vSwitch1.

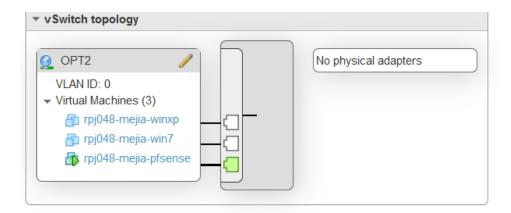


Figure 17 (Screen shot 8): vSwitch topology for vSwitct2.

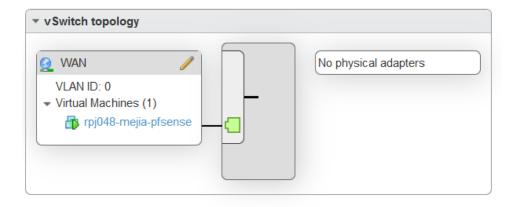


Figure 18 (Screen shot 9): vSwitch topology for vSwitch3.