En-Jung Tsai

Email: enrong.tsai@gapp.nthu.edu.tw Mobile: (+886) 978-508-523

LinkedIn & Github

Summary

- 2+ years of machine learning and deep learning development experience for multiple applications, including Face Recognition, Defect Inspections, and Prediction-Based Scheduling for real-time industrial IoT data.
- 2+ years of research experience in designing novel Attention Models and Loss Function for large-scale face recognition models.

EDUCATION



National Tsing Hua University

Hsinchu, Taiwan

Master of Science in Industrial Engineering; GPA: 3.85

Sep. 2018 - Aug. 2021

• Thesis: PAM: Pose Attention Module for Pose-Invariant Face Recognition (CVPR 2022 Submitted)



National Chung Cheng University

Chiayi, Taiwan

Bachelor of Arts in Economics; GPA: 3.46

Sep. 2012 - Jun. 2016

SKILLS

- Programing Language: Python, SQL, Matlab, C/C++
- ML & CV Toolkits: PyTorch, TensorFlow, OpenCV, Scikit-learn, Numpy, Pillow, Pandas, etc.
- Other Tools: Linux, Git, Bash, Vim, Tmux, LATEX, GCP, etc.

EXPERIENCE



Research Assistant

Integration & Collaboration Lab (PI: Dr. Wei-Chang Yeh)

Feb. 2019 - Aug. 2021

- Publish paper as first author (CVPR 2022 Submitted)
- Designed and implemented deep face recognition model with large-scale dataset (MS1MV2) to solve the intra-class imbalance problem caused by pose variation and feature imbalance problem.
- Proposed a lightweight yet effective module, named Pose Attention Module (PAM), that improves the robustness of pose-invariant representation learning for deep face recognition.
- Effectively reduce the memory requirements by more than **75 times** compared to the state-of-the-art lightweight method DREAM (CVPR 2018). Achieved the state-of-the-art performance in 5 face recognition benchmarks, including LFW, CFP-FP, Age-DB, CPLFW, and CALFW.



Machine Learning Intern

AU Optronics Corporation

 $Jul.\ 2020$ - $Aug.\ 2020$

- Develop an evaluation system for the periodic preventive maintenance(PM) process of ITO Sputtering Targets.
- Implement state-of-the-art deep learning algorithms to solve defect detection problems in manufacturing at > 95% precision and recall.
- Deploy end-to-end inference pipeline to Nvidia Jetson edge devices. Optimize algorithms to provide real-time inference results for PM scheduling.
- The system has increased the overall production capacity by 5.5% by effectively controlling periodic PM costs and downtime. The system has now replaced the original manual process, and is **currently patent pending**.



Teaching Assistant

 ${\bf IEEM1080105:\ Artificial\ Intelligence}$

Sep. 2019 - Jan. 2020

- Lectured to students with 3 research topics: the recent progress of deep learning-based face recognition, the evolution of loss function design, and the attention mechanism of neural networks.
- Provided support for students with learning difficulties.

References

• Wei-Chang Yeh / 葉維彰 (PI of Integration & Collaboration Lab, I&C Lab)

Distinguished Professor, Department of Industrial Engineering, National Tsing-Hua University, Taiwan.

Email: yeh@ieee.org Tel: +886-3-574-2443

• Tzu-Ching Huang / 黄姿菁 (Mentor of AUO internship)

Senior Engineer, Thin Film Engineering Dept., AU Optronics Corporation, Taiwan.

Email: TzuChing. Huang@auo.com Tel
: +886-3-407-8899 (Ext.2791)