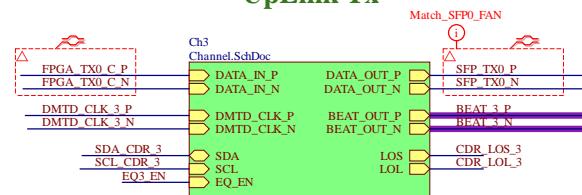


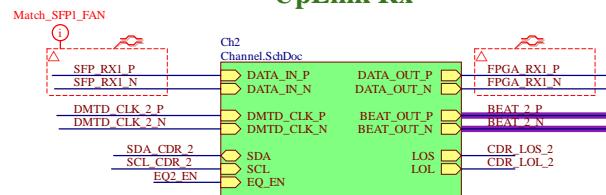
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Dedicated to GT 1 (Channel 1) on Quad 0 and SFP 1

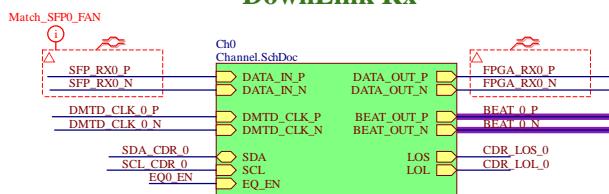
UpLink Tx



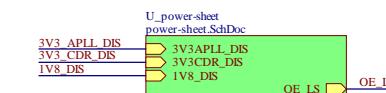
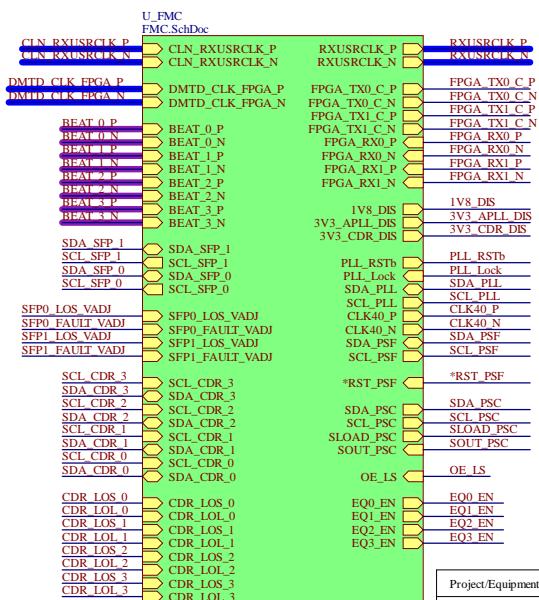
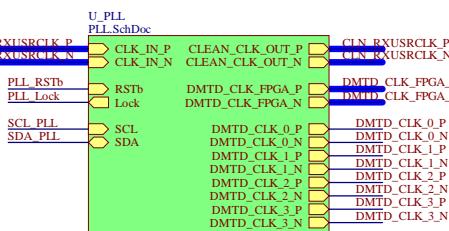
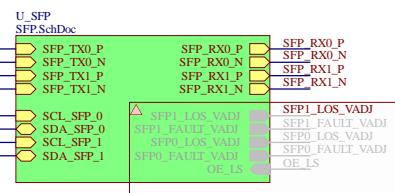
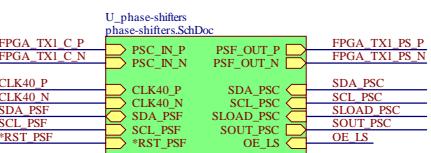
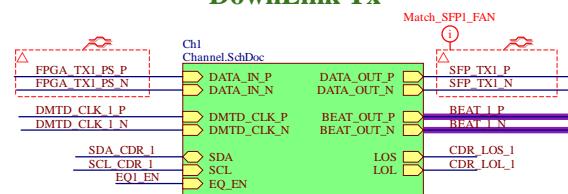
UpLink Rx



DownLink Rx



DownLink Tx



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3. Append sheet number to local nets (NO)
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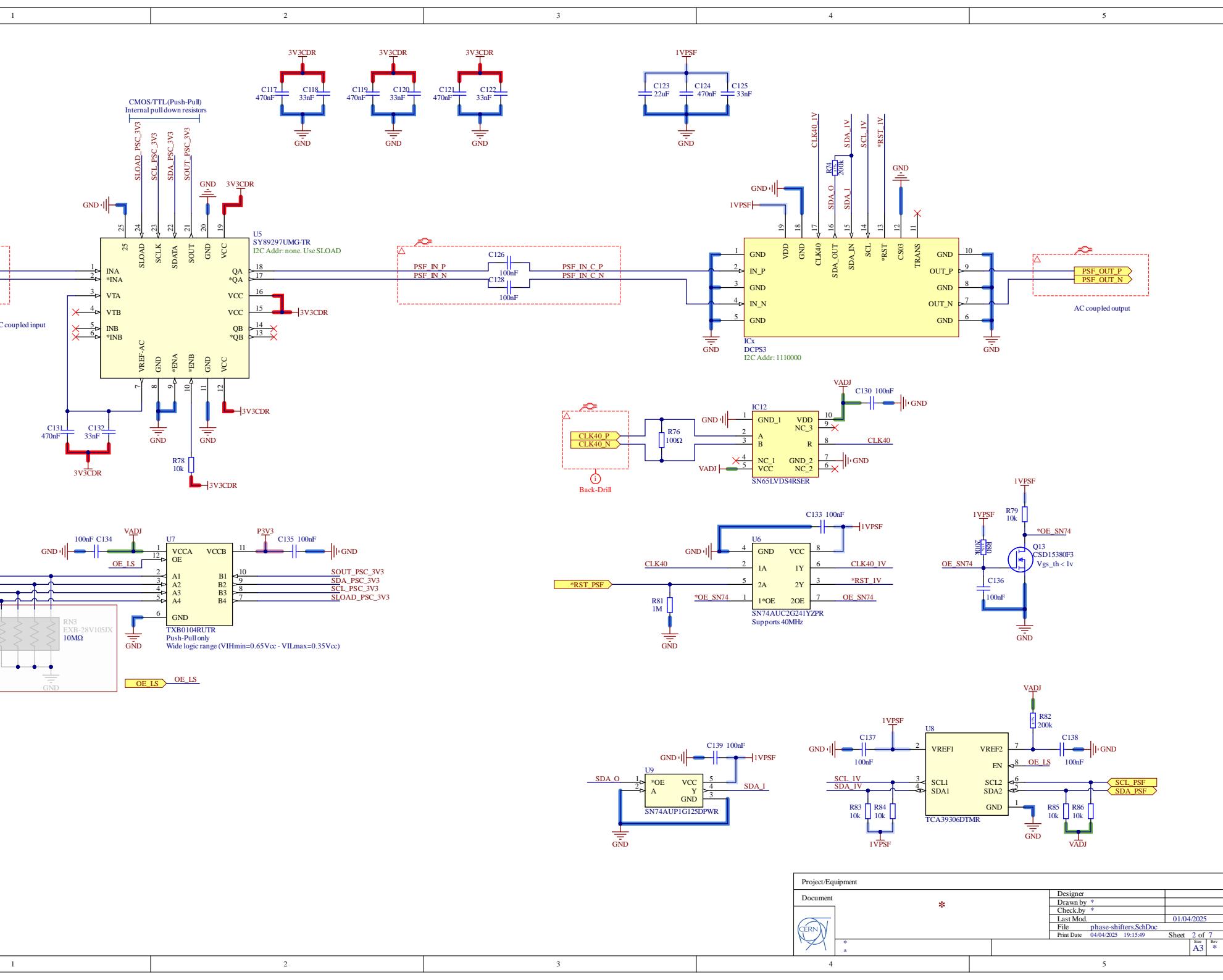
Project/Equipment

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Check by *	
Last Mod.	04/04/2025
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Sheet	1 of 7
Size	A3
Rev	*



A

B

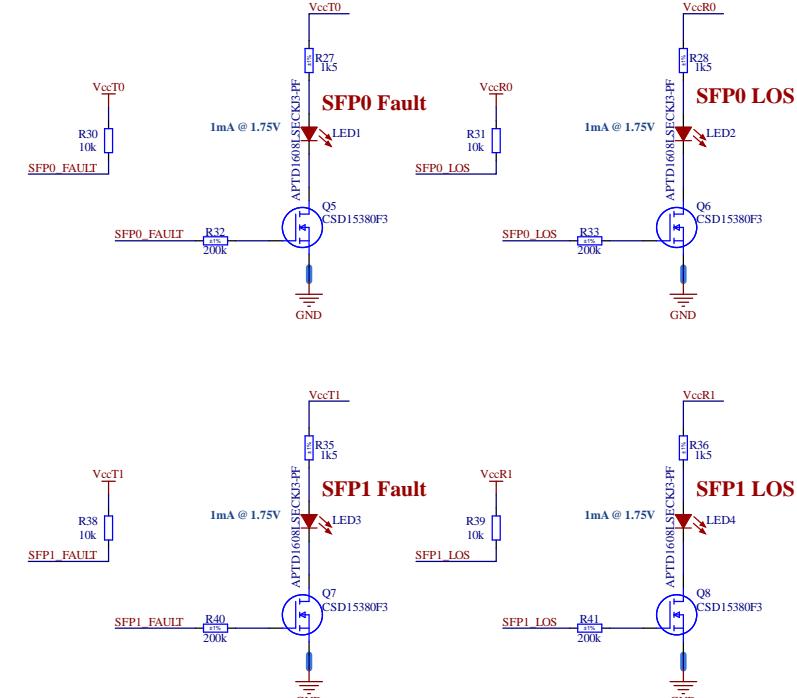
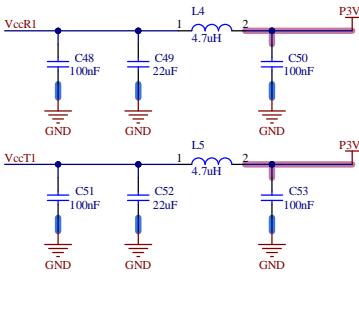
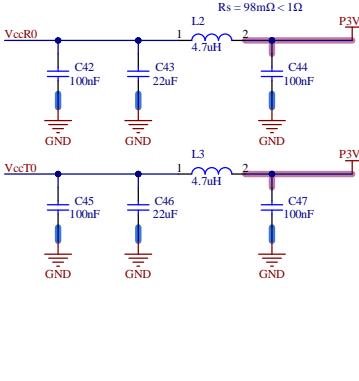
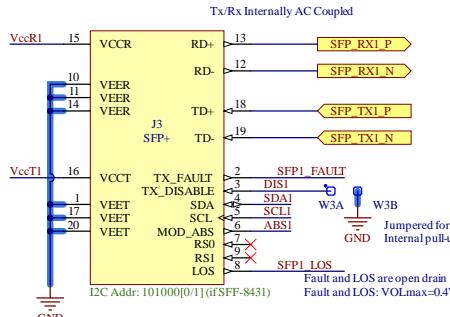
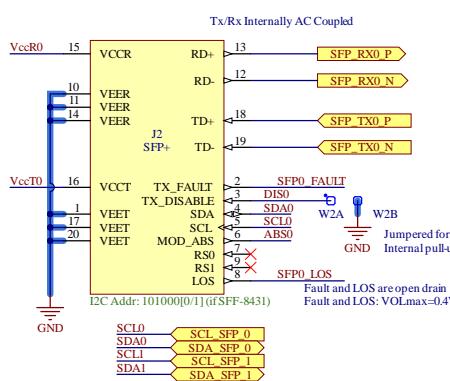
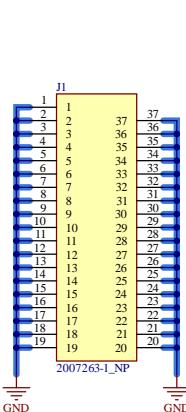
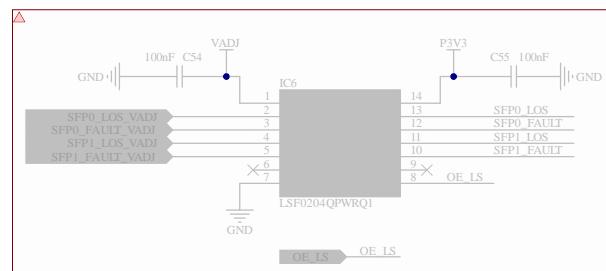
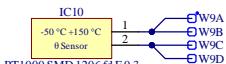
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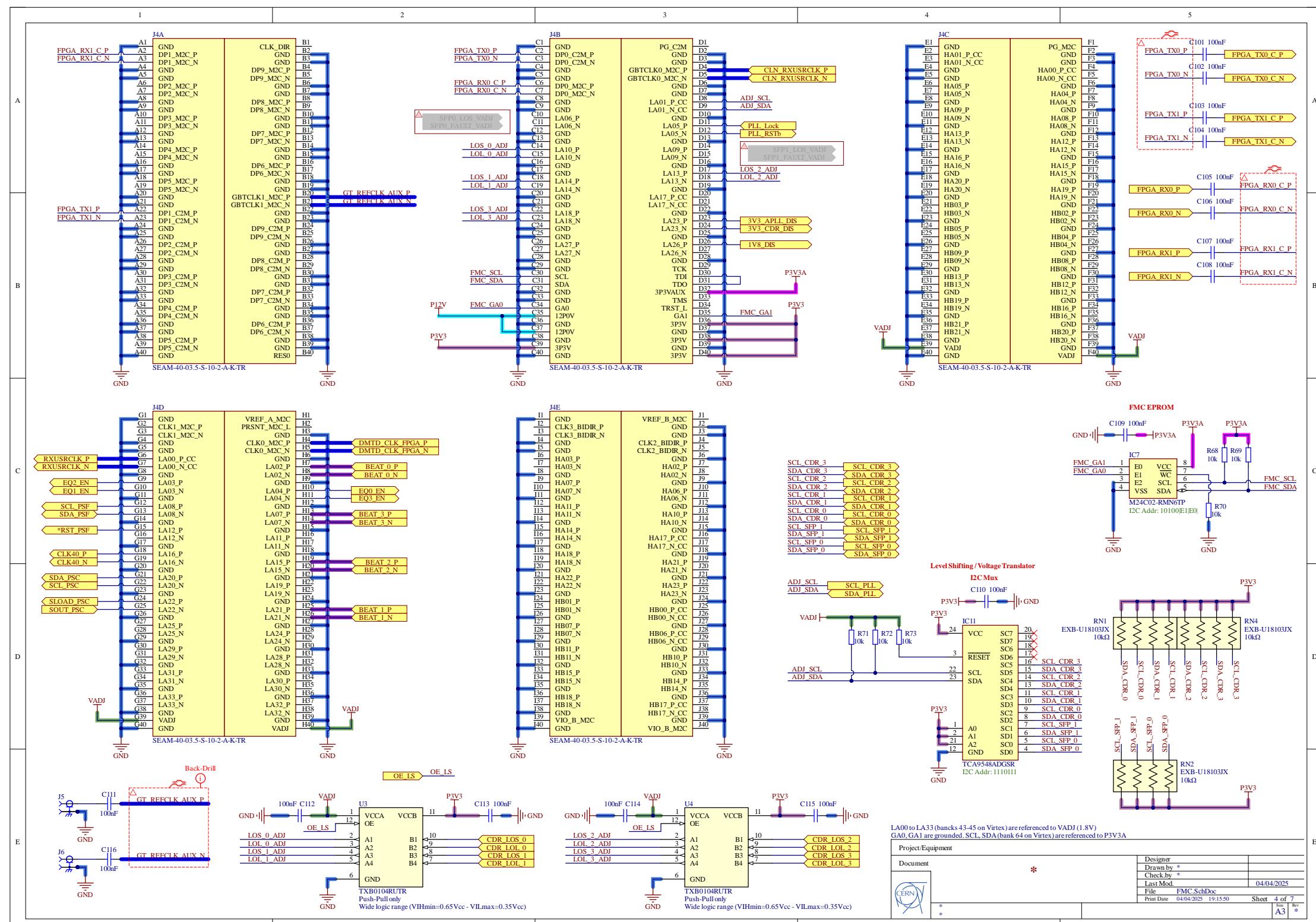
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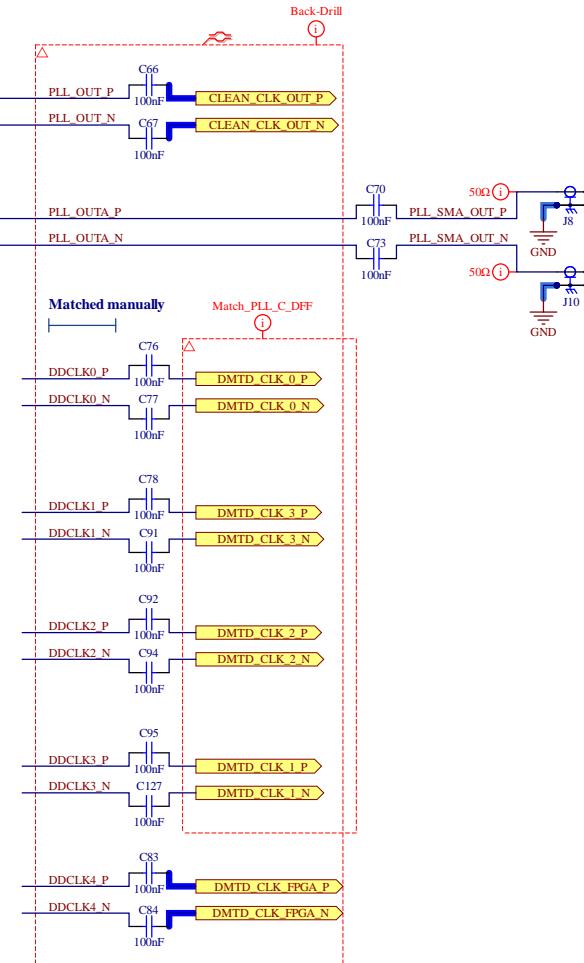
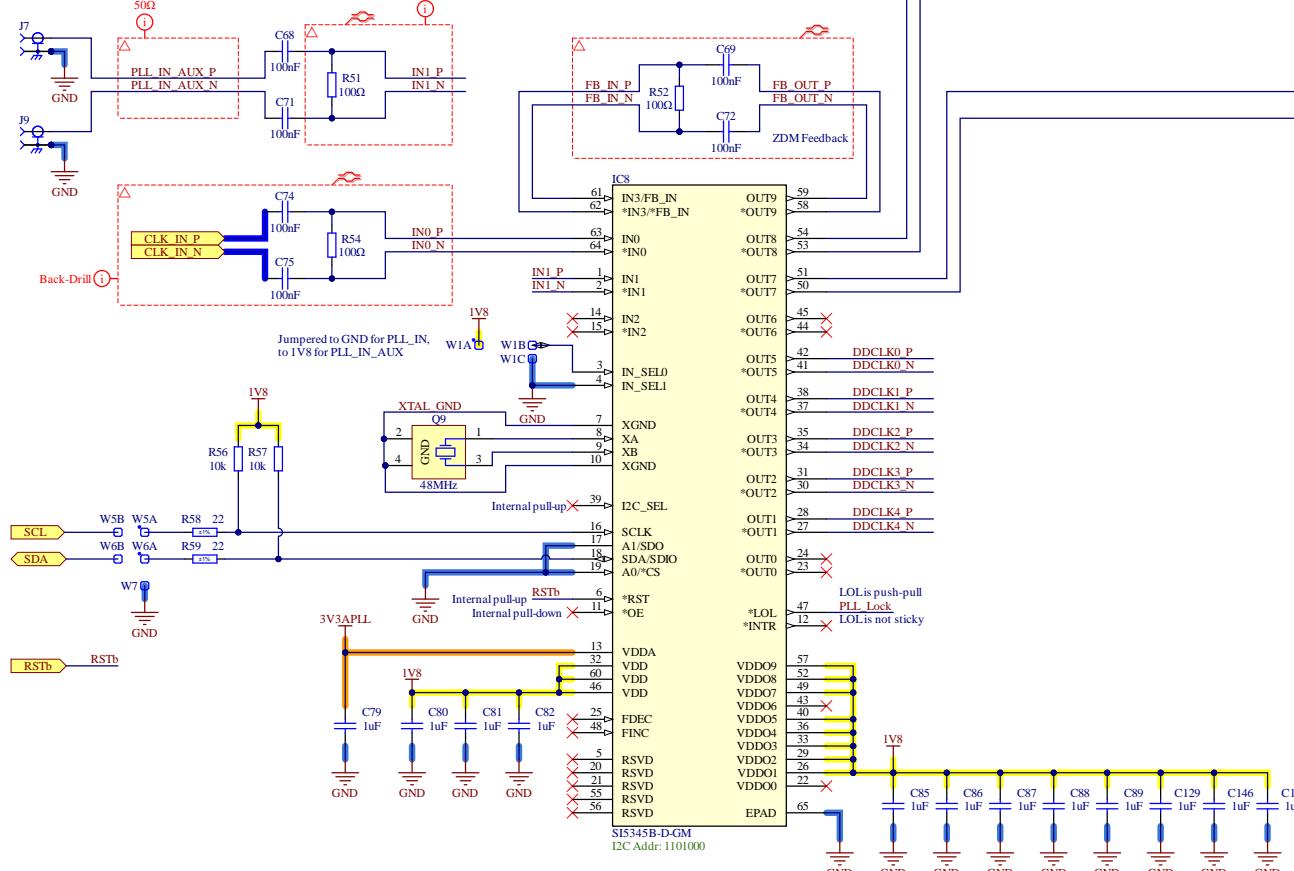
Temperature Sensor



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		Check by *	
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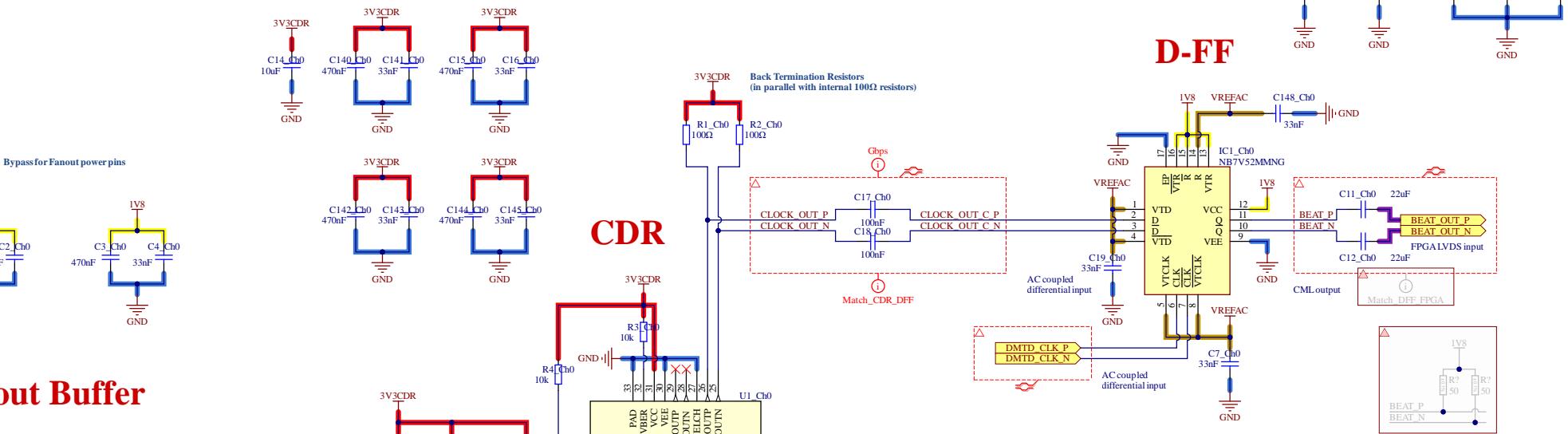


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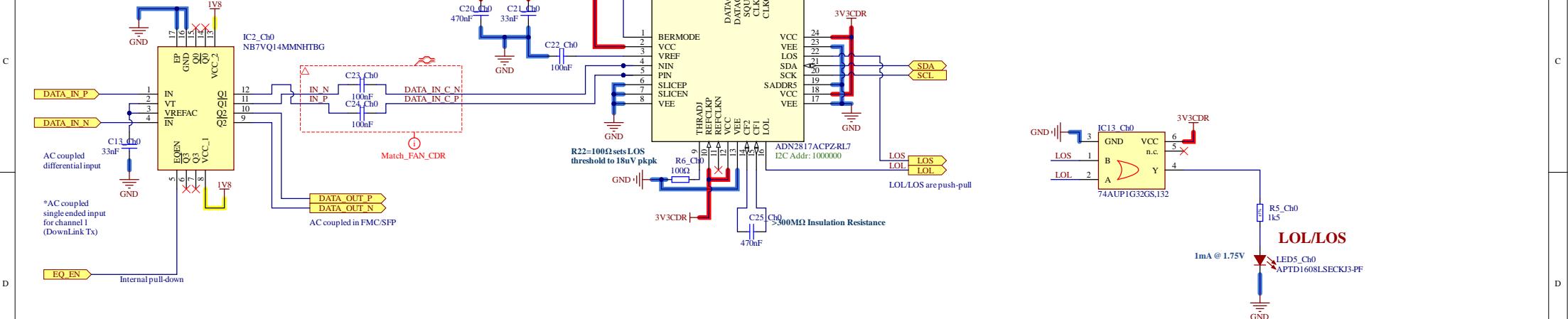


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Bypass for D-FF power pins



Fanout Buffer



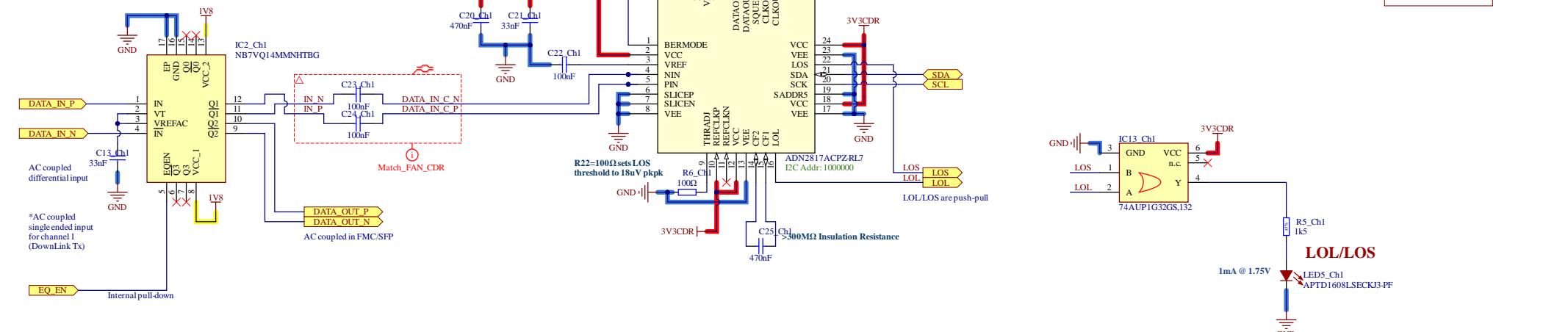
Project/Equipment Phase Stable Link

Document

**CDR, Fanout, D-FF**European Organization for Nuclear Research
CH-1211 Geneva 23 - Switzerland

Designer: E.Orzes	Drawn by: E.Orzes
Drawn by: E.Orzes	Check by: E.Orzes F.Martina S.Baron
Last Mod:	03/04/2025
File: Channel.SchDoc	Print Date: 04/04/2025 19:15:52
Sheet: 6 of 7	Page: A3 Rev: 1.0

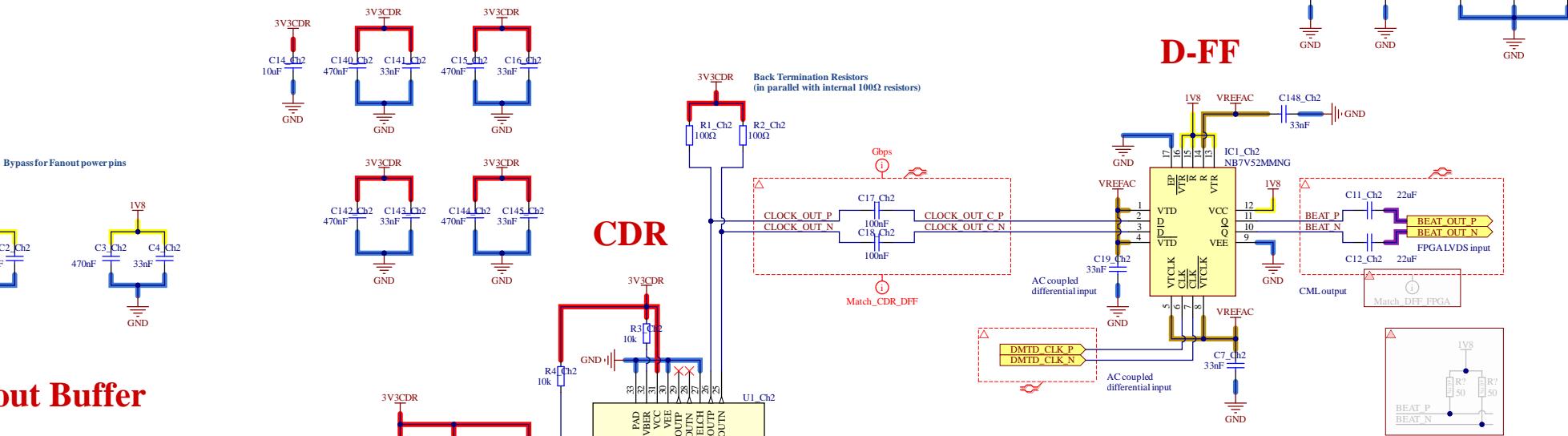
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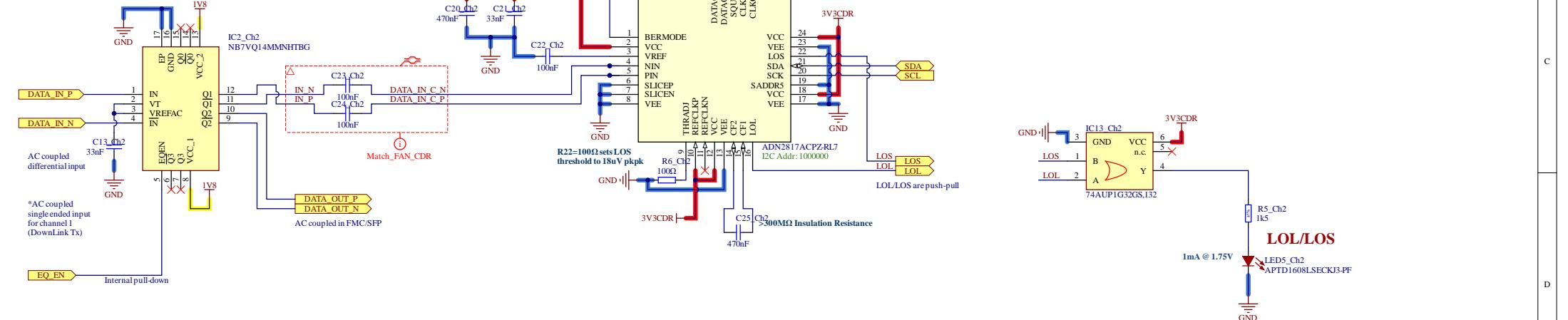
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European Organization for Nuclear Research CERN CH-1211 Genève 23 - Switzerland		Spec Rev	A3 1.0

CDR, Fanout, D-FF

Bypass for D-FF power pins



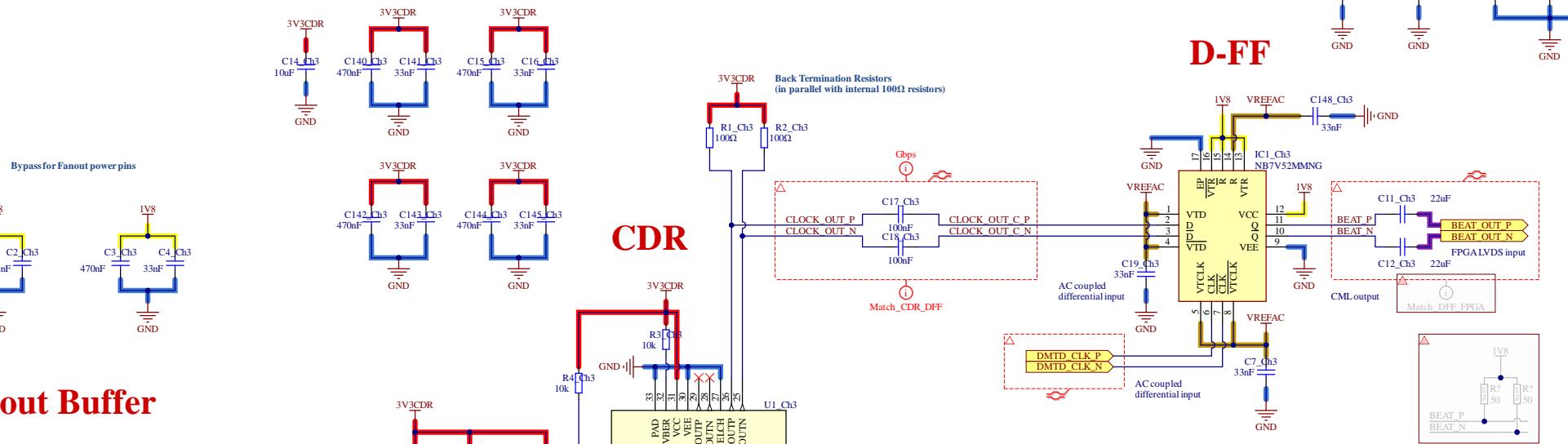
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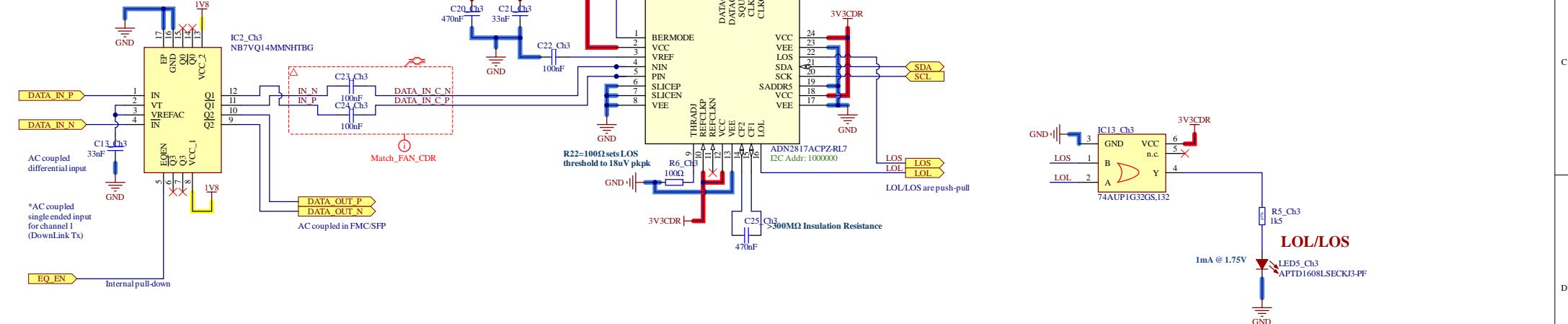
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	European Organization for Nuclear Research CH-1211 Genève 23 - Switzerland	Rev A3 1.0

CDR, Fanout, D-FF

Bypass for D-FF power pins



Fanout Buffer



Project/Equipment Phase Stable Link

Document



Designer: E.Orzes	Drawn by: E.Orzes
Drawn by: E.Orzes	Check by: E.Orzes F.Martina S.Baron
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