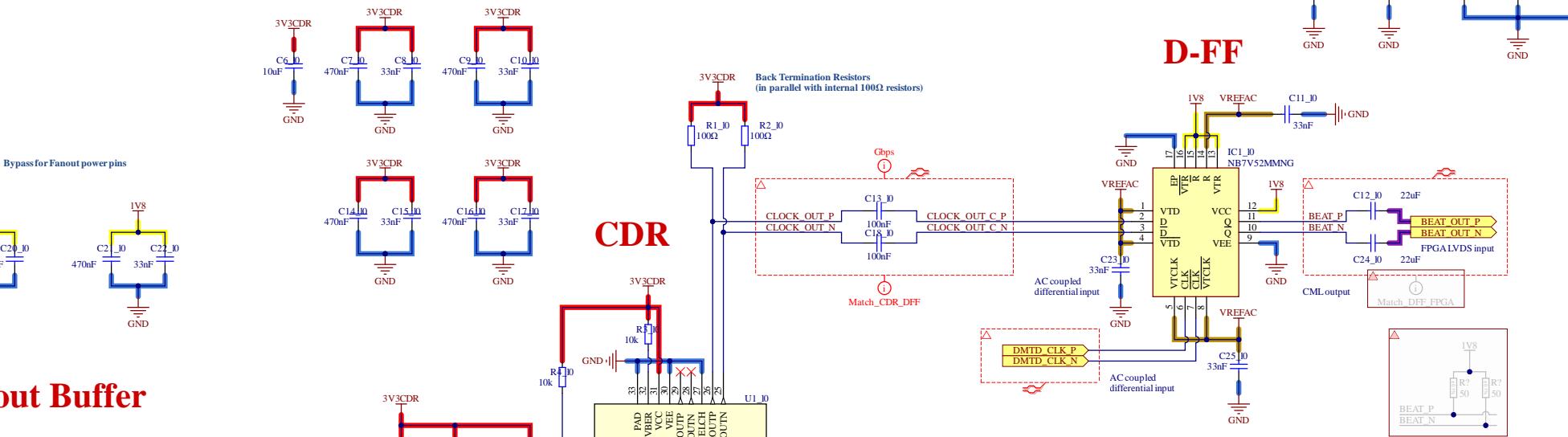
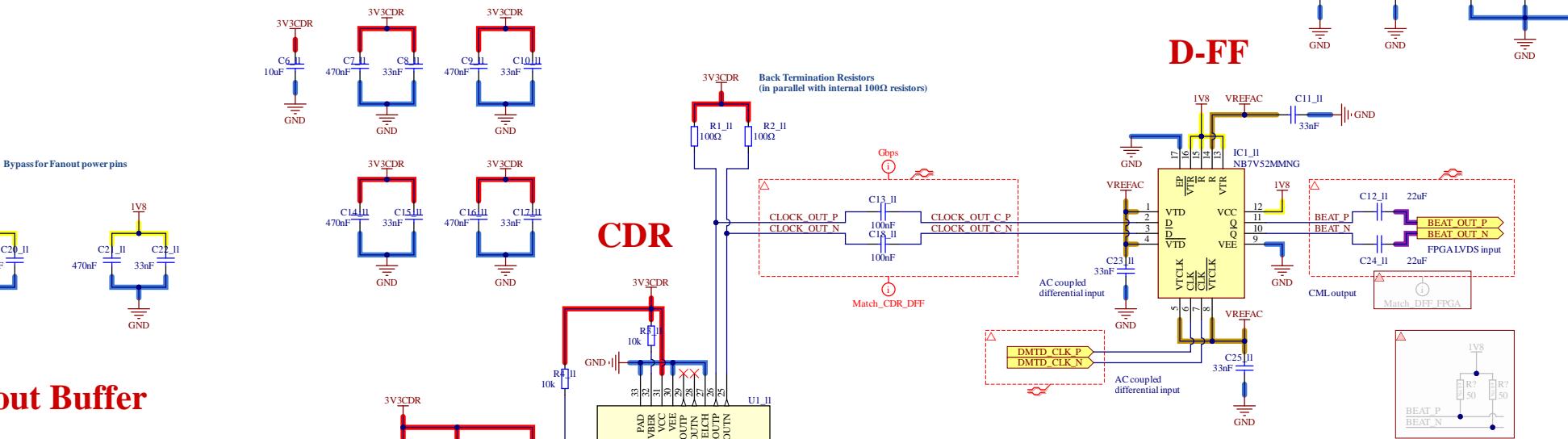


Bypass for D-FF power pins

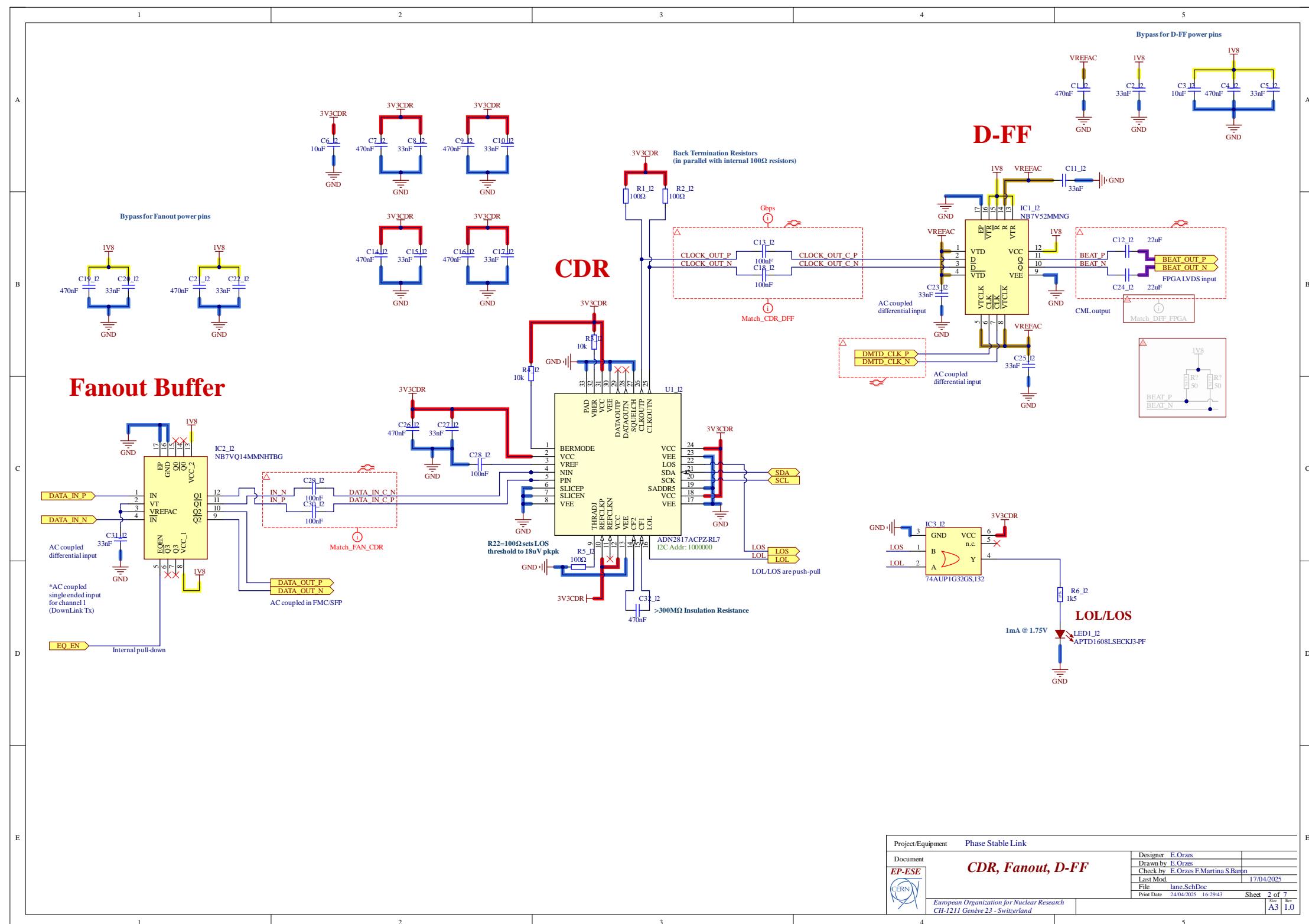


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CDR, Fanout, D-FF		European Organization for Nuclear Research CH-1211 Genève 23 - Switzerland	

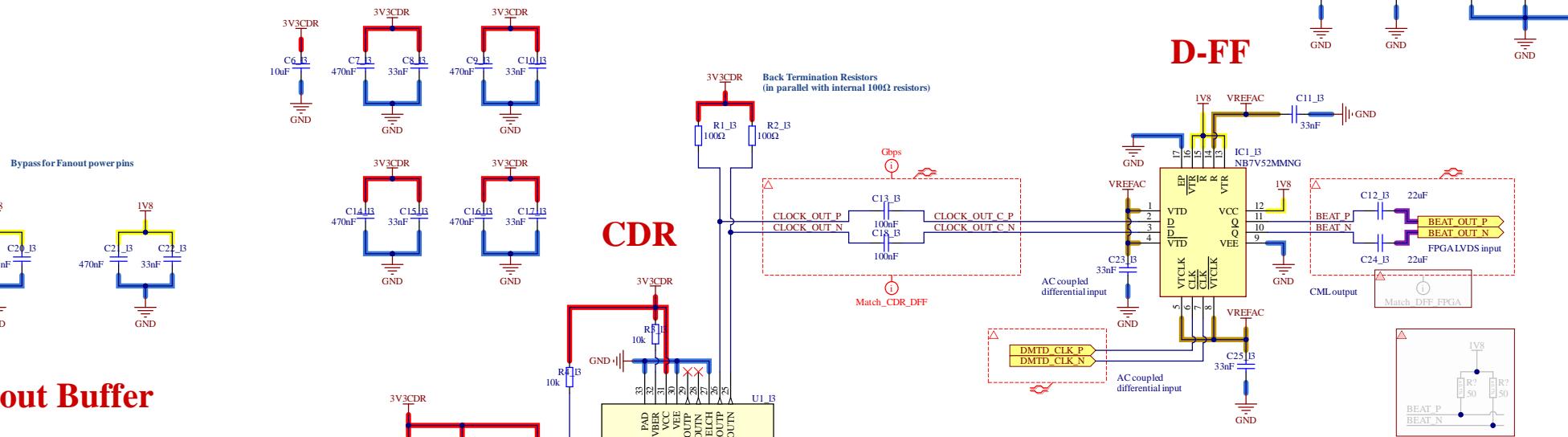
Bypass for D-FF power pins

**CDR****D-FF****Fanout Buffer****CDR, Fanout, D-FF**

Project/Equipment	Phase Stable Link
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EP-ESE	
CERN	
Designer: E.Orzes	
Drawn by: E.Orzes	
Check by: E.Orzes F.Martina S.Baron	
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Bypass for D-FF power pins



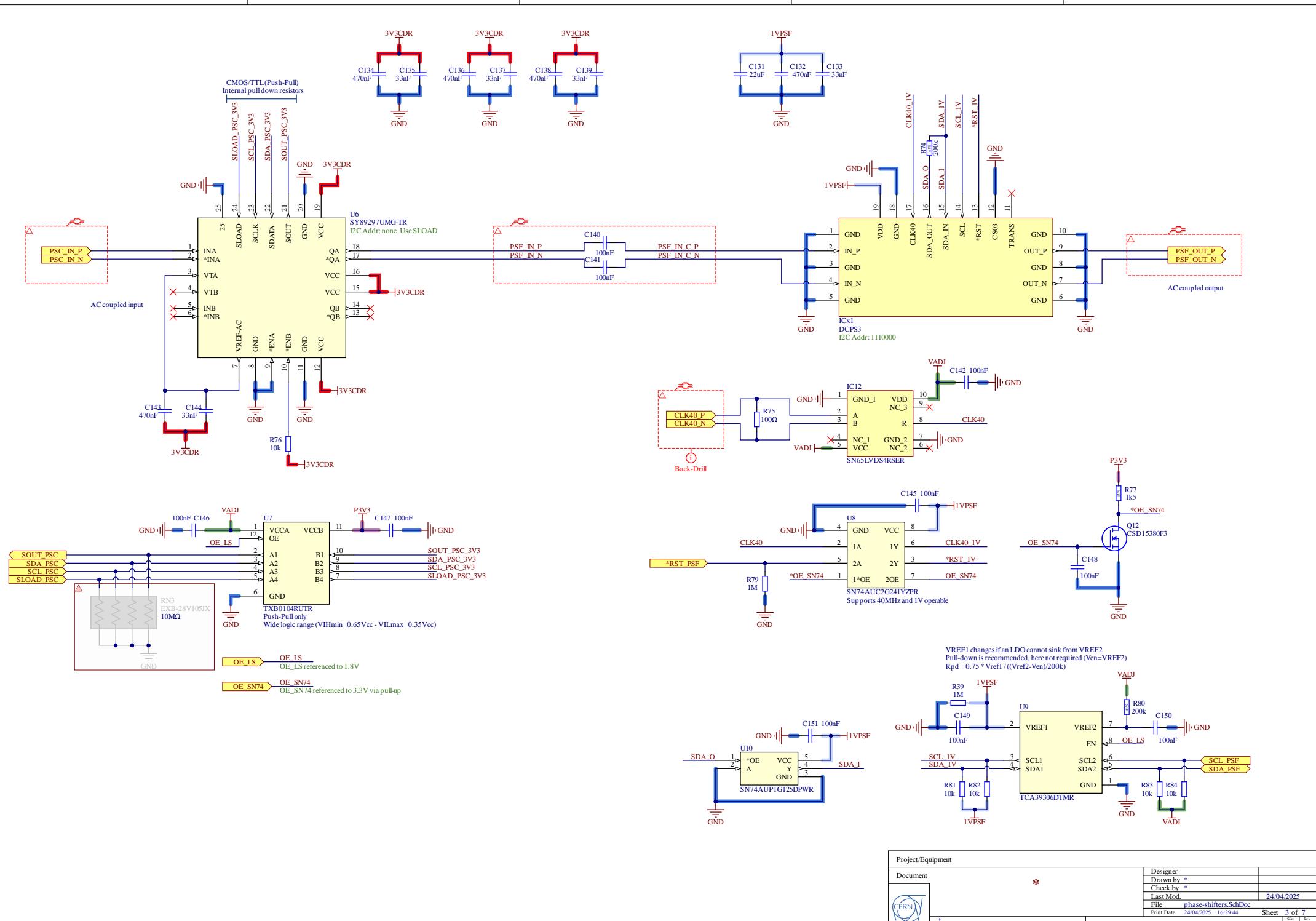
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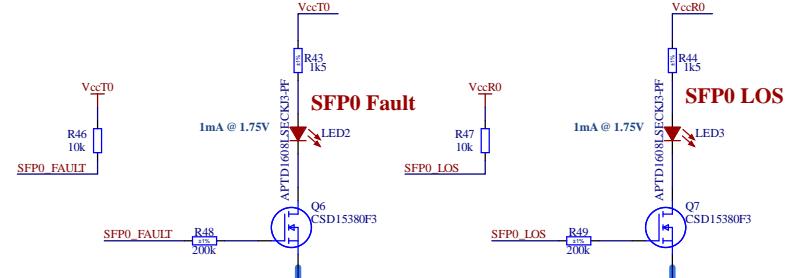
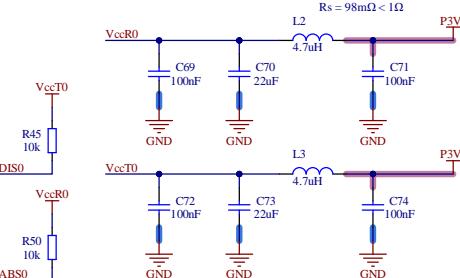
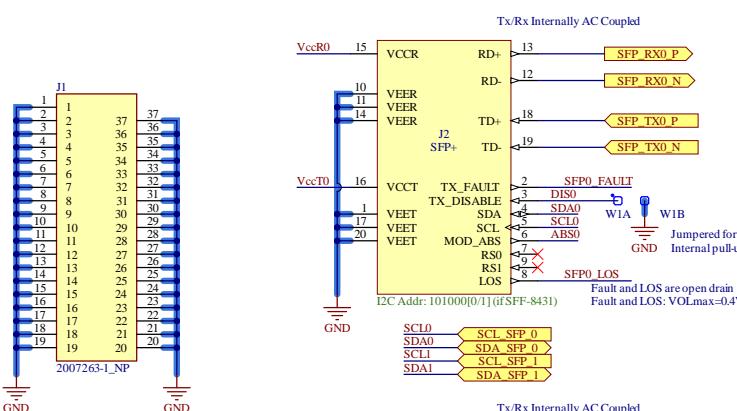


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Drawn by: E.Orzes	
Check by: E.Orzes F.Martina S.Baron	
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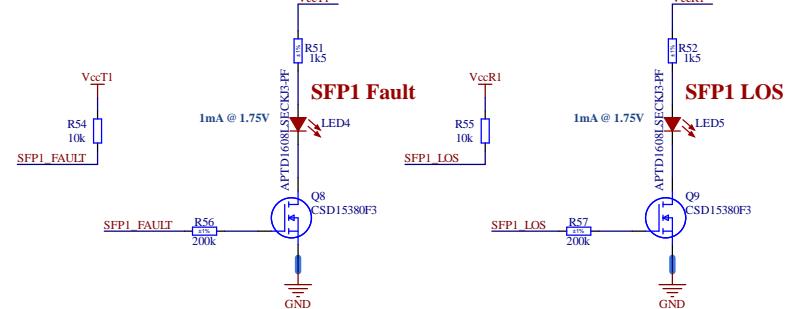
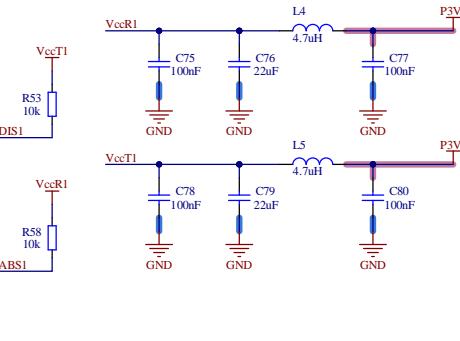
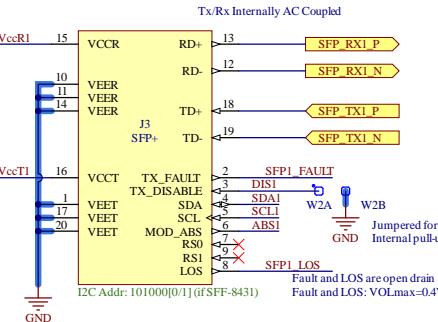


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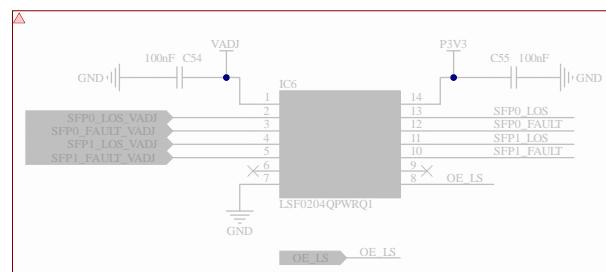
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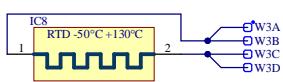


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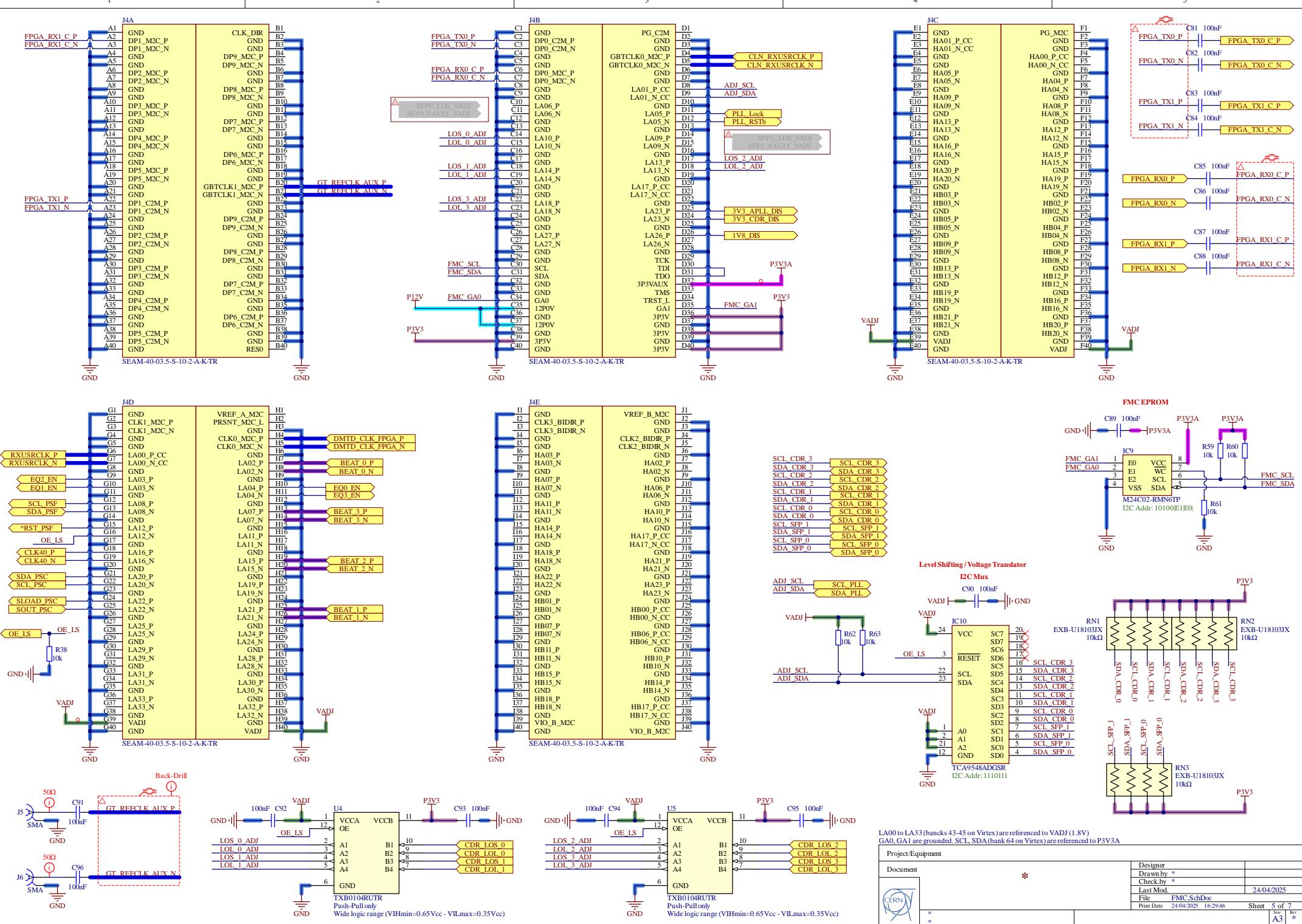
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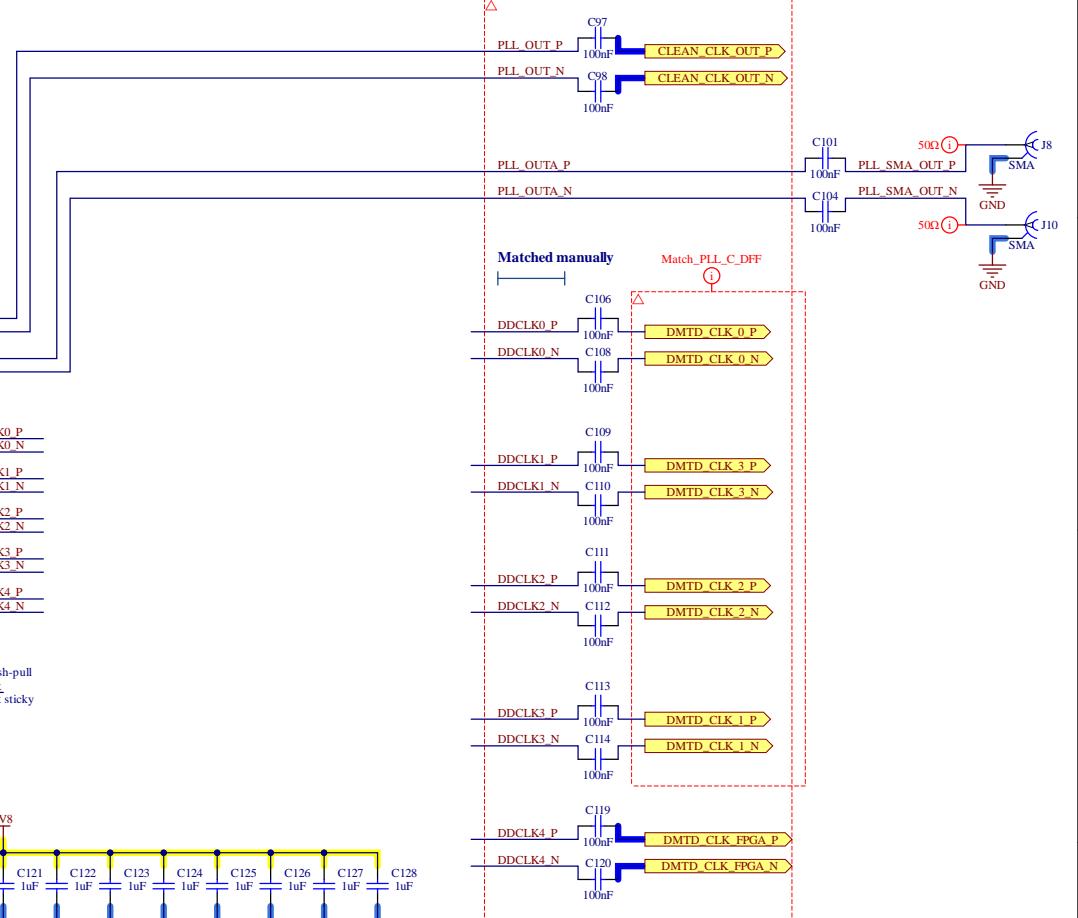
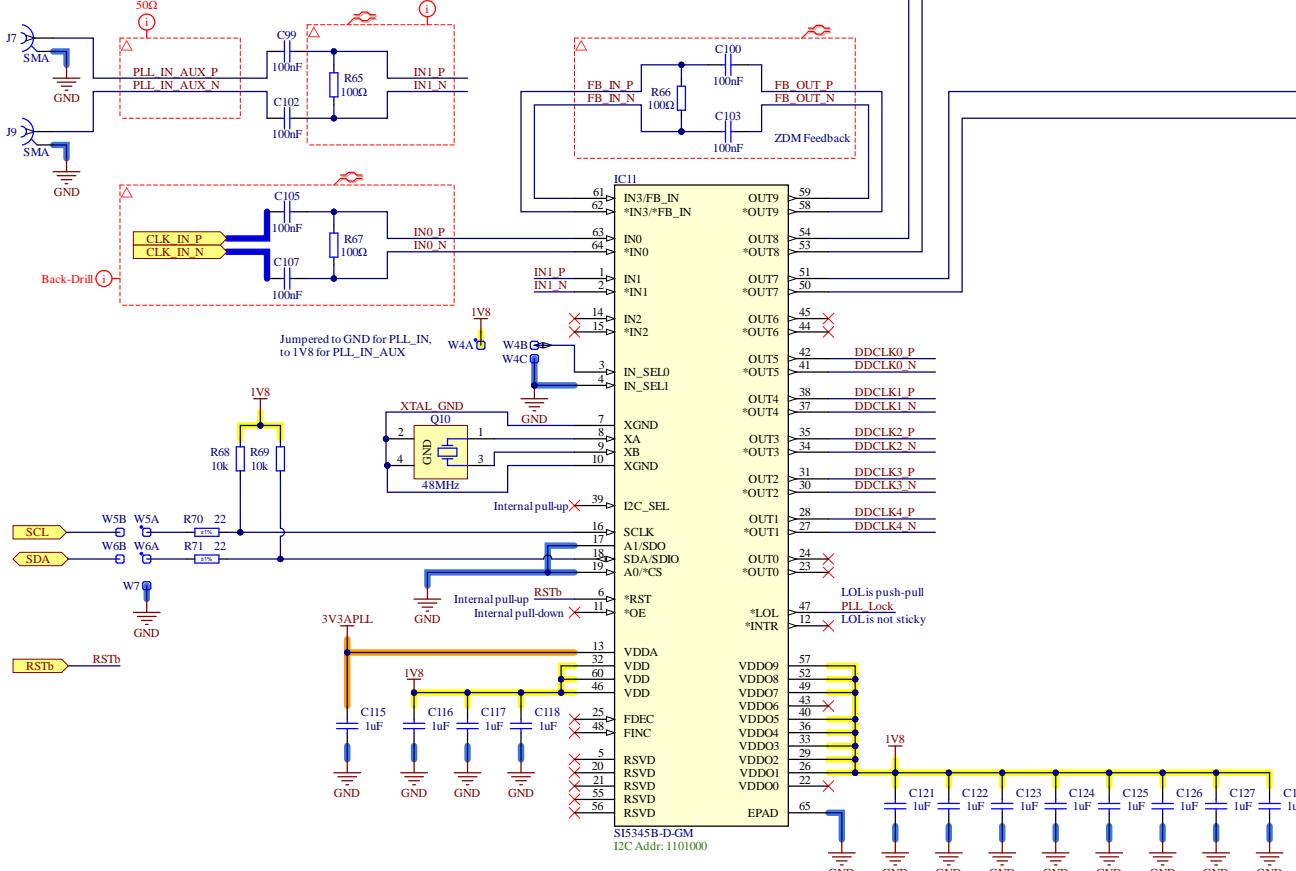
## Temperature Sensor



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## Jitter Cleaner PLL



Project/Equipment		Designer	
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