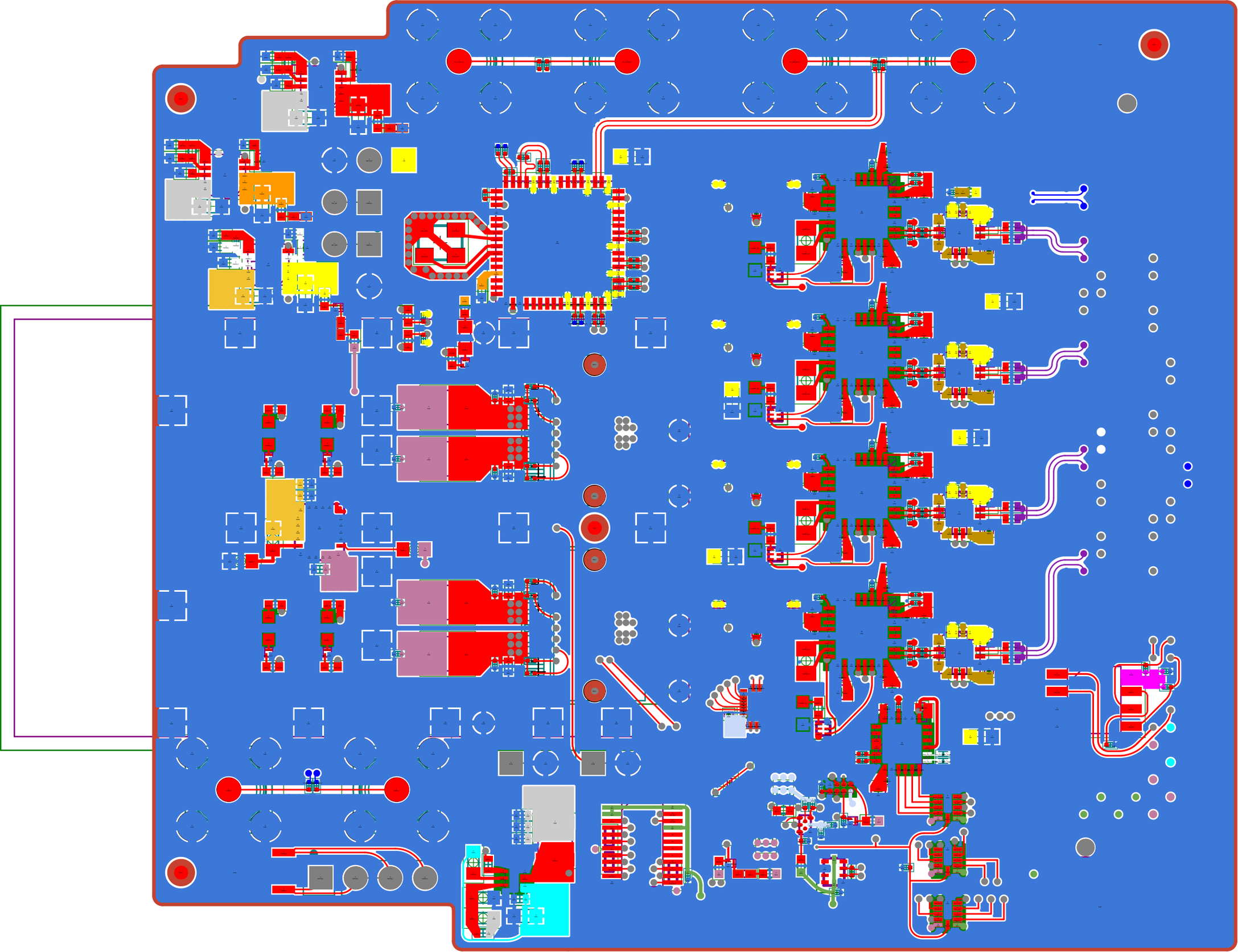
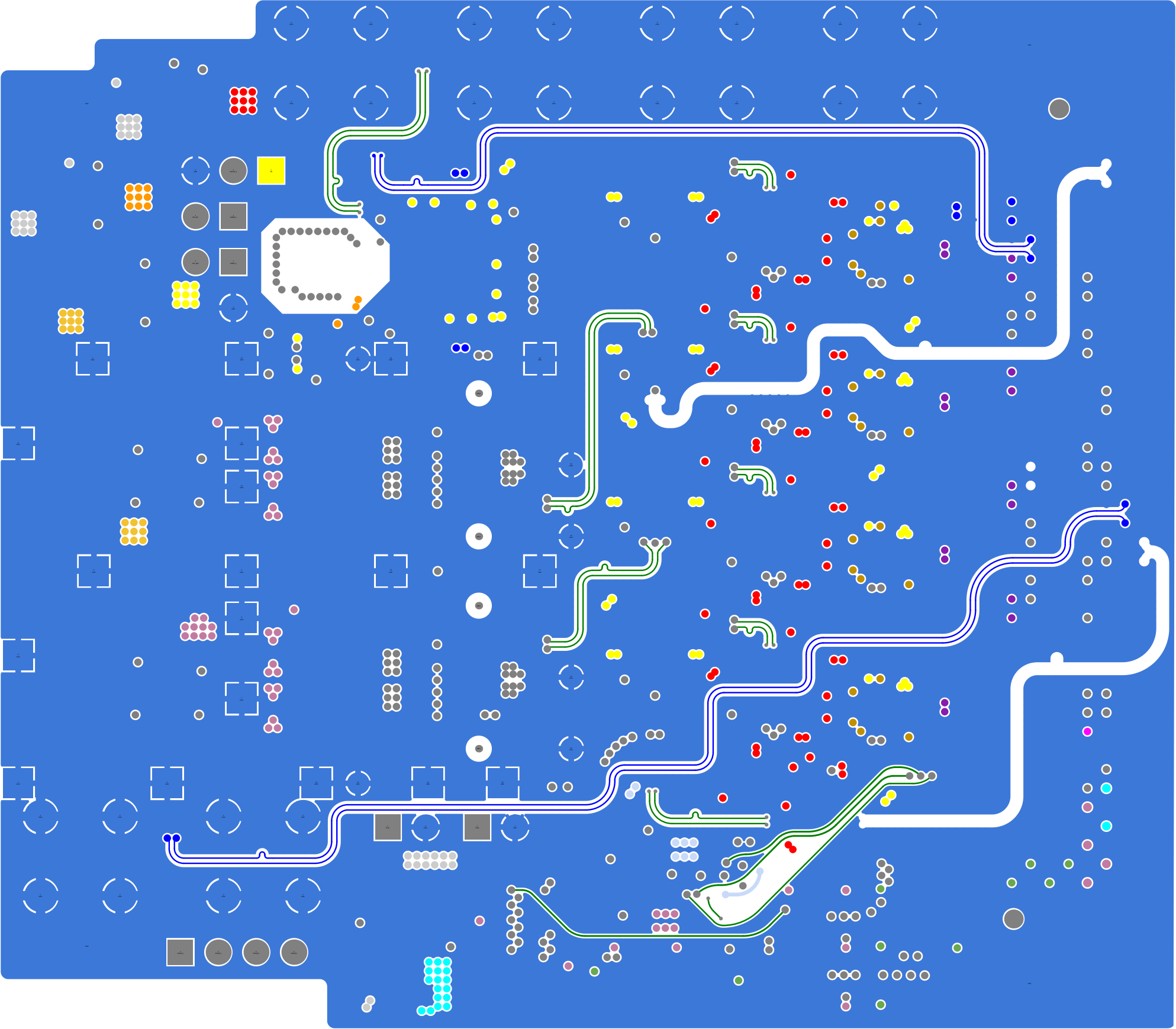
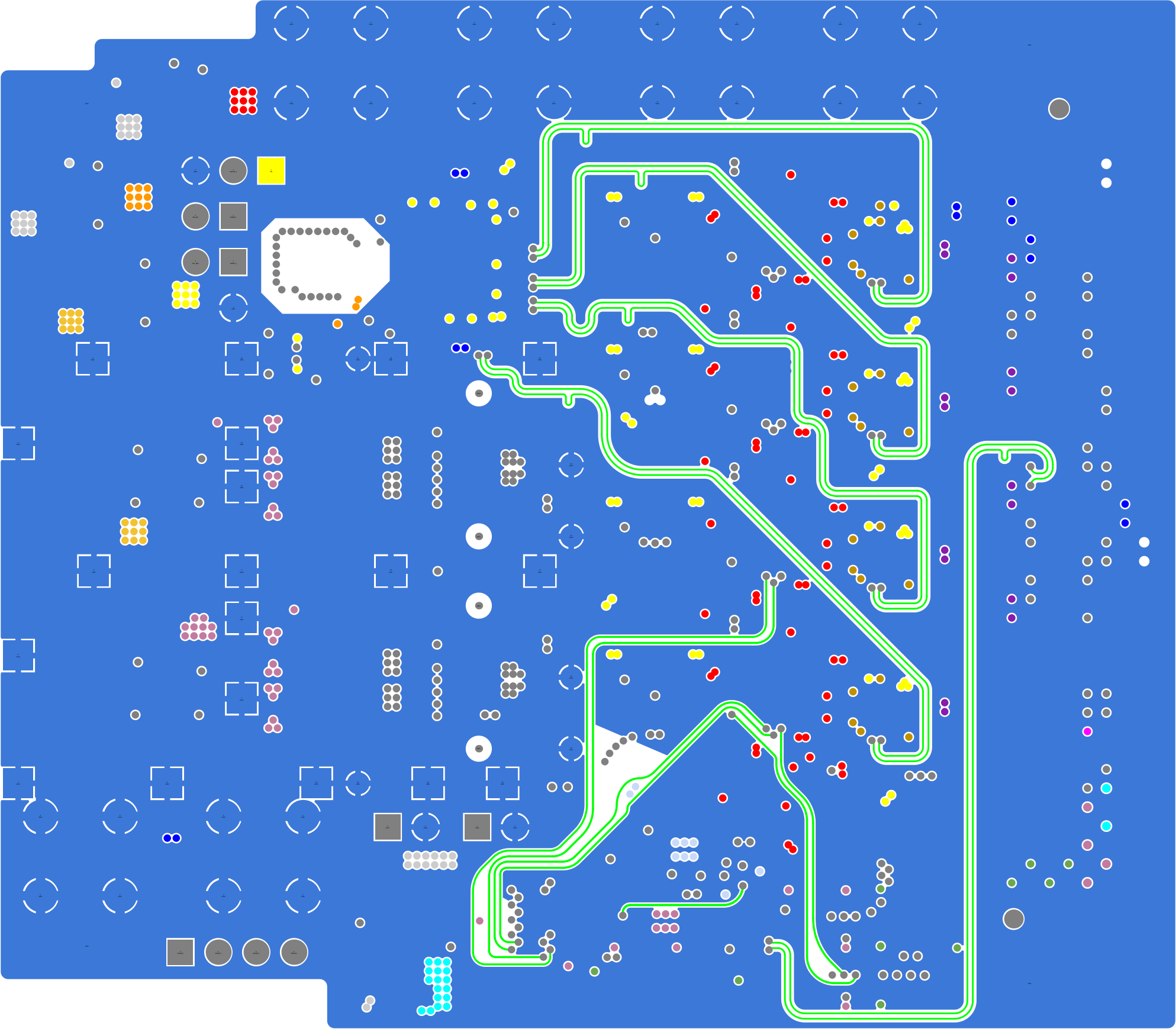
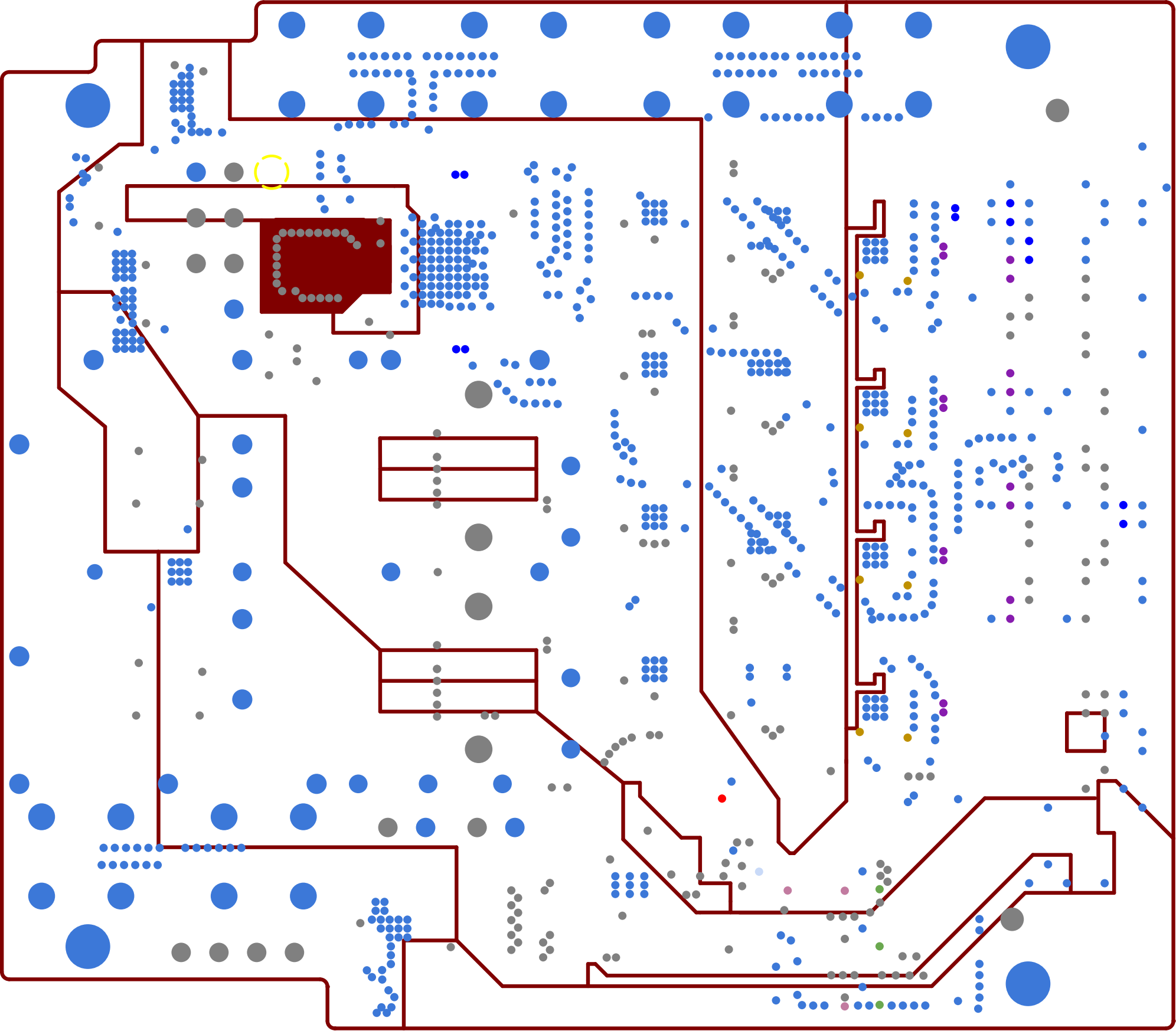
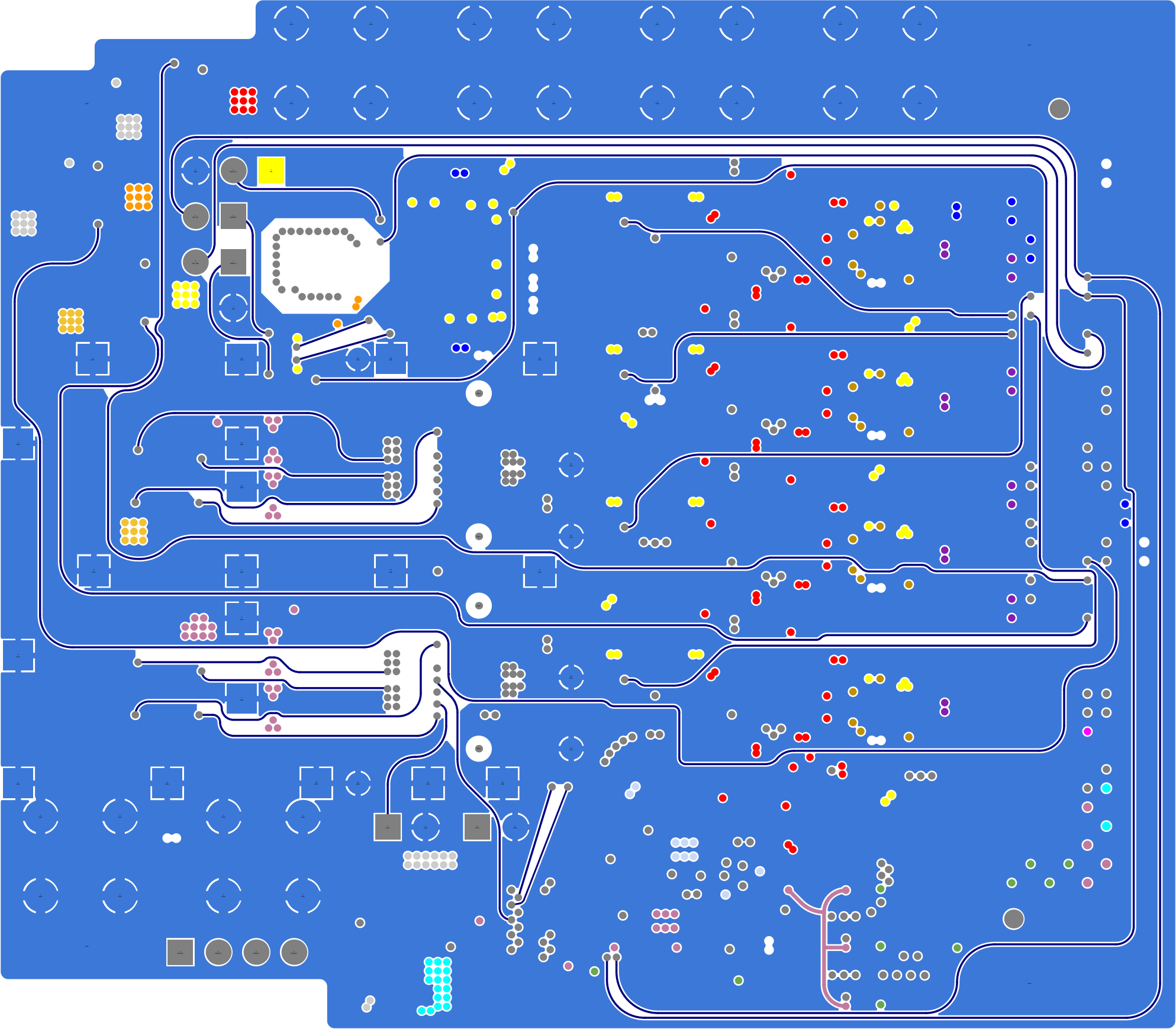
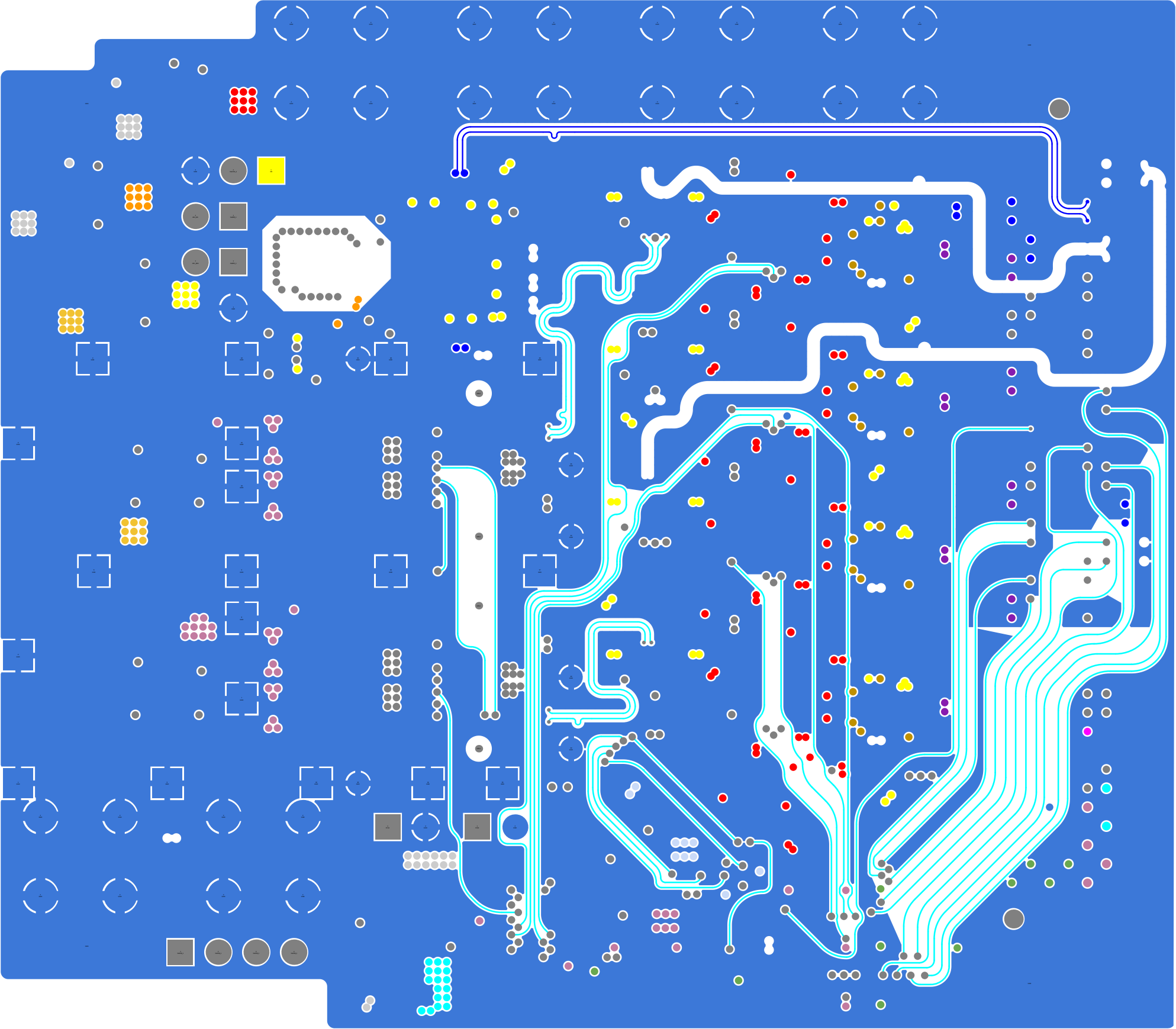


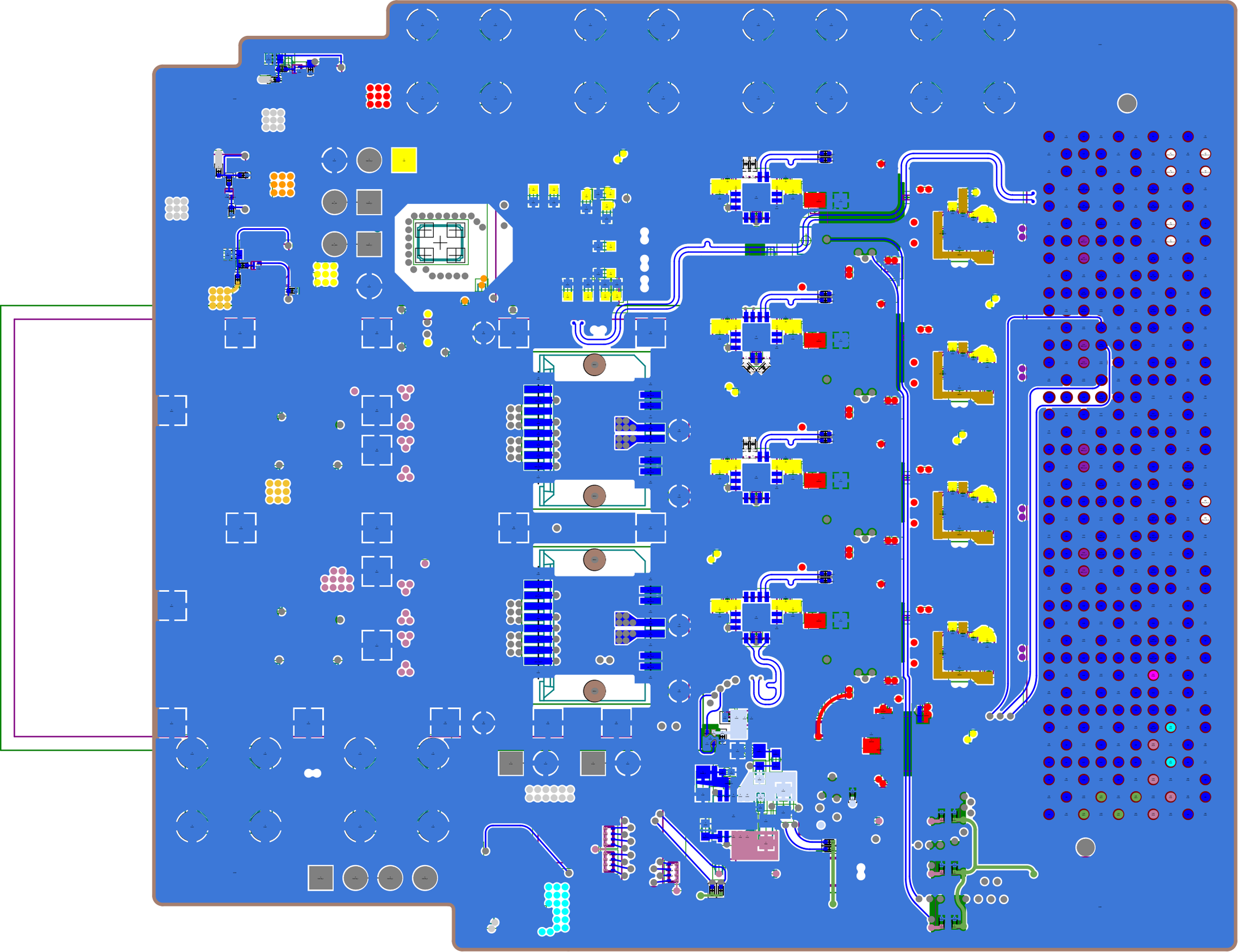


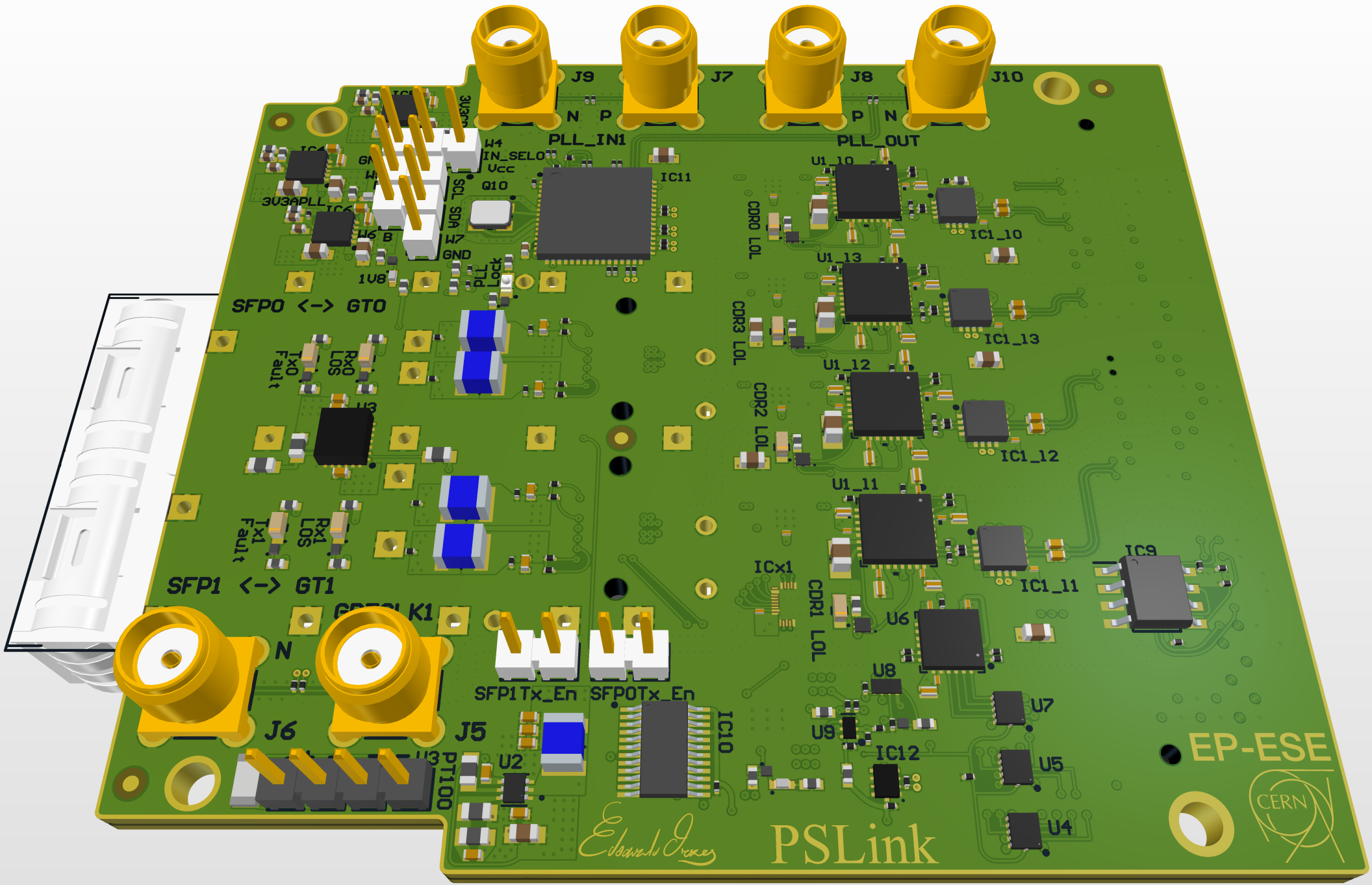


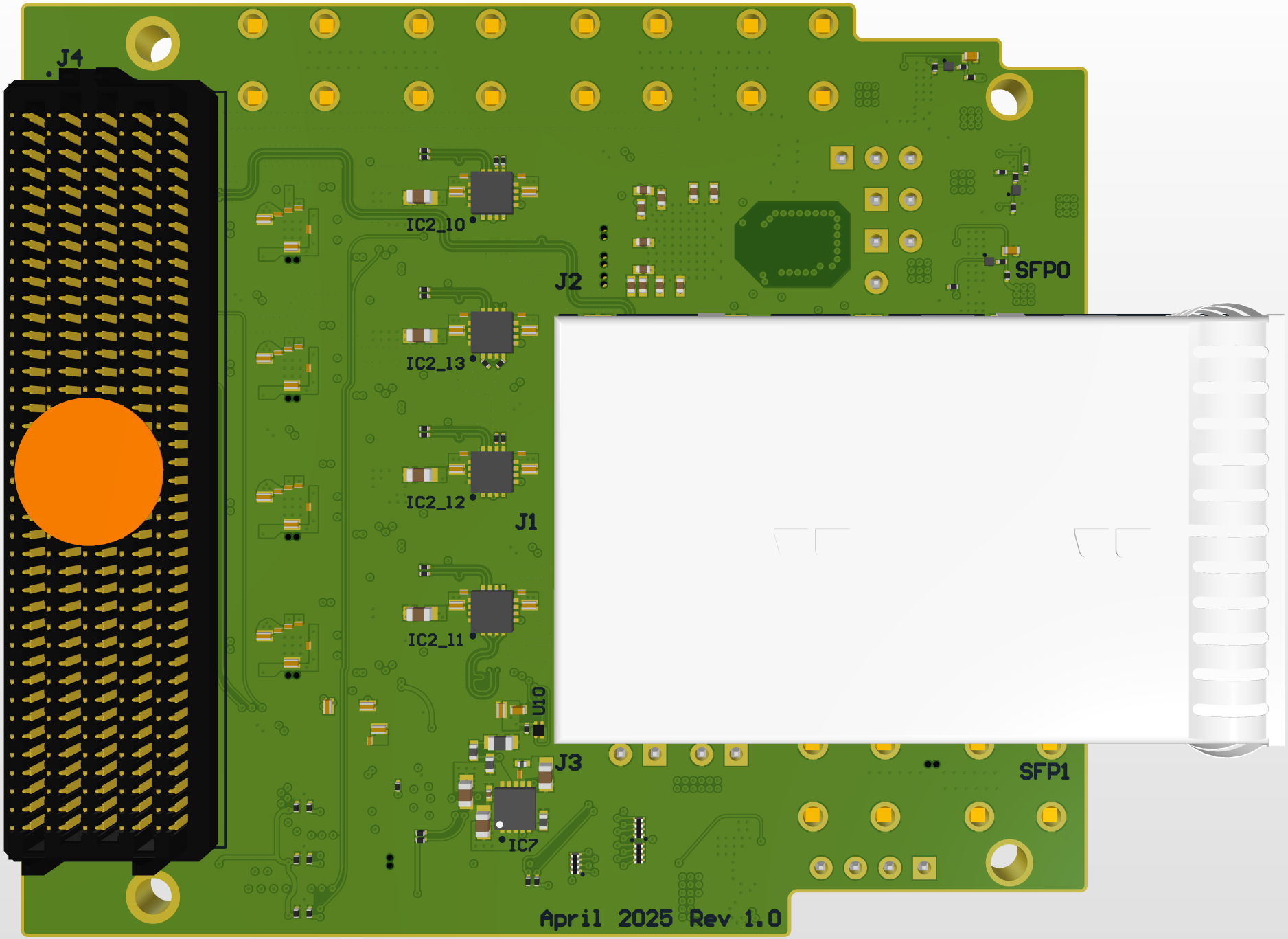









J9
J7
J8
J10
N P
P N
PLL_IN1
PLL_OUT
W4
IN_SEL0
Vcc
Q10
IC11
SCL
SDA
W7
GND
W6
B
3V3APLL
1V8
PLL Lock
SFP0 <-> GT0
Tx0 Fault
Rx0 LOS
U3
Tx1 Fault
Rx1 LOS
SFP1 <-> GT1
GBTCLK1
N
J6
J5
PT100
U2
SFP1Tx_En
SFP0Tx_En
IC10
ICx1
CDR0 LOL
CDR3 LOL
CDR2 LOL
CDR1 LOL
U1_10
IC1_10
U1_13
IC1_13
U1_12
IC1_12
U1_11
IC1_11
IC9
U6
U8
U9
IC12
U7
U5
U4
EP-ESE
CERN
PSLink


J4
IC2_10
J2
SFP0
IC2_13
IC2_12
J1
IC2_11
U10
J3
IC7
SFP1
April 2025 Rev 1.0