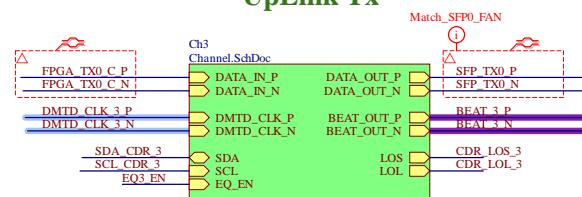


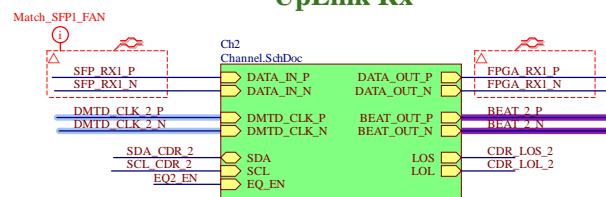
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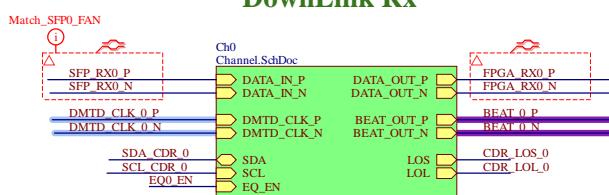
UpLink Tx



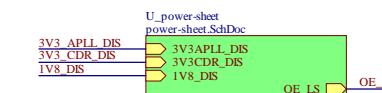
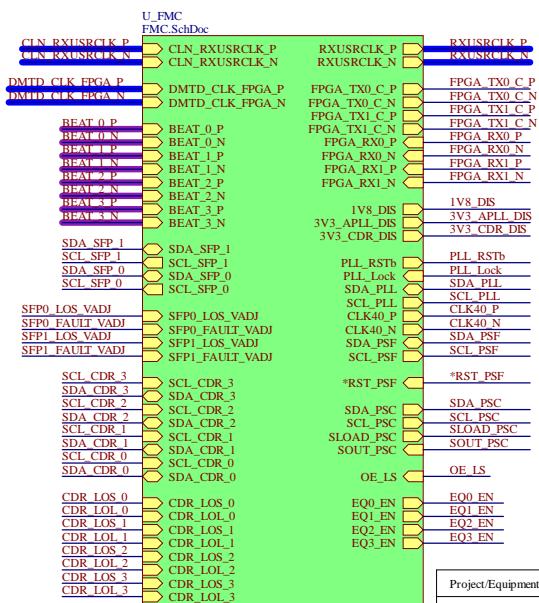
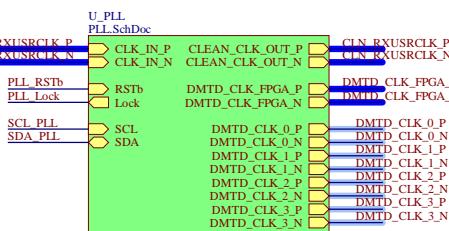
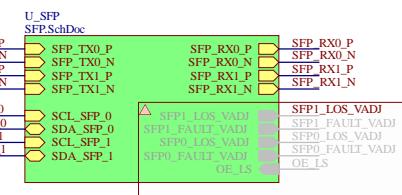
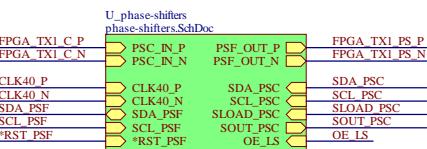
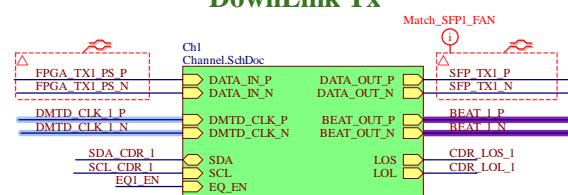
UpLink Rx



DownLink Rx



DownLink Tx



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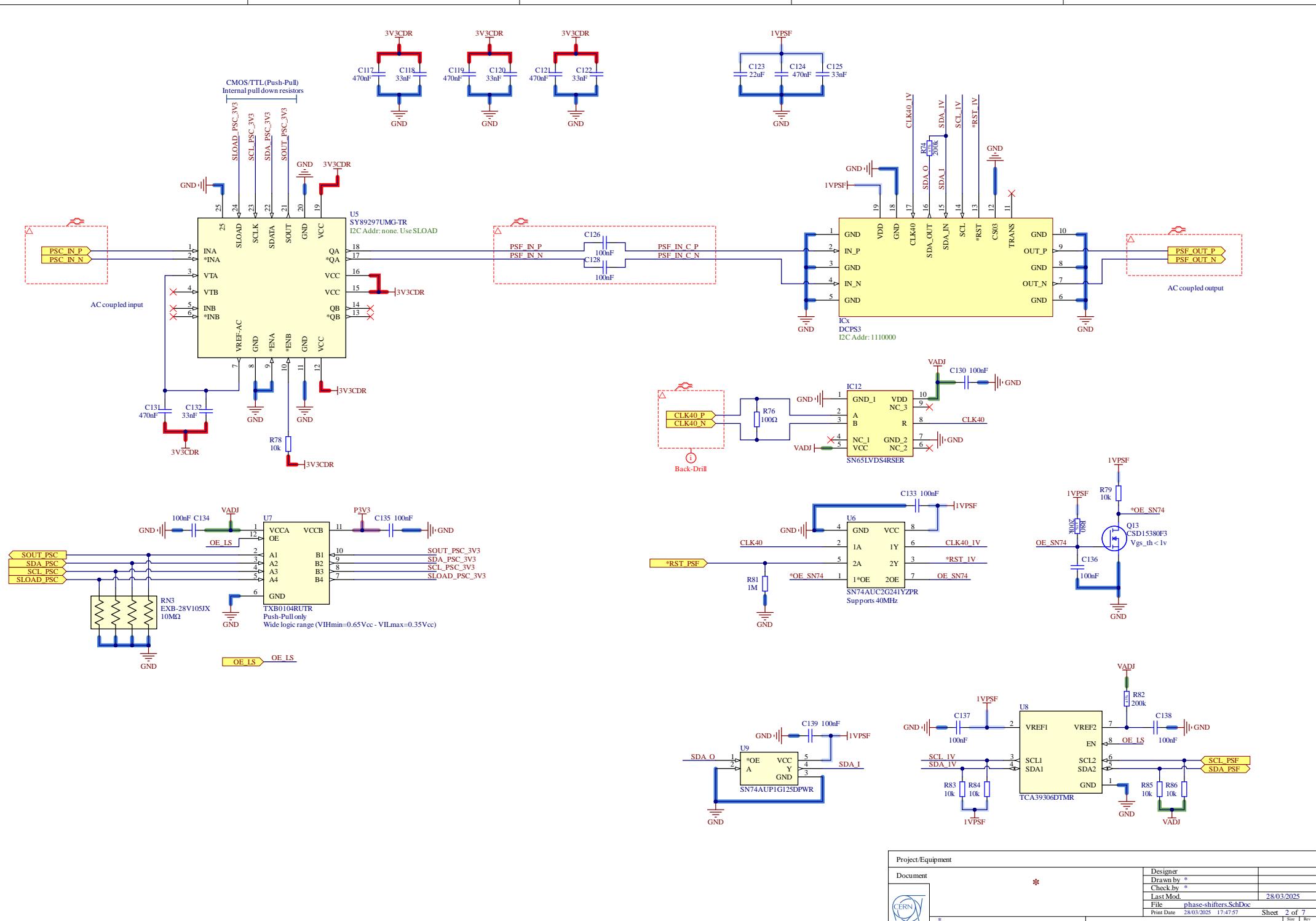
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B

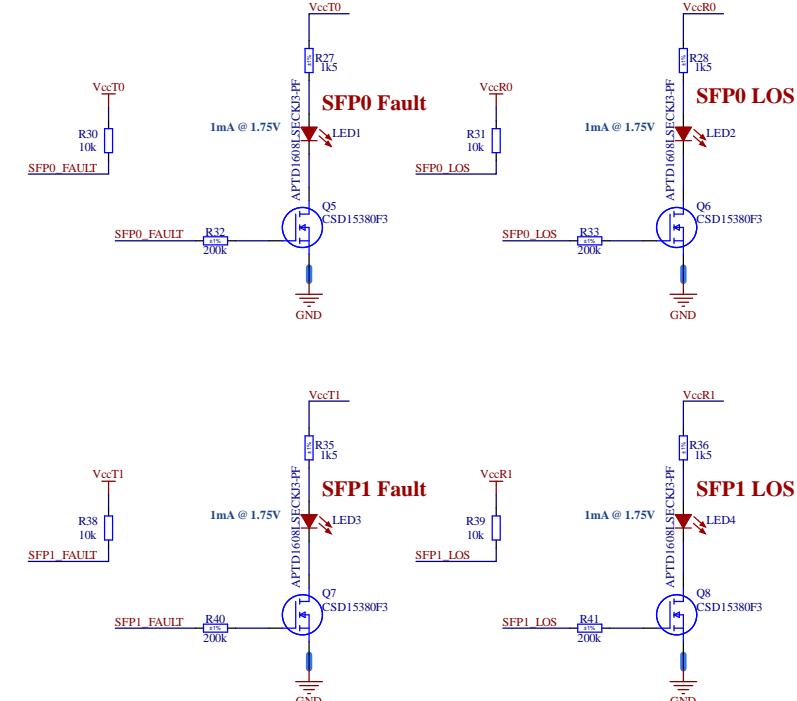
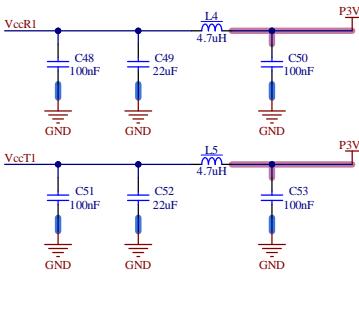
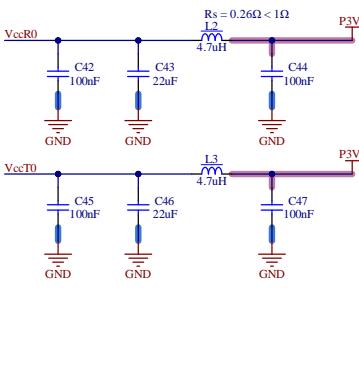
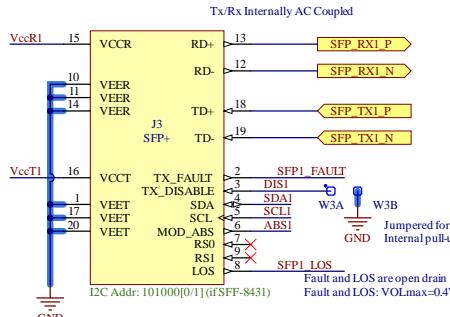
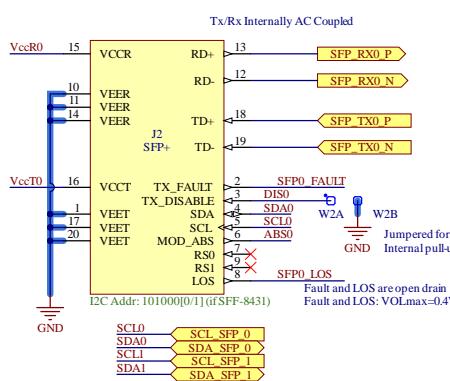
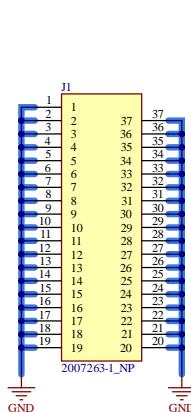
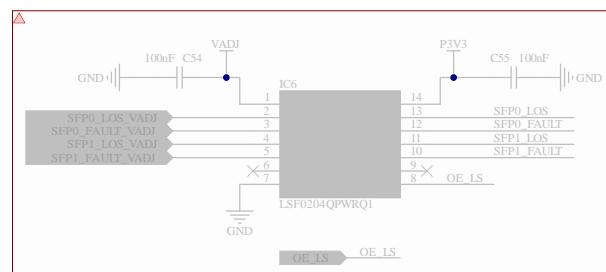
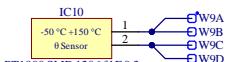
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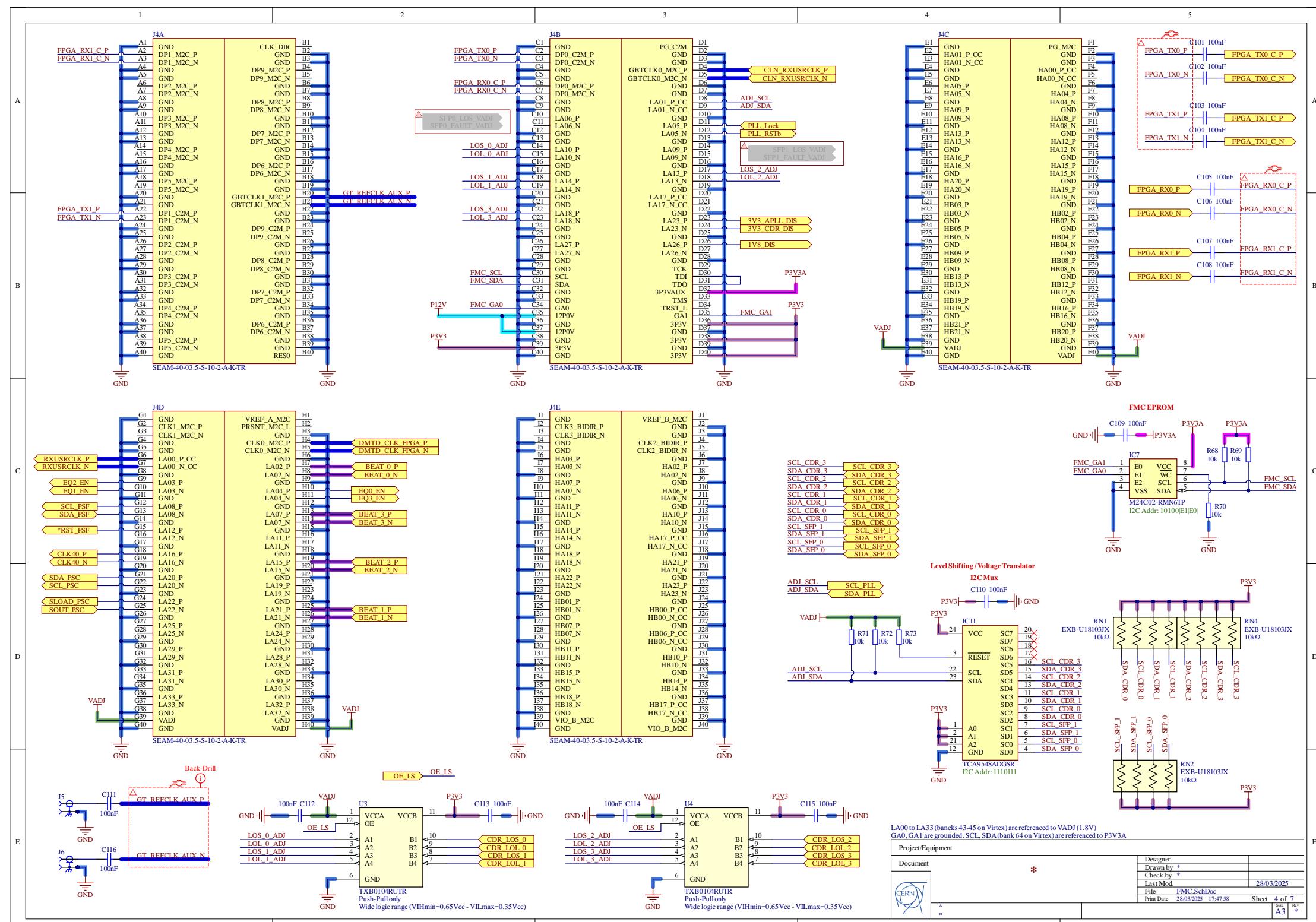
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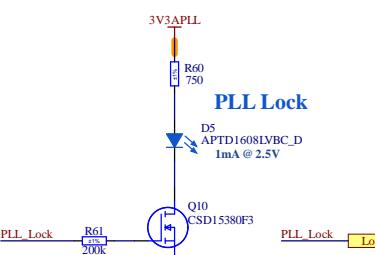
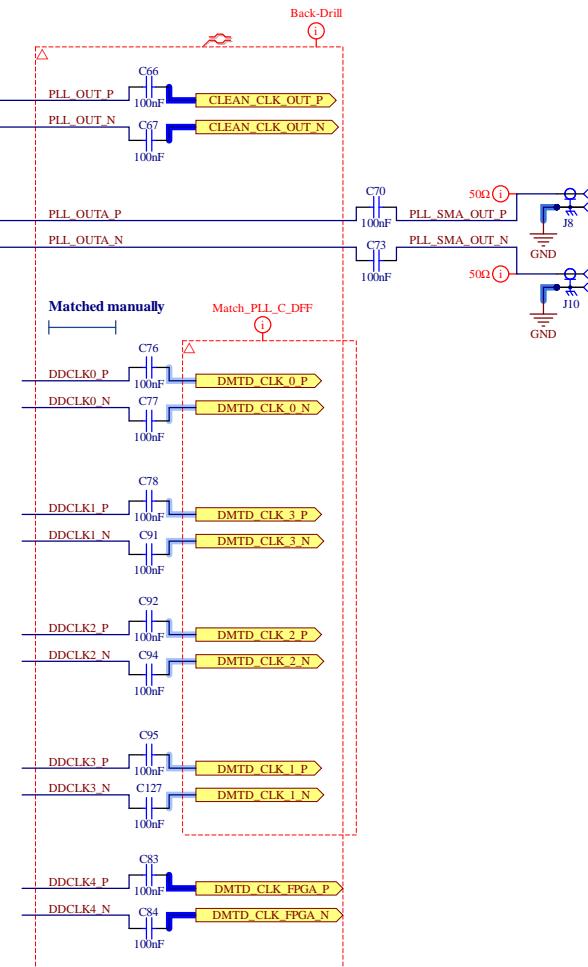
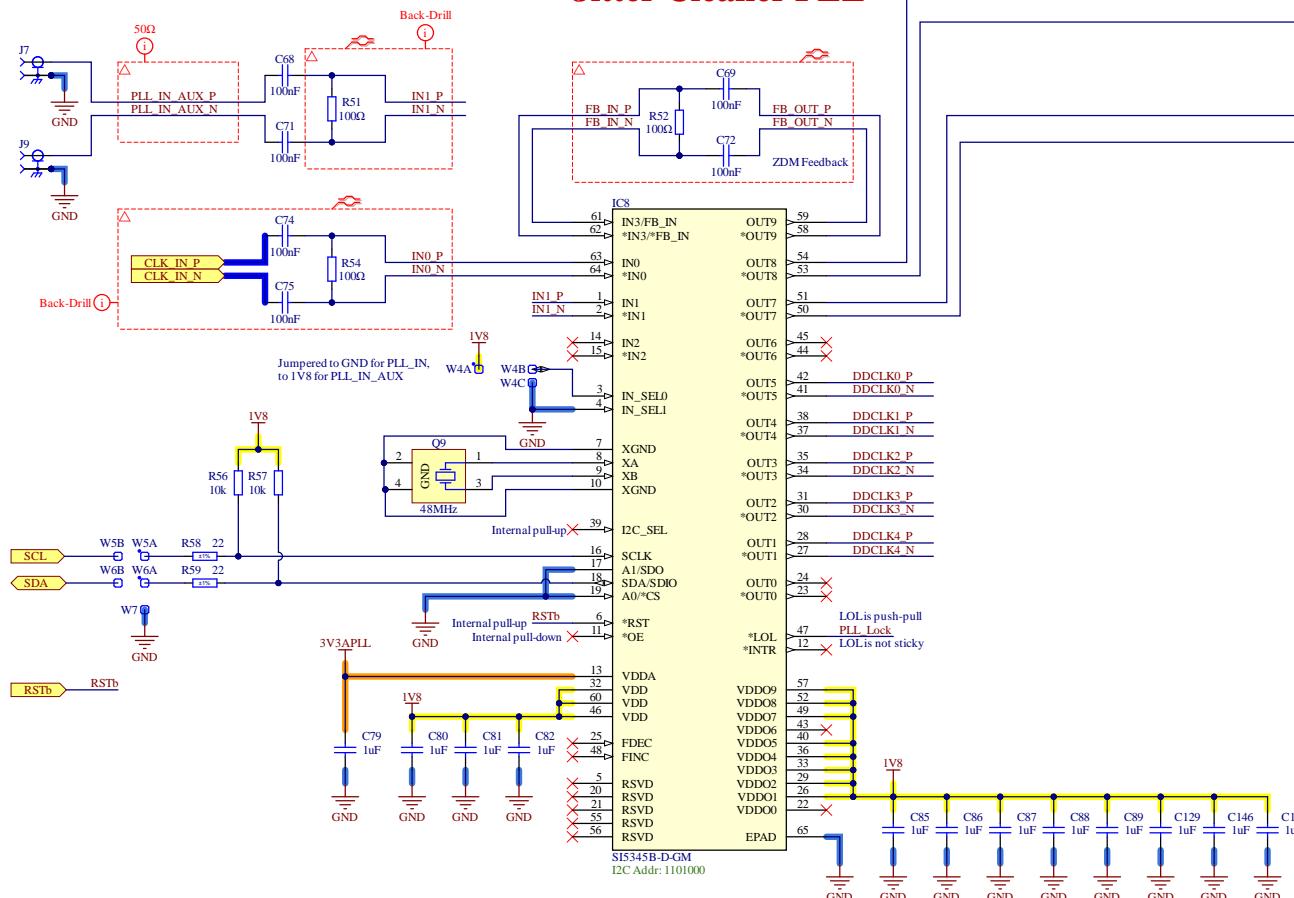
Temperature Sensor



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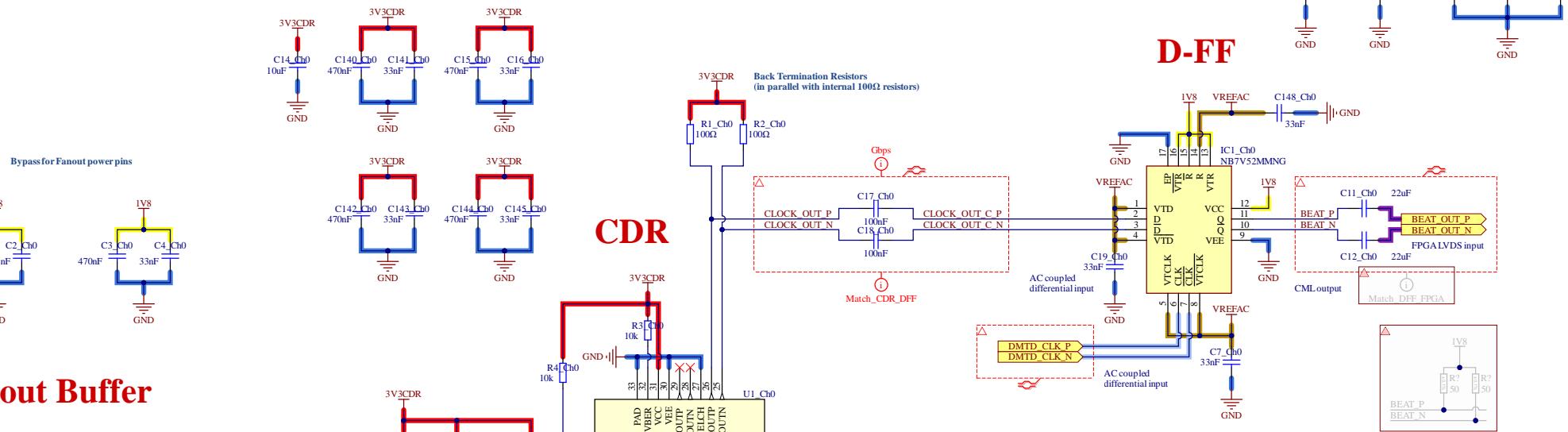


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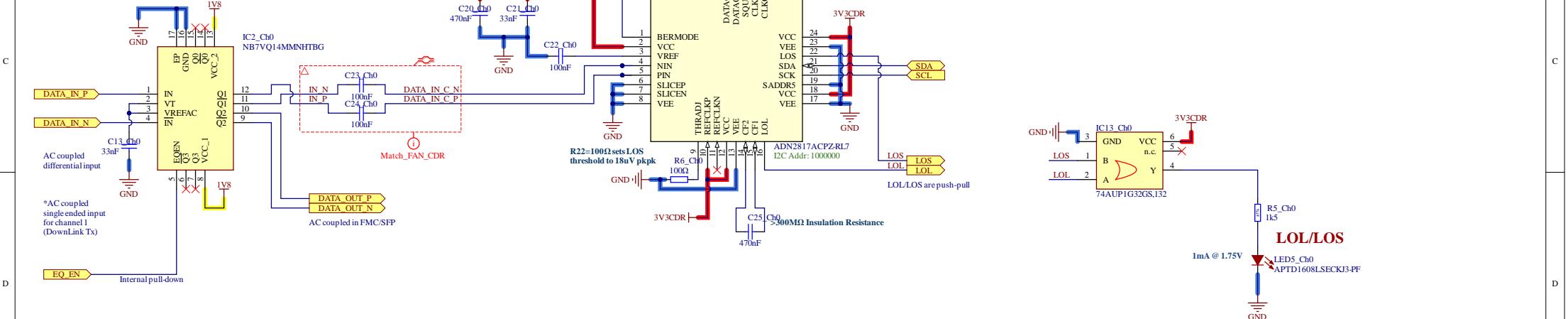


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Bypass for D-FF power pins



Fanout Buffer



Project/Equipment Phase Stable Link

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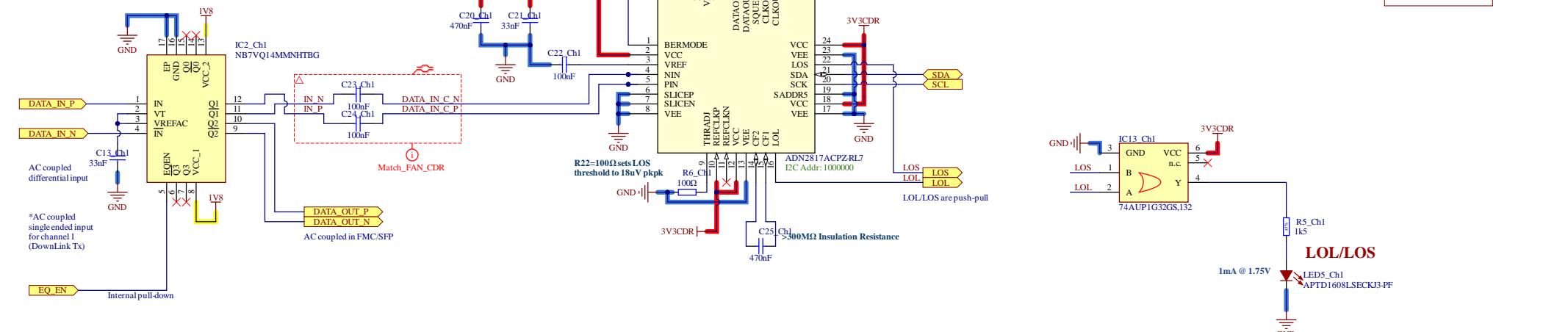


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Drawn by: E.Orzes	
Check by: E.Orzes F.Martina S.Baron	
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CDR, Fanout, D-FFEuropean Organization for Nuclear Research
CH-1211 Geneva 23 - Switzerland

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Fanout Buffer



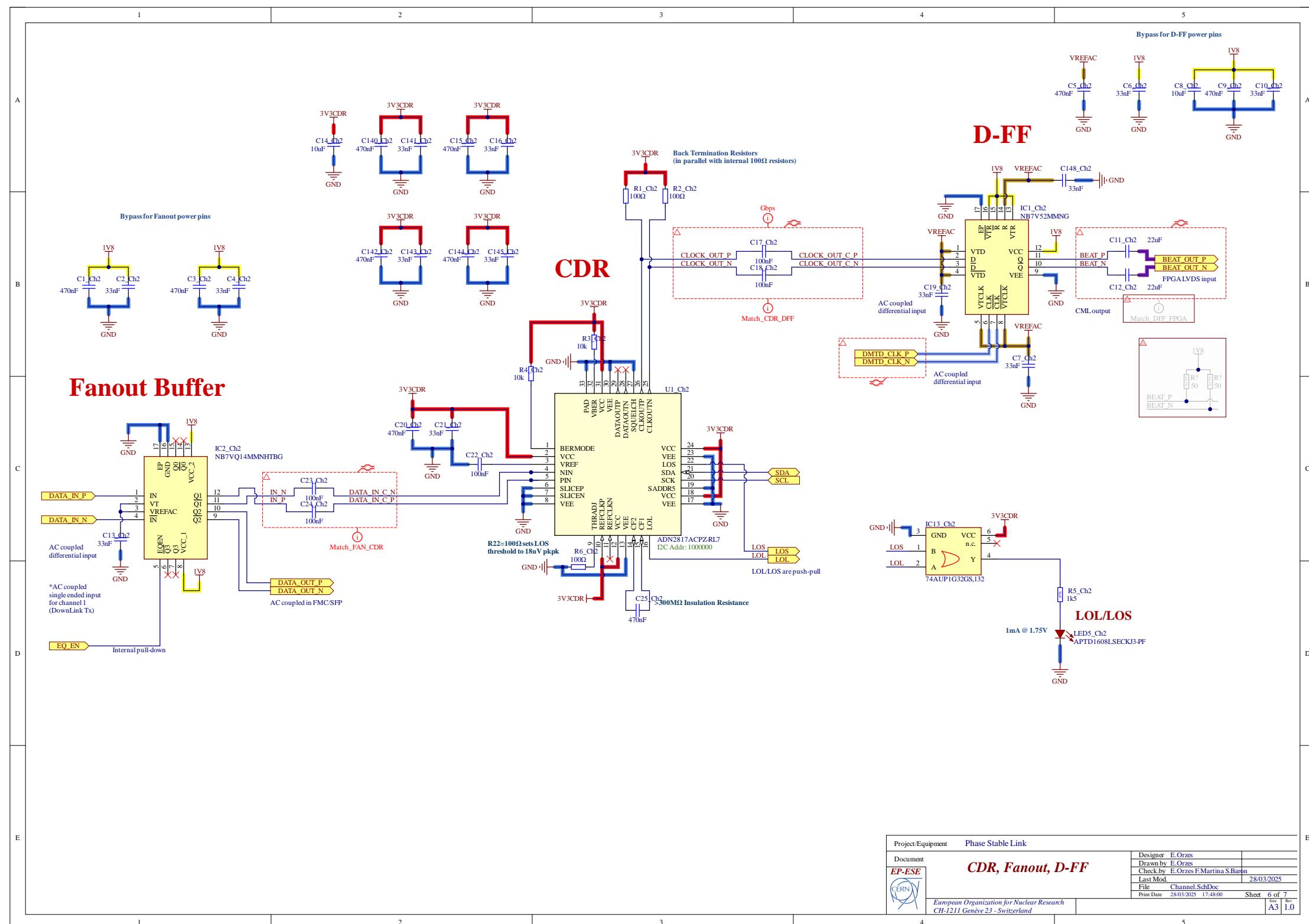
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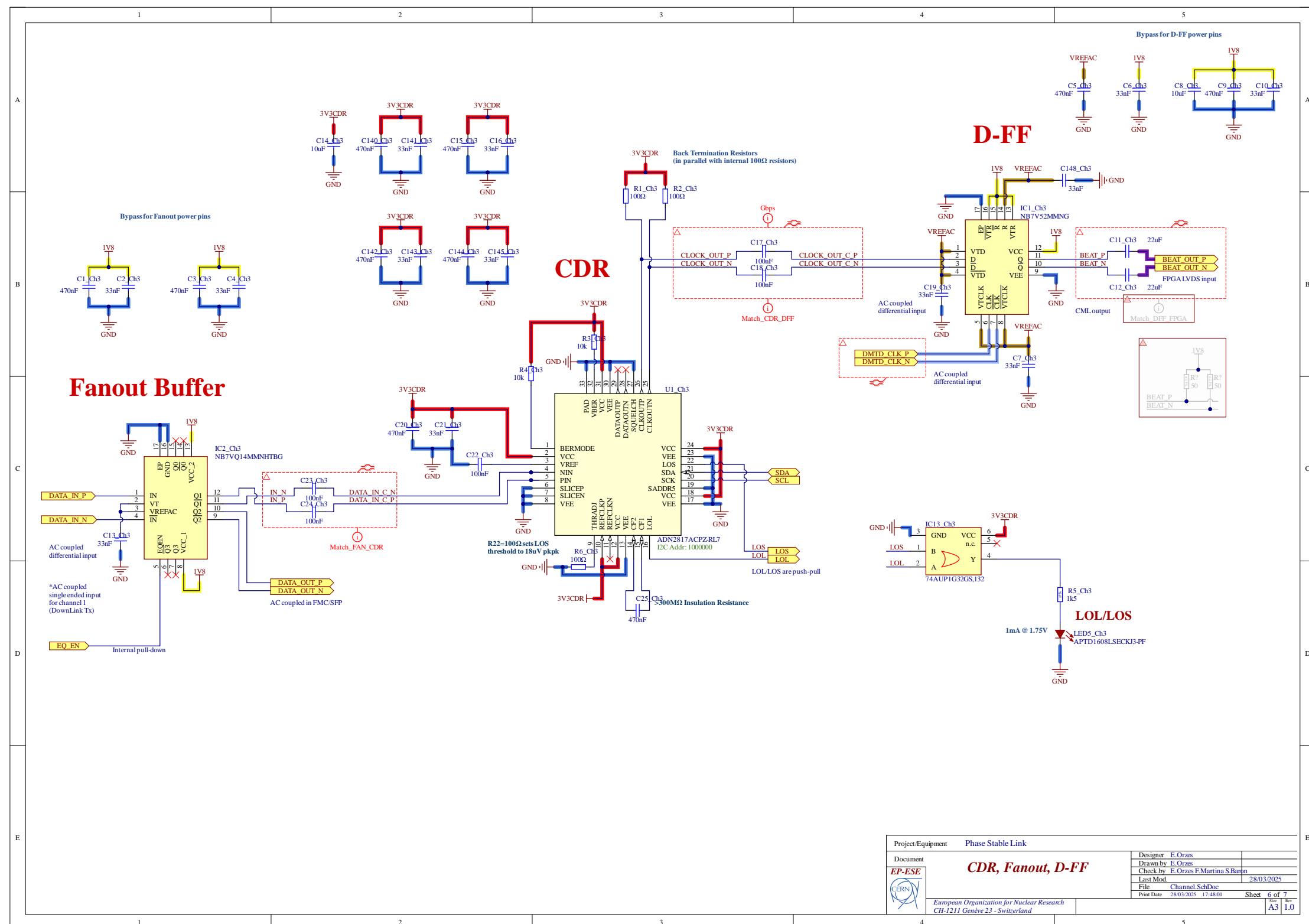
Document EP-ESE



CDR, Fanout, D-FF

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Sheet 6 of 7	
Page A3 Rev 1.0	





Linear Regulators

