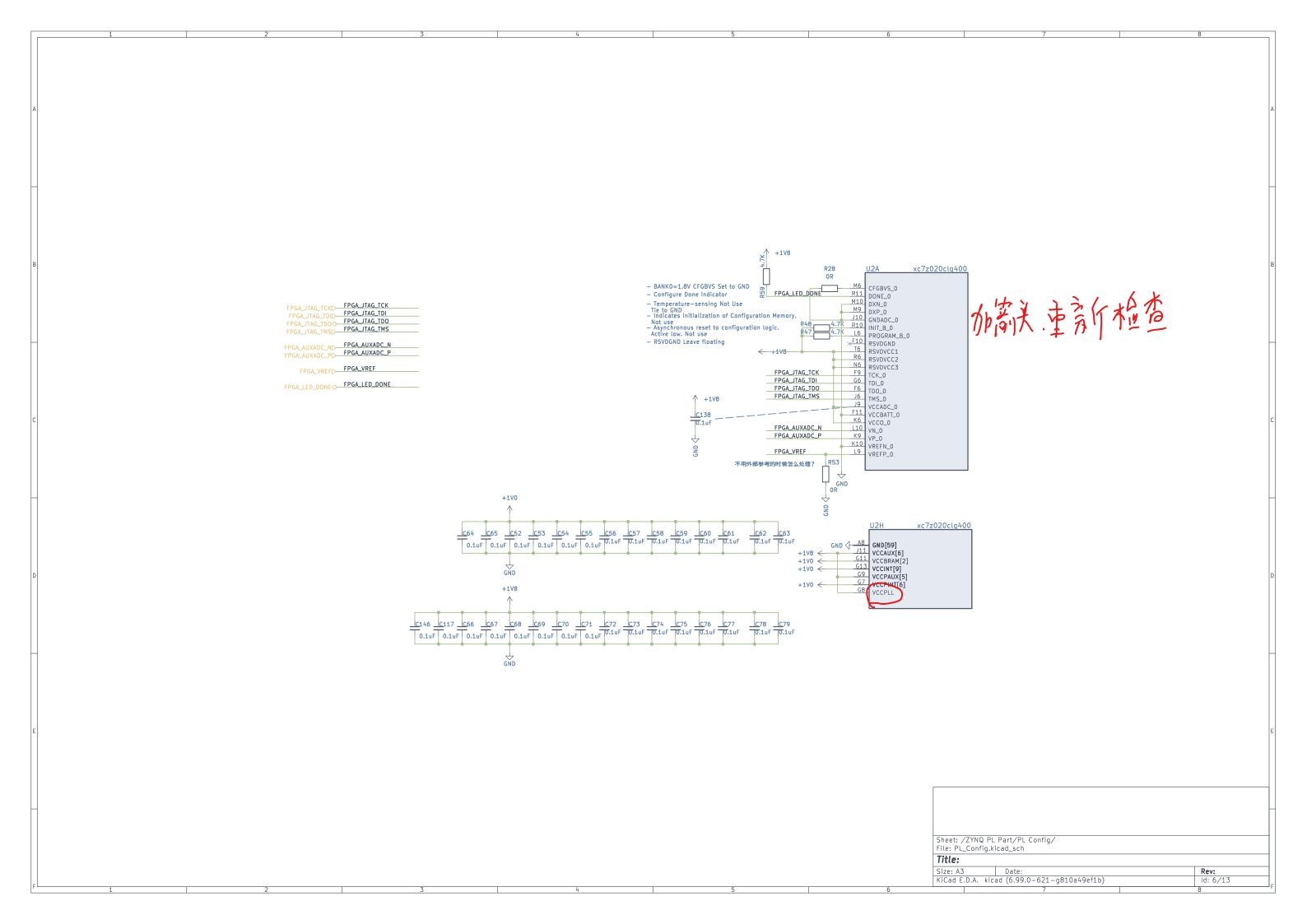
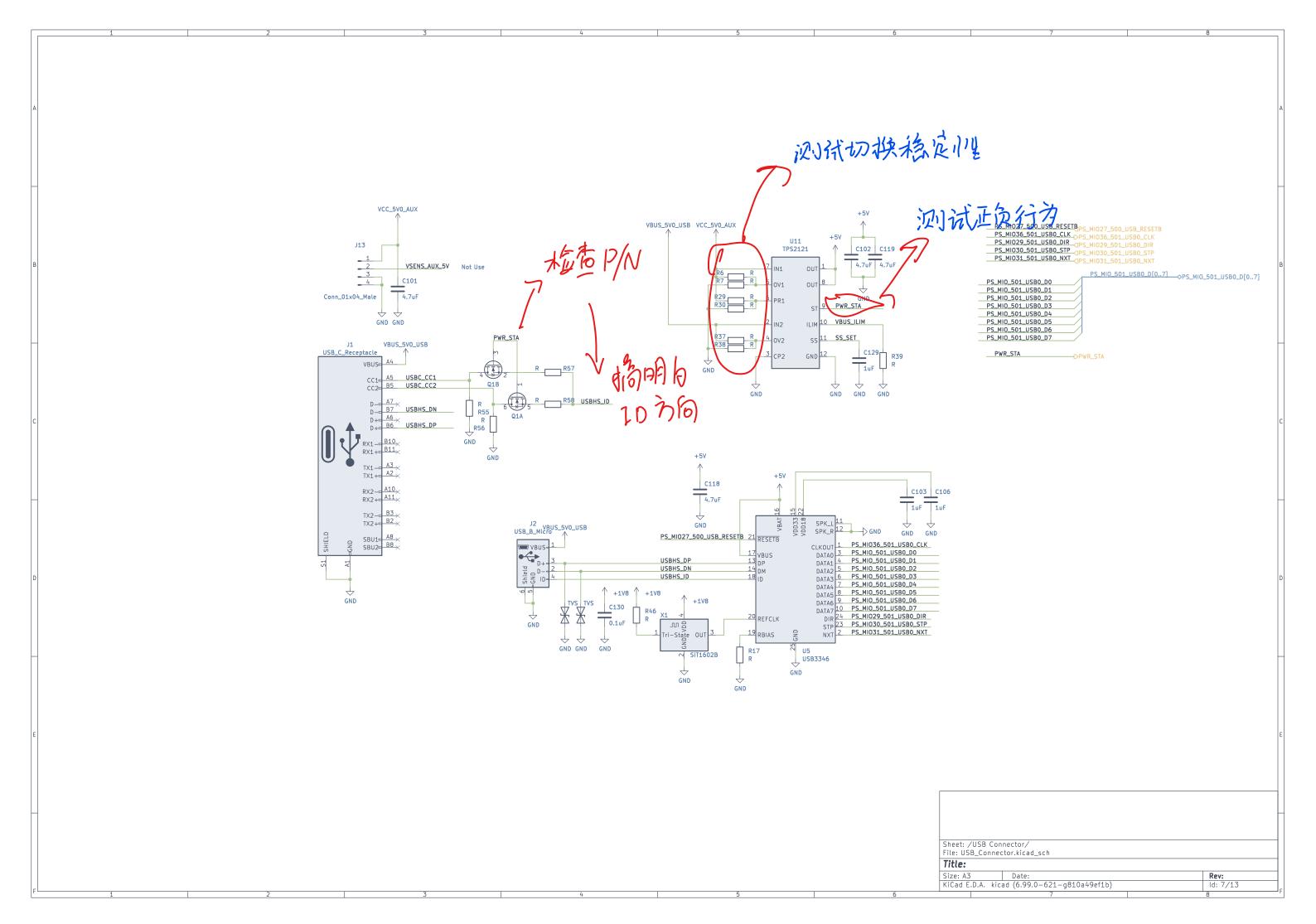


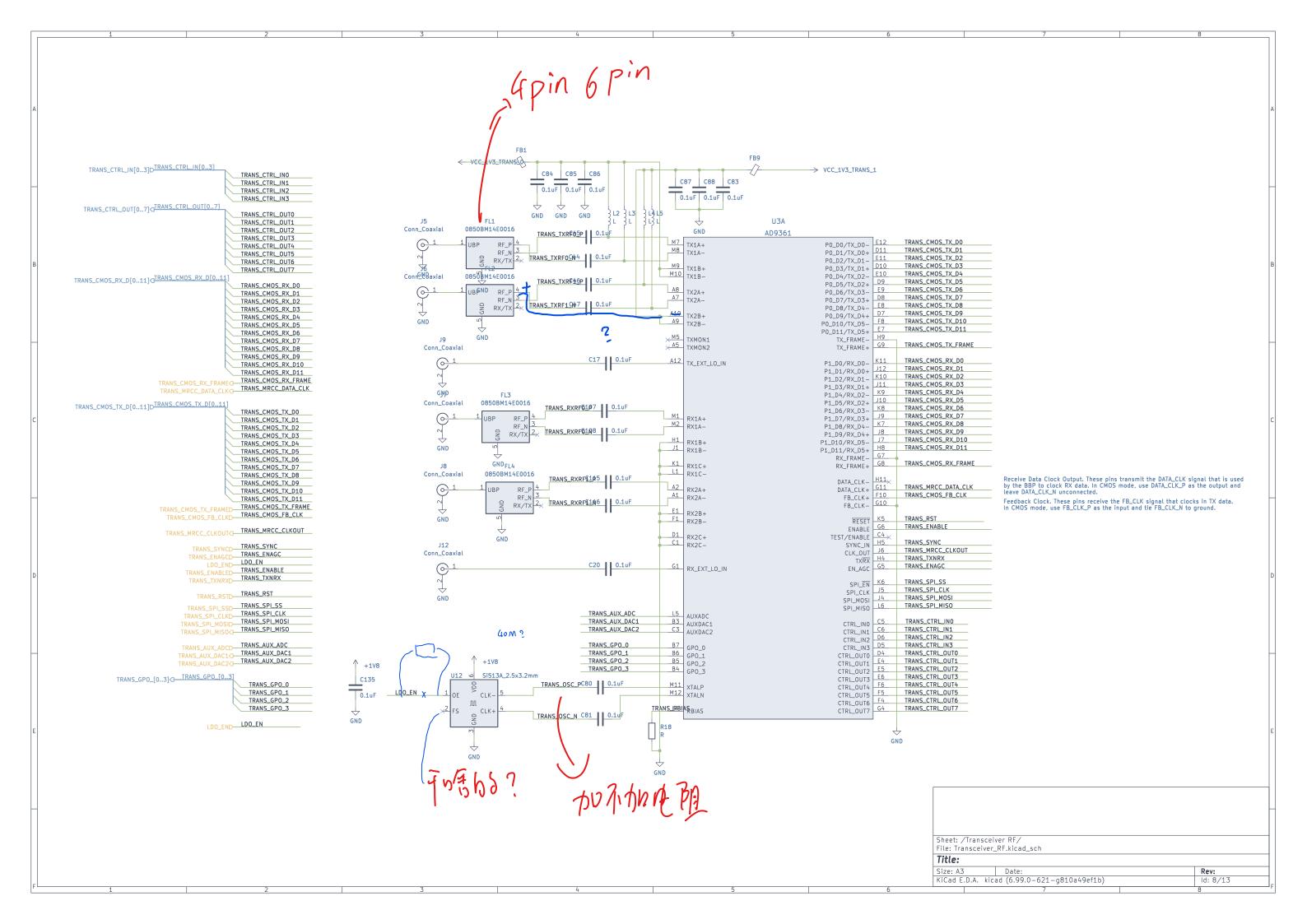
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File: PL_Transceiver.kicad_sch

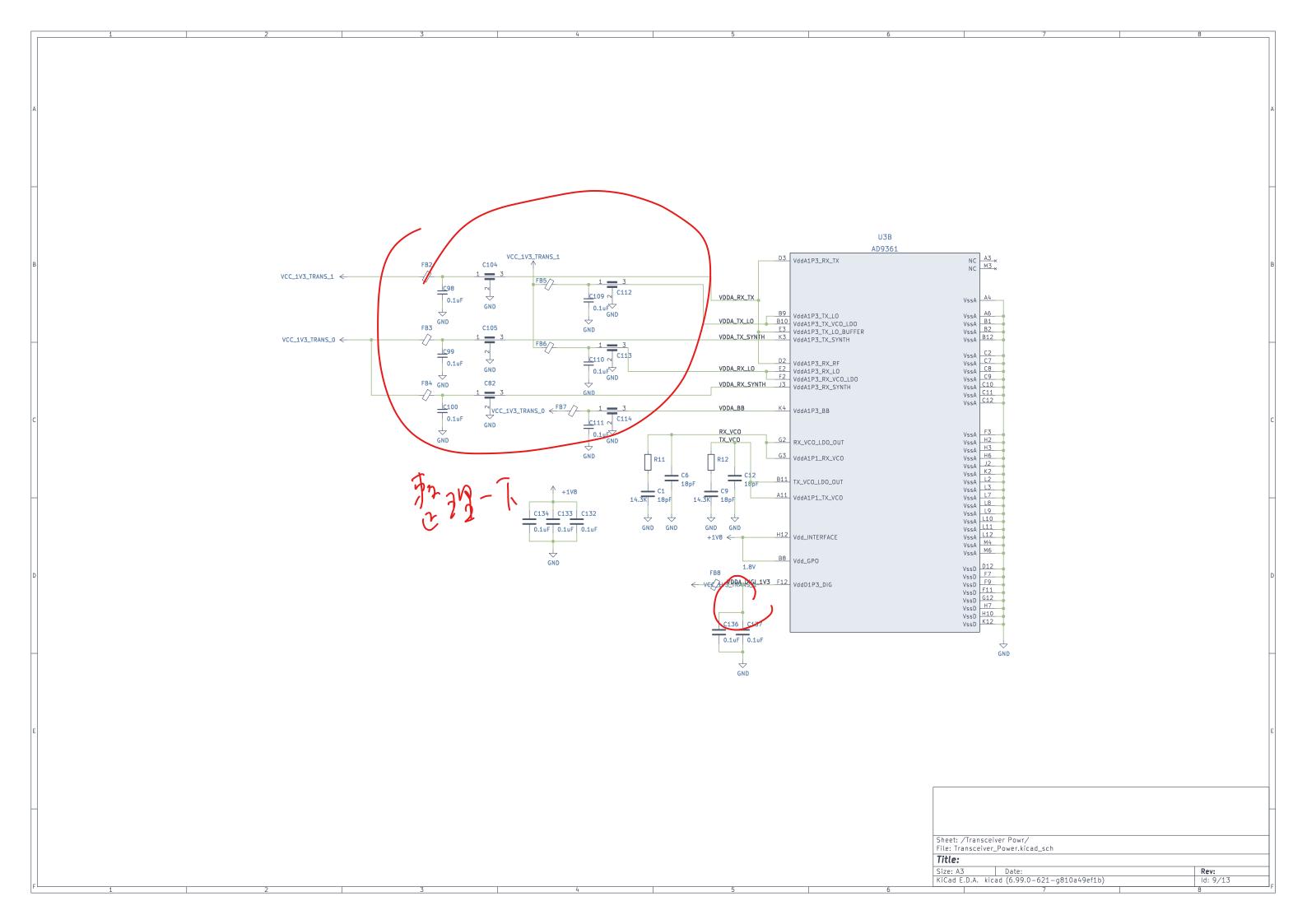
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KiCad E.D.A. kicad (6.99.0-621-g810a49ef1b) Id: 5/13

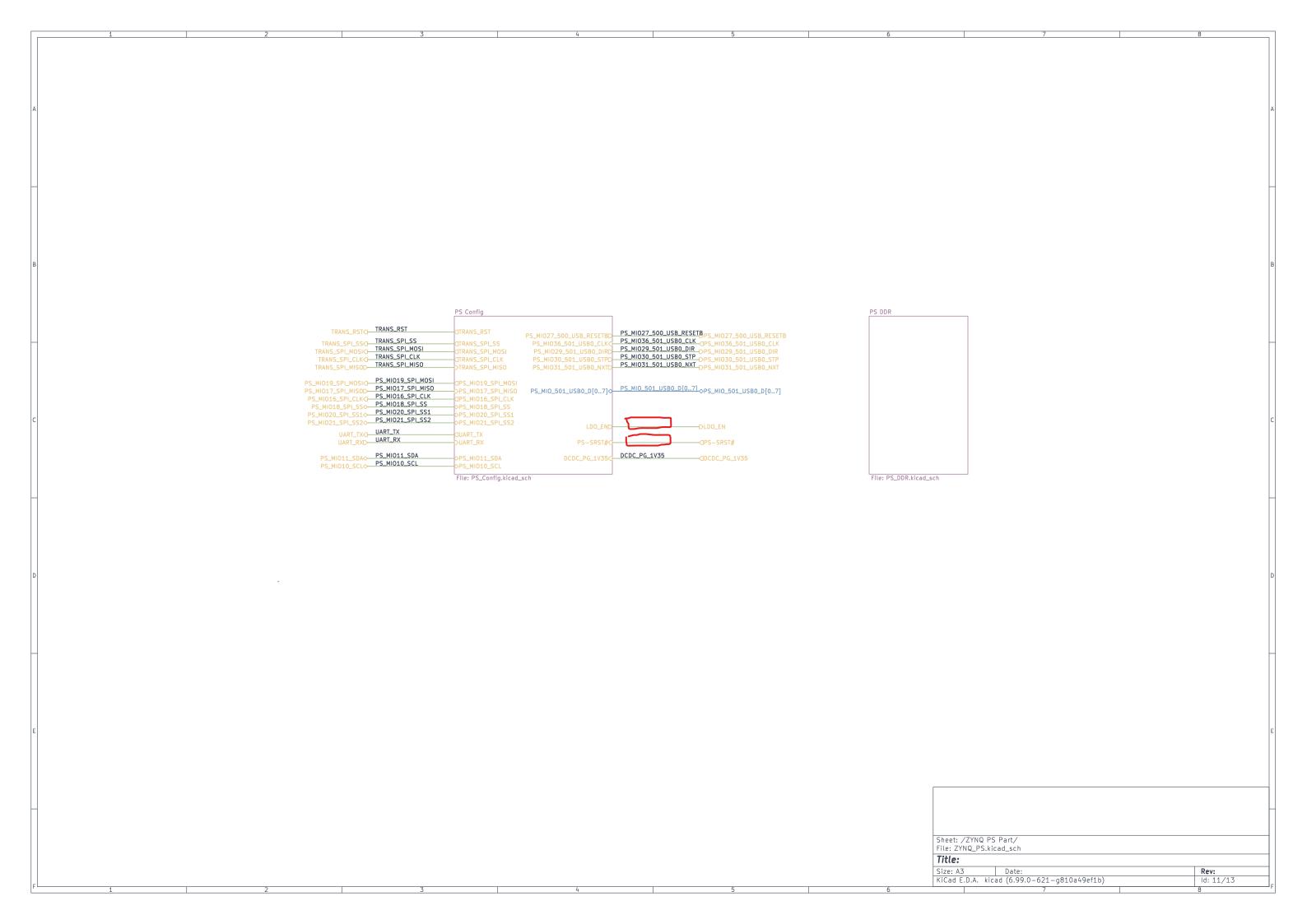
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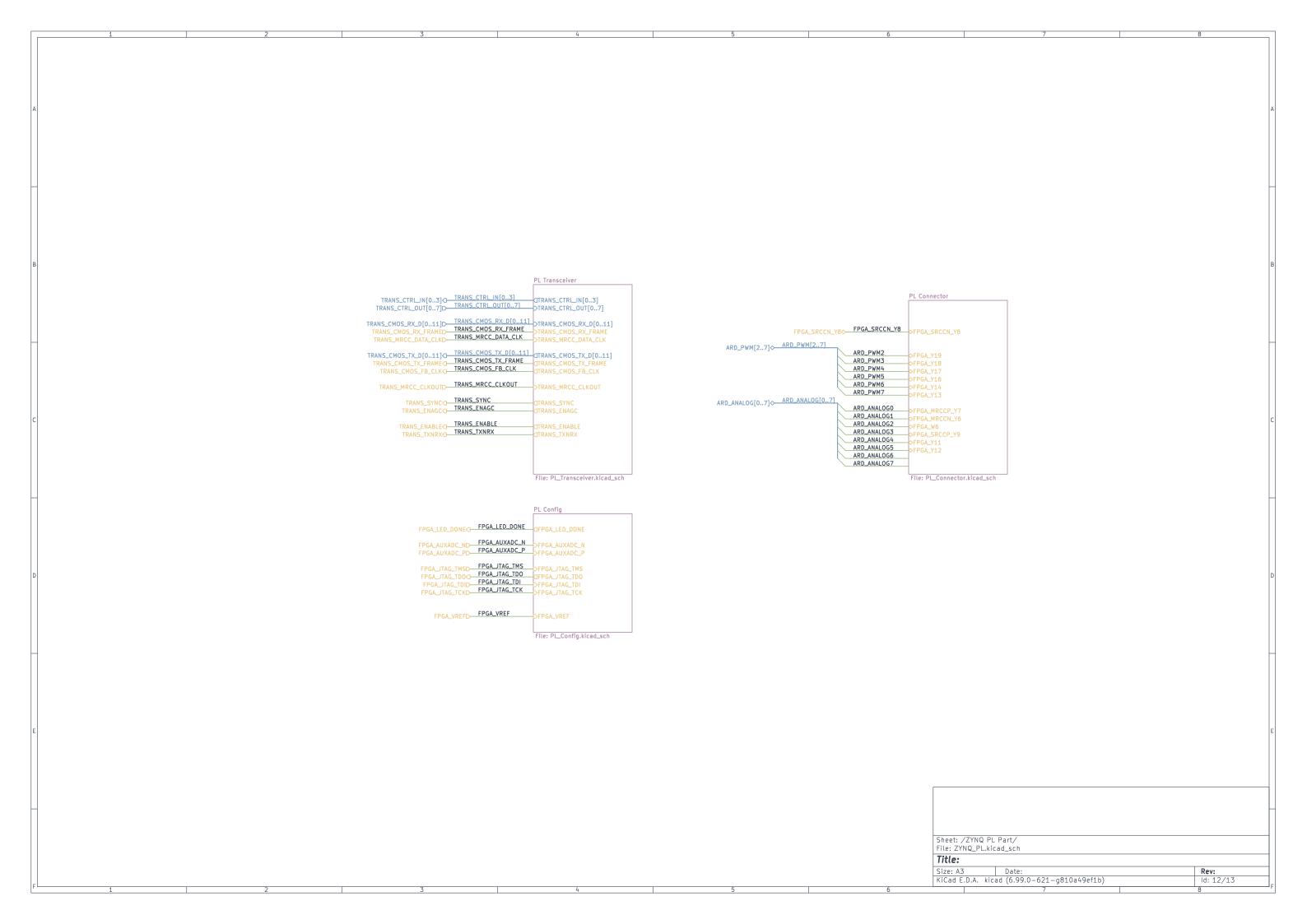












xc7z020clg400 14 FPGA_R19 FPGA_R19 R19 10_0_34 14 FPGA_T19 FPGA_T19
14 FPGA_T10 FPGA_T10 T19 10_25_34 T10 10_L1N_T0_34 14 FPGA_T11 14 FPGA_U12 14 FPGA_T12 14 FPGA_V13 FPGA_T11 T11 IO_L1P_T0_34 FPGA_U12 U12 IO_L2N_T0_34 FPGA_V13 FPGA_U13 U13 IO_L3P_TO_DQS_PUDC_B_34 14 FPGA_U13 14 FPGA_W13 14 FPGA_V12 14 FPGA_T15 14 FPGA_T14 14 FPGA_R14 14 FPGA_P14 FPGA_W13 FPGA_V12 W13 IO_L4N_T0_34 xc7z020clg400 R40 V12 IO_L4P_T0_34 T15 IO_L5N_T0_34 FPGA_T15 FPGA_T14 14 FPGA_V5 FPGA_V5 14 FPGA_U7 V5 IO_L6N_T0_VREF_13 T14 I0_L5P_T0_34 U7 | 10_L11P_T1_SRCC_13 | V7 | 10_L11N_T1_SRCC_13 | 10_L11N_T1_SRCC_13 1K R14 | IO_L6N_T0_VREF_34 FPGA_P14 T9 IO_L12P_T1_MRCC_13 14 (FPGA_U10) FPGA_U10 U10 | IO_L12N_T1_MRCC_13 | FPGA_MRCCP_Y7 | Y7 | IO_L13P_T2_MRCC_13 | FPGA_MRCCN_Y6 | Y6 | IO_L13N_T2_MRCC_13 | IO_L13N_T2_MRC_13 | IO_L13N_T2_MRCC_13 | IO FPGA_Y17 FPGA_Y16 FPGA_Y14 Y14 IO_L8N_T1_34 14 FPGA W14 FPGA U17
14 FPGA U17 FPGA U17
14 FPGA T16
14 FPGA W15 FPGA W15
14 FPGA W15 FPGA W15 W14 IO_L8P_T1_34 U17 IO_L9N_T1_DQS_34 T16 | IO_L9N_T1_DQS_34 | W15 | IO_L10N_T1_34 14 FPGA_W8 FPGA_W8
14 FPGA_W10 FPGA_W10
14 FPGA_W9 W8 IO_L15N_T2_DQS_13 V15 | IO_L10N_T1_34 | U15 | IO_L11N_T1_SRCC_34 FPGA_V15 14 FPGA_V15 W10 IO_L16P_T2_13 FPGA U15 14 FPGA_U15 14 FPGA_U14 W9 IO_L16N_T2_13 FPGA_U14 U14 | IO_L11P_T1_SRCC_34 IO_L17P_T2_13 U19 IO_L12N_T1_MRCC_34 FPGA_U19 FPGA_U18 W11 IO_L17P_T2_13 W11 IO_L18P_T2_13 14 FPGA_U19 FPGA_U19
14 FPGA_U18
14 FPGA_P19 FPGA_P19 U18 | IO_L12P_T1_MRCC_34 14 FPGA_W11 FPGA_W11 FPGA_Y11 P19 IO_L13N_T2_MRCC_34 Y11 IO_L18N_T2_13 N18 | IO_L13P_T2_MRCC_34 | P20 | IO_L14P_T2_SRCC_34 ×T5 | IO_L19P_T3_13 ×U5 | IO_L19P_T3_13 ×U5 | IO_L19N_T3_VREF_13 14 FPGA P20 FPGA P20 14 FPGA N20 FPGA N20 14 FPGA U20 FPGA U20 14 FPGA T20 FPGA T20 FPGA_Y12 FPGA_Y13 Y12 IO_L20P_T3_13 U20 IO_L15N_T2_DQS_34
T20 IO_L15P_T2_DQS_34 Y13 IO_L20N_T3_13 14 FPGA_V11 FPGA_V11 14 FPGA_V10 FPGA_V10 V11 | IO_L21P_T3_DQS_13 V10 | IO_L21N_T3_DQS_13 FPGA_W20 W20 IO_L16N_T2_34 FPGA_V20 V20 IO_L16P_T2_34 × V6 I0_L22P_T3_13 FPGA_Y19 Y19 | IO_L17N_T2_34 | IO_L17P_T2_34 FPGA_W6 W6 IO_L22N_T3_13 14 FPGA_W16 FPGA_W16
14 FPGA_V16 W16 IO_L18N_T2_34 T8 VCC0_13[4] VCC_BANK34 ← V16 IO_L18P_T2_34 R17 | IO_L18P_I2_34 R16 | IO_L19N_T3_VREF_34 R18 | IO_L19P_T3_34 R18 | IO_L20N_T3_34 14 FPGA_T17 FPGA_T17 14 FPGA_V18 FPGA_V18 14 FPGA_V17 FPGA_V17 14 FPGA_W19 FPGA_W19 14 FPGA_W18 FPGA_W18 T17 | IO_L20N_I3_34 | V18 | IO_L21N_I3_DQS_34 | V17 | IO_L21P_I3_DQS_34 | V17 | IO_L21P_I3_DQS_54 | V17 | IO_L21P_I3_DQS_5 V17 | O_L21P_T3_DQS W19 | O_L22N_T3_34 W18 | O_L22P_T3_34 P18 | O_L23N_T3_54 N17 | O_L23P_T3_34 P15 | O_L24N_T3_34 P15 | O_L24P_T3_34 14 FPGA_P15 FPGA_P15
VCC_BANK34 < N19 VCC0_34[6]

> Sheet: /ZYNQ PL Part/PL Connector/ File: PL_Connector.kicad_sch Title: Size: A3 Date: KiCad E.D.A. kicad (6.99.0-621-g810a49ef1b)

FPGA_MRCCP_Y7
FPGA_MRCCN_Y6
FPGA_MRCCN_Y6

FPGA_W6 FPGA_W6

FPGA_SRCCP_Y9

FPGA_Y11

FPGA_Y11

FPGA_Y11

FPGA_Y12♦ FPGA_Y12

FPGA_SRCCN_Y8

FPGA_SRCCN_Y8

FPGA_Y19

FPGA_Y19

FPGA_Y18 FPGA_Y17

FPGA_Y17 FPGA_Y17

FPGA_Y13♦ FPGA_Y13

FPGA_Y16 FPGA_Y14

