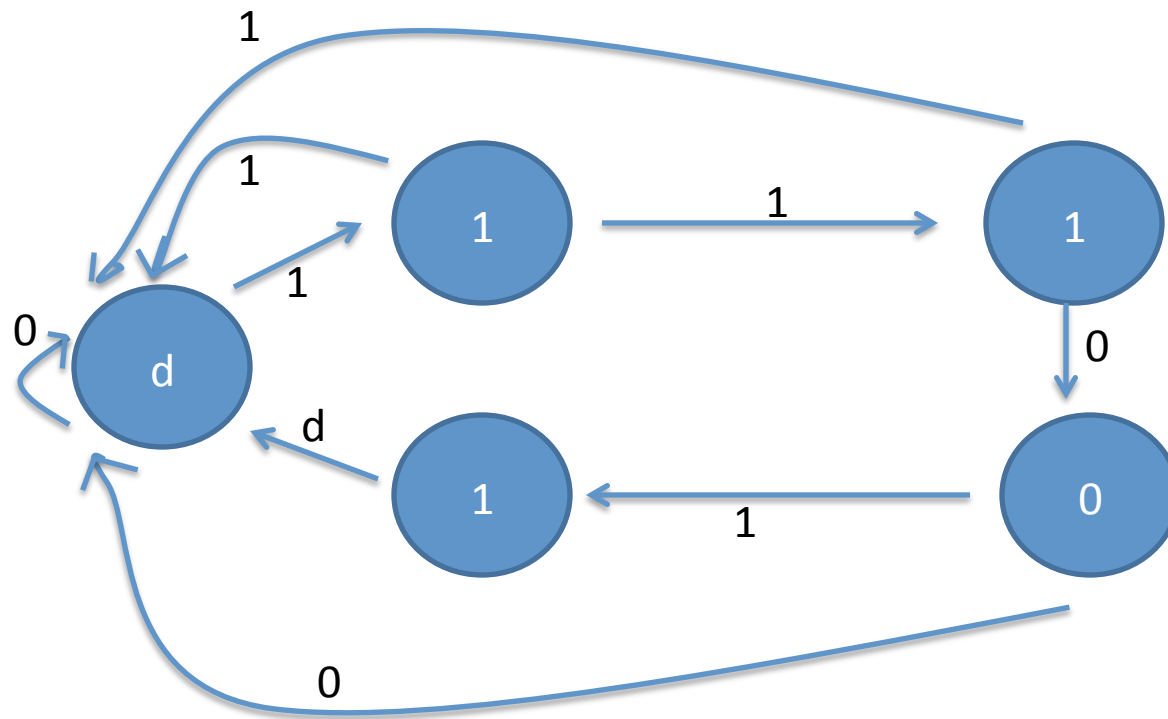
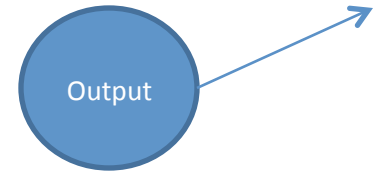
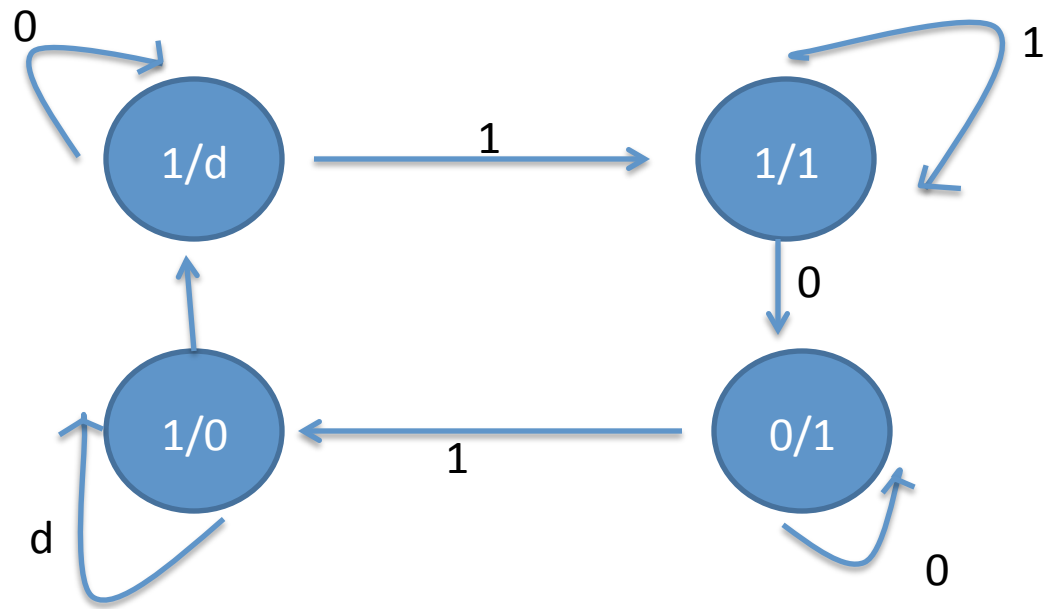
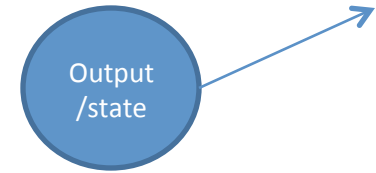


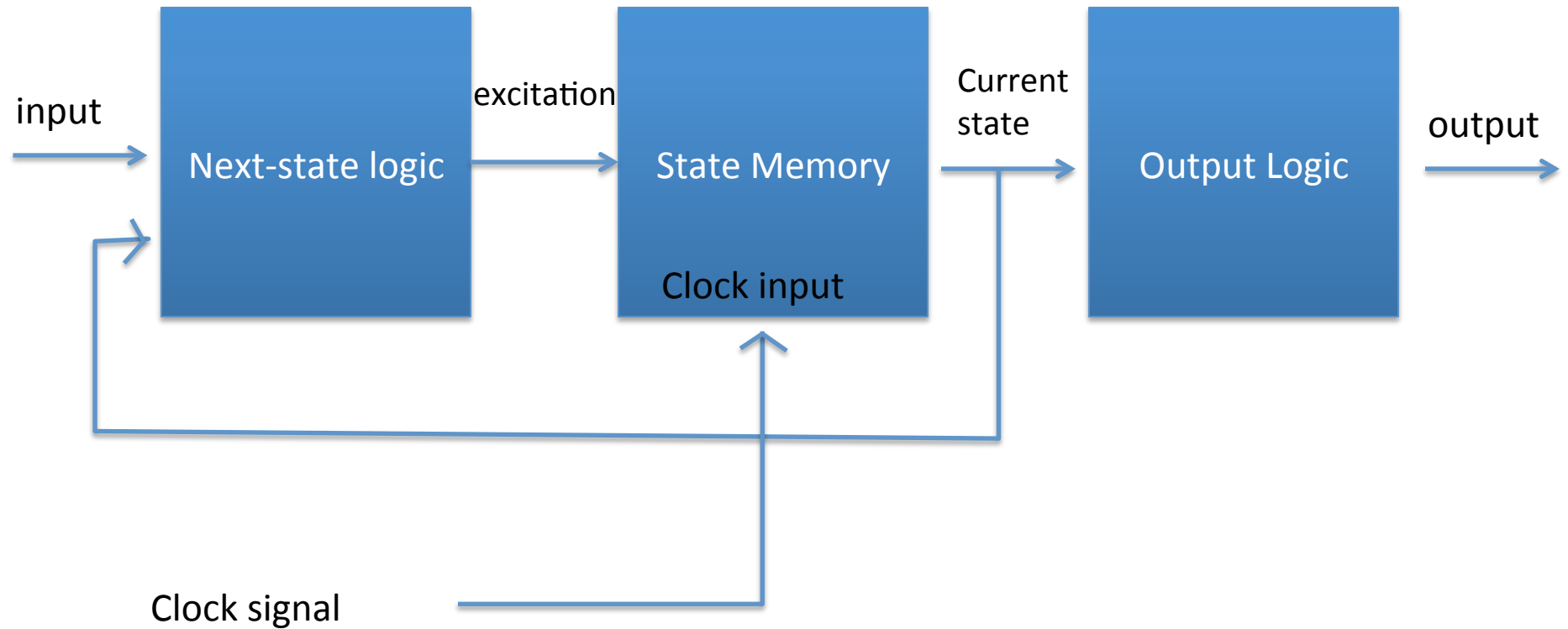
Moore State Transition Diagram



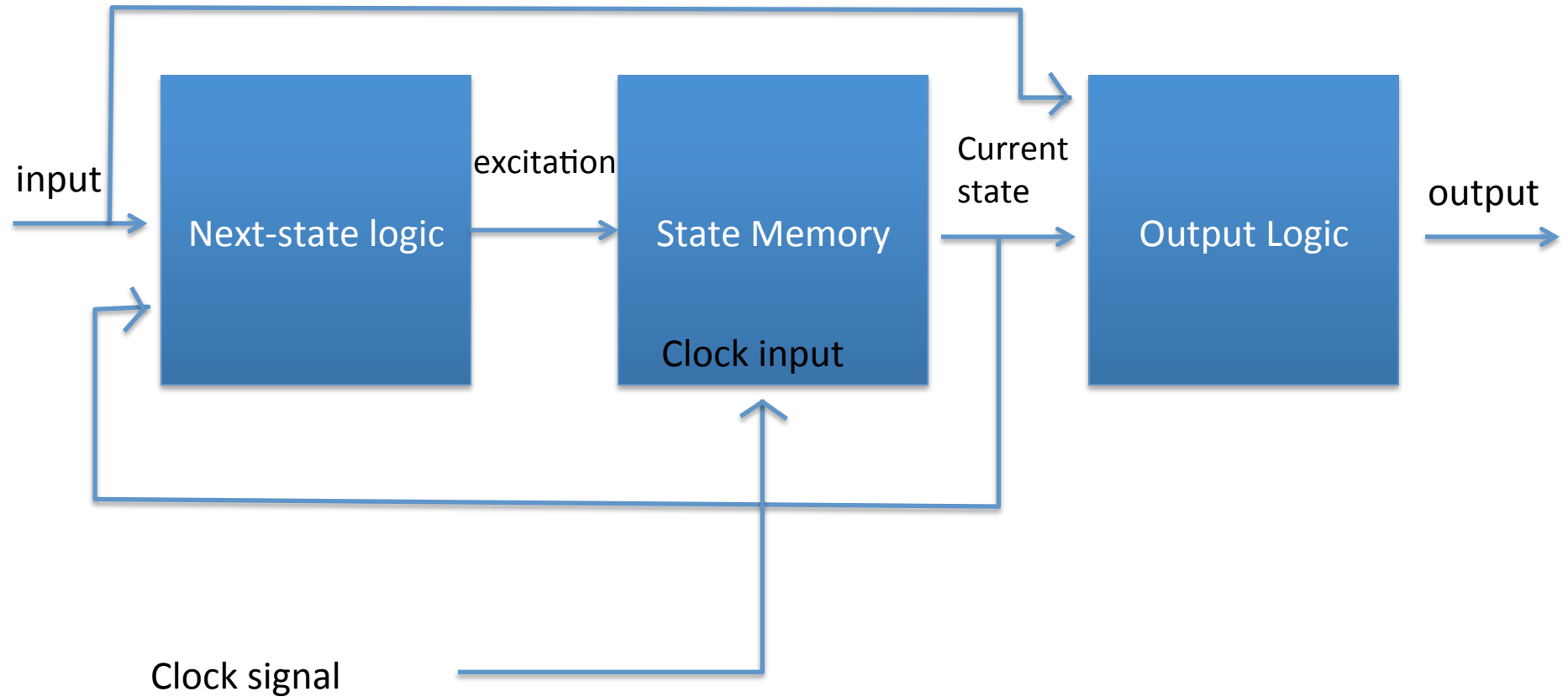
Mealy State Transition Diagram



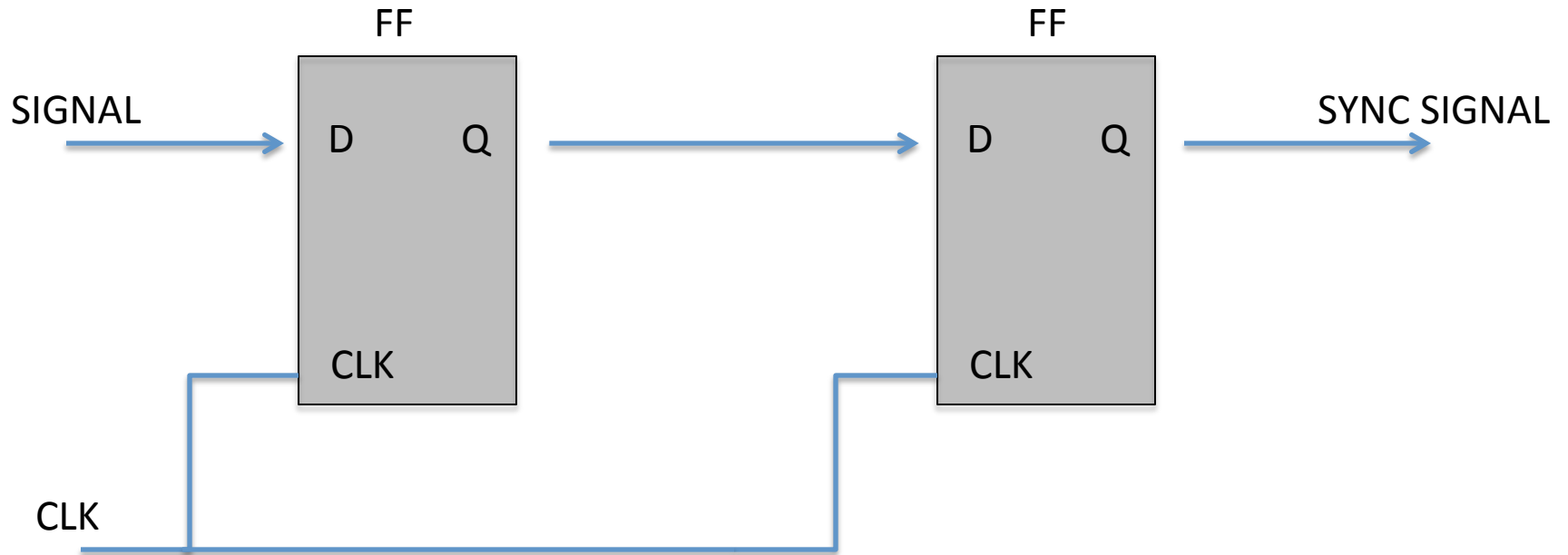
RTL Diagram for Moore Model



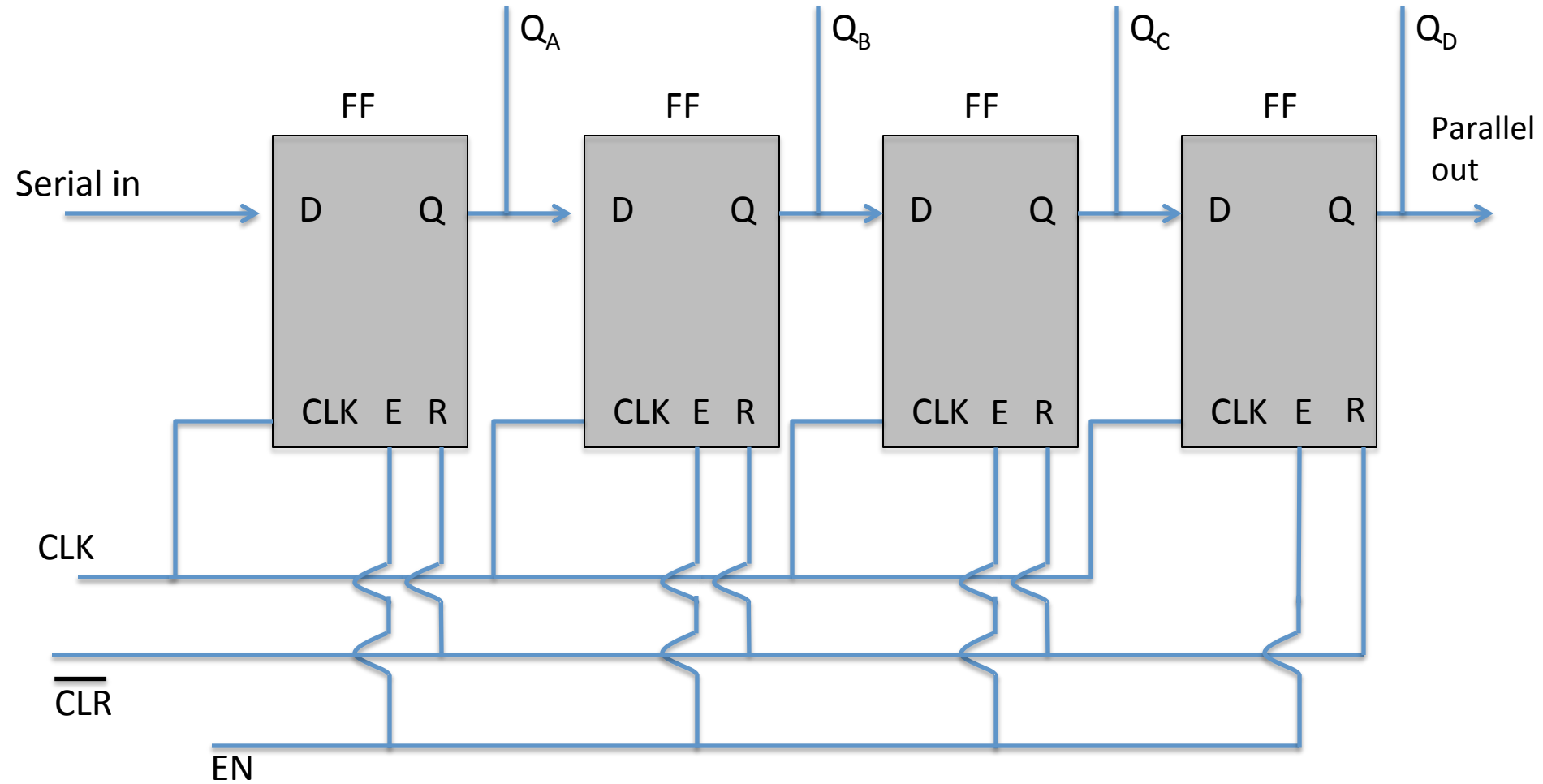
RTL Diagram for Mealy Model



RTL Diagram For Synchronizer



RTL Diagram For MSB Serial to Parallel Shift Register



RTL Diagram For MSB Parallel to Serial Shift Register

