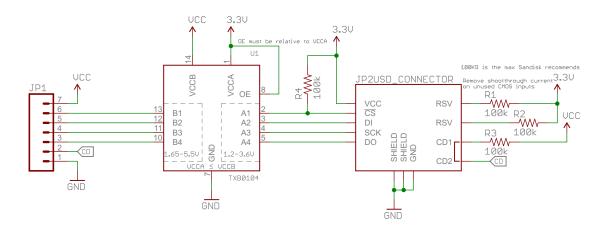
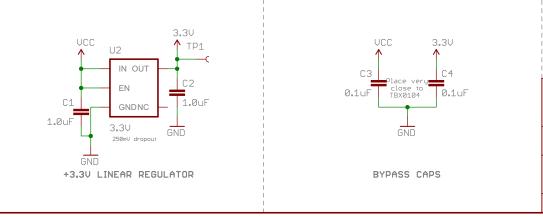
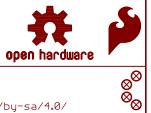
VCC +3.3V to +5.0V EXPECT UP TO VCC ON SIGNAL LINES

When VCC drops below about 3.0V @ 25MHz the output wave form starts to degrade fast.







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TITLE: S	Shifting ₋	_microSD _.	_v10
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Design bBrent	Wilkins	REV: v10

Date: 2/11/2016 2:07:56 PM Sheet: 1/1