

Fig. 1: TQFP adaptor board overlay

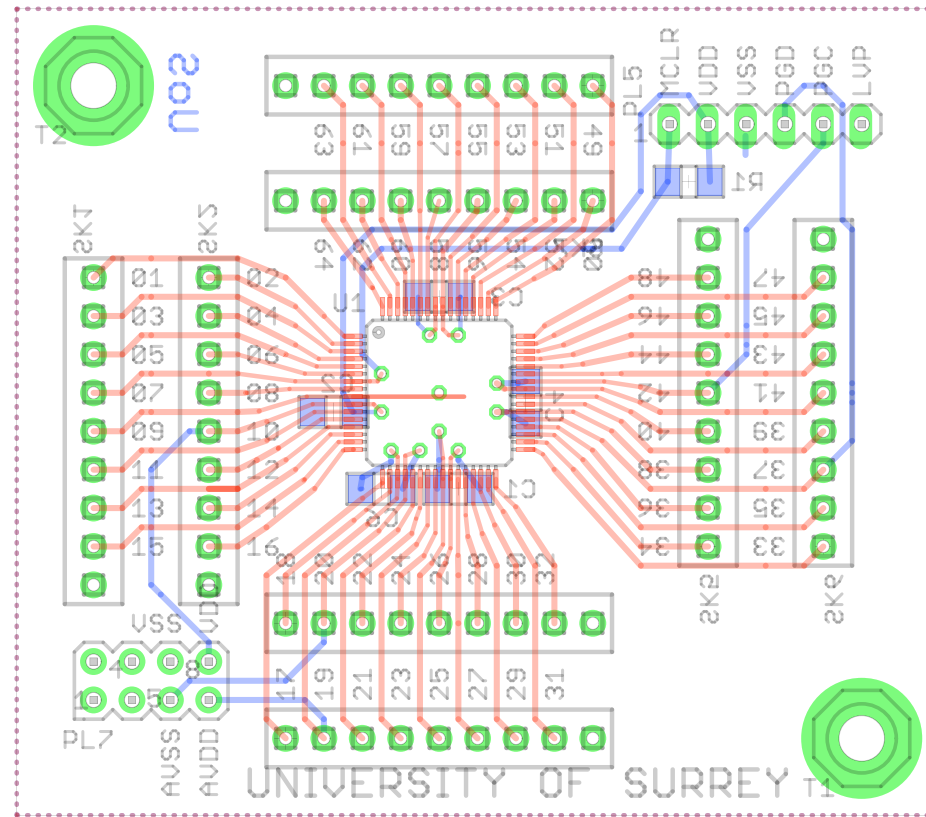


Fig. 2: Connecting the debugger

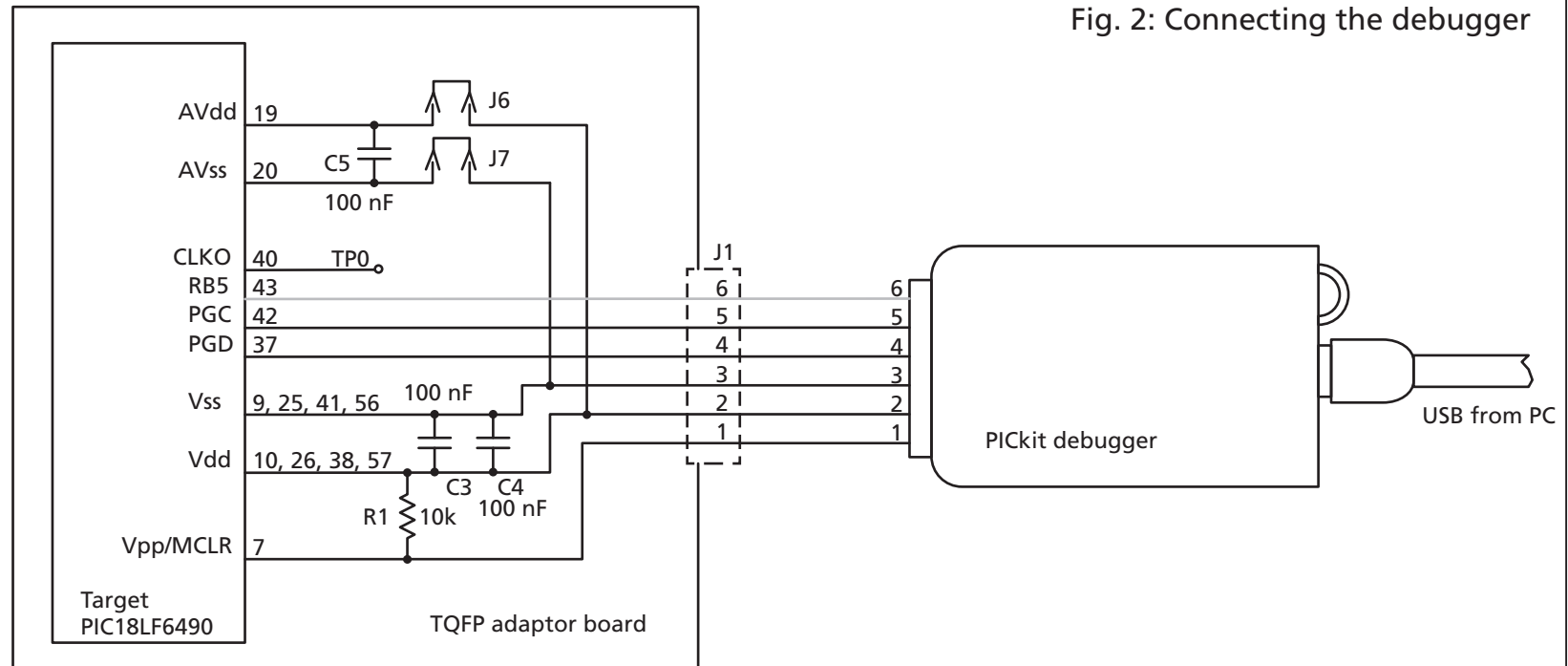


Fig. 3: LDO 3.3 volt regulator.

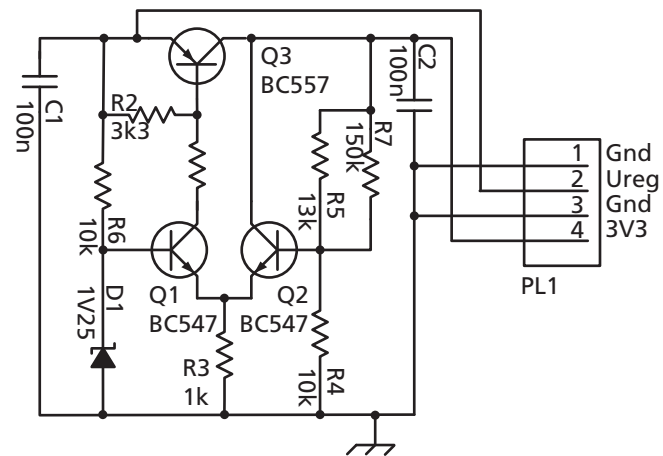
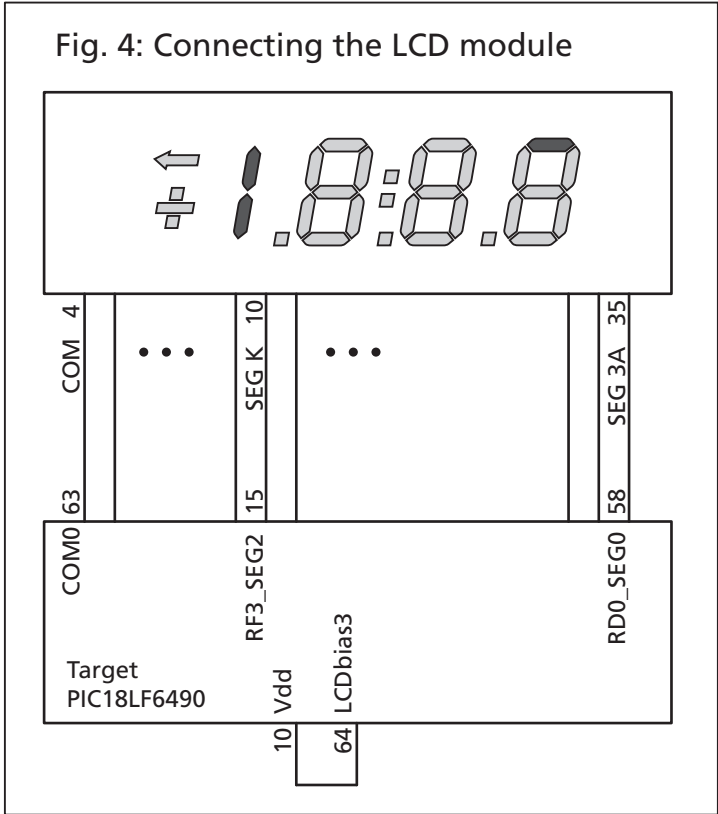


Fig. 4: Connecting the LCD module



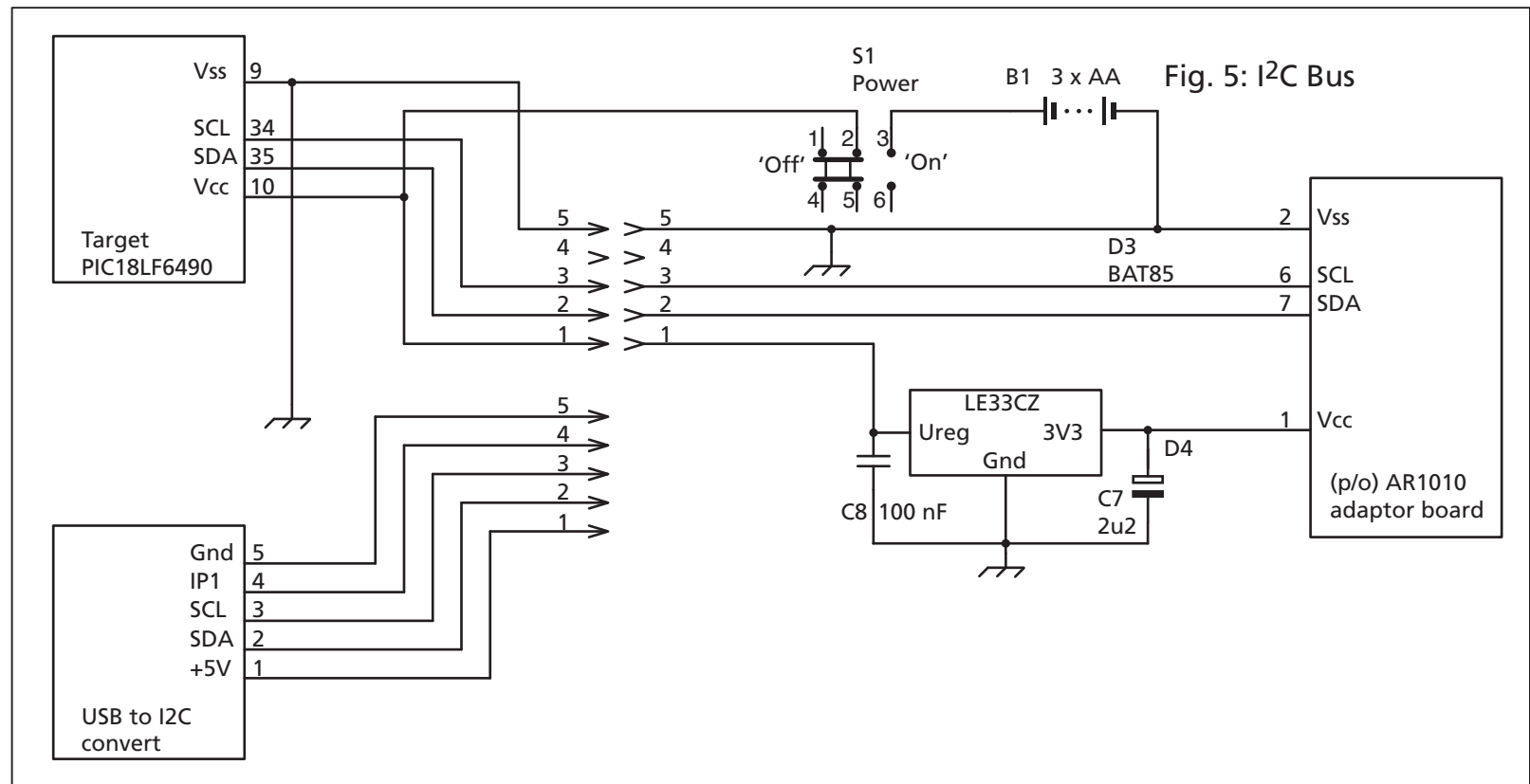
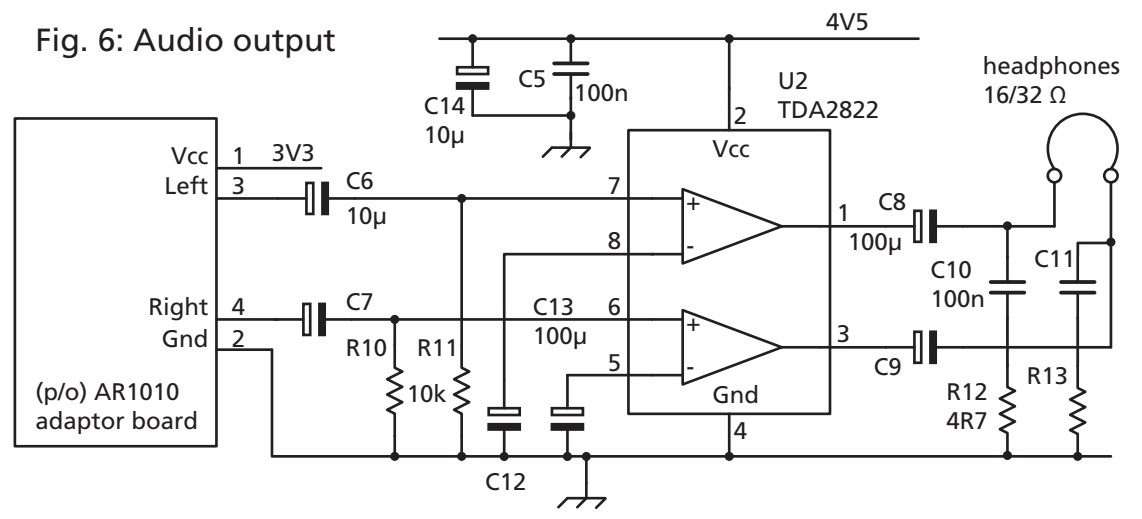


Fig. 6: Audio output



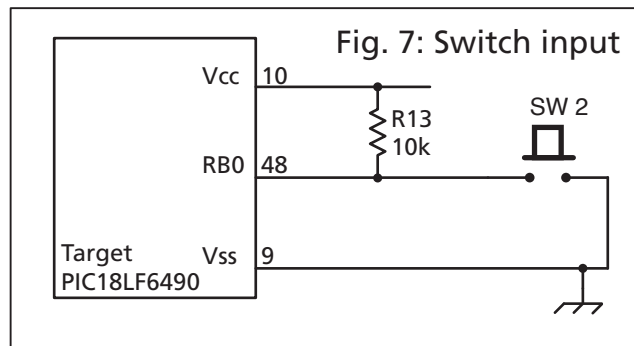


Fig. 8: Receiver adaptor board

