Spartan-3 Board -- Diagrama de Entradas/Salidas -- Grupo: _____

Xilinx Spartan-3 FPGA (XC3S1000-FT256) **Clock Oscillator** (50MHz) **Pushbuttons** Discrete LEDs (H when pressed) (ON when H) Slide Switches (H when up) Seven-Segment Display Module (segment ON when L; digit selected when L) D1[15:0] D2[15:0] SRAM Serial Port (two 256Kx16 banks; (DB-9 Connector) unnumbered ports are TXD **Serial Port** common to both banks) (J1 Header Pins) CE2 CLK PS/2 Port RED **VGA Port**