

ARM Cortex-A72 peak performance (throughput) prediction

workload (convolution shape)

[1280, 1, 1, 1000, 1280, 1, 1, 1, 1]
 [64, 224, 224, 64, 64, 3, 3, 1, 1]
 [16, 112, 112, 96, 16, 1, 1, 1, 1]
 [256, 56, 56, 256, 256, 3, 3, 1, 1]
 [64, 56, 56, 128, 64, 1, 1, 2, 2]
 [512, 13, 13, 1000, 512, 1, 1, 1, 1]
 [64, 35, 35, 96, 64, 3, 3, 1, 1]
 [160, 17, 17, 160, 160, 7, 1, 1, 1]
 [1024, 14, 14, 512, 1024, 1, 1, 1, 1]
 [512, 14, 14, 512, 512, 1, 1, 1, 1]
 [256, 14, 14, 512, 256, 3, 3, 2, 2]
 [48, 27, 27, 192, 48, 3, 3, 1, 1]
 [2048, 7, 7, 512, 2048, 1, 1, 1, 1]
 [544, 14, 14, 128, 544, 1, 1, 1, 1]
 [640, 7, 7, 128, 640, 1, 1, 1, 1]
 [160, 56, 56, 128, 160, 1, 1, 1, 1]
 [800, 7, 7, 128, 800, 1, 1, 1, 1]
 [416, 14, 14, 128, 416, 1, 1, 1, 1]
 [1280, 8, 8, 320, 1280, 1, 1, 1, 1]
 [384, 27, 27, 64, 384, 1, 1, 1, 1]
 [864, 7, 7, 128, 864, 1, 1, 1, 1]

