

<u>Address</u>	<u>Description</u>
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FFEA	WRITE/READ
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Bit	Description
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Bit 0:	ROM PAGING 0
Bit 1:	ROM PAGING 1
Bit 2:	ROM PAGING 2
Bit 3:	ROM PAGING 3
Bit 4:	ROM PAGING 4
Bit 5:	System Type (Reserved - See Note)
6-7	Bit 7 Bit 6
0	0 512K System Memory
0	1 512K System Memory
1	0 512K System Memory
1	1 640K System Memory

NOTE: When reading Port FFEA, Bit 4 will be inverted from what was written, (i.e. when a 0 is written, a 1 will be read; when a 1 is written, a 0 will be read.) FFEA Bit 4 can be used to determine the system type. If Bit 4 is read back inverted, the system is identified as a Tandy 1000 SL. If Bit 4 is NOT inverted, the system is identified as a Tandy 1000 TL.

Writing 1 to bit 4 disables access to the ROM segment at E0000.

Writing X to bit 6 or 7, with the 128k video RAM upgrade installed, disables access to memory between 80000-9FFFF.

ROM Paging Definition:

4 1 Meg X 8 ROMs	ADDRESS				ROM PAGES					ROM CS		SELECT	64K Page			
	19	18	17	16	4	3	2	1	0	#0	#1	2	1	0	ROM 0	ROM 1
F0000-FFFFF	1	1	1	1	x	x	x	x	x	0	1	x	1	1	1	
E0000-EFFFF	1	1	1	0	1	x	1	1	1	1	1	x	x	x		
E0000-EFFFF	1	1	1	0	1	x	1	1	0	0	1	x	1	0	2	
E0000-EFFFF	1	1	1	0	1	x	1	0	1	0	1	x	x	1	3	
E0000-EFFFF	1	1	1	0	1	x	1	0	0	0	1	x	0	0	4	
E0000-EFFFF	1	1	1	0	1	x	0	1	1	1	0	x	1	1		1
E0000-EFFFF	1	1	1	0	1	x	0	1	0	1	0	x	1	0		2
E0000-EFFFF	1	1	1	0	1	x	0	0	1	1	0	x	0	1		3
E0000-EFFFF	1	1	1	0	1	x	0	0	0	1	0	x	0	0		4
2 2 Meg X 8 ROMs	19	18	17	16	4	3	2	1	0	#0	#1	2	1	0	ROM 0	ROM 1
F0000-FFFFF	1	1	1	1	x	x	x	x	x	0	1	1	1	1	1	
E0000-EFFFF	1	1	1	0	0	1	1	1	1	1	1	x	x	x		
E0000-EFFFF	1	1	1	0	0	1	1	1	0	0	1	1	1	0	2	
E0000-EFFFF	1	1	1	0	0	1	1	0	1	0	1	1	0	1	3	
E0000-EFFFF	1	1	1	0	0	1	1	0	0	0	1	1	0	0	4	
E0000-EFFFF	1	1	1	0	0	1	0	1	1	0	1	0	1	1	5	
E0000-EFFFF	1	1	1	0	0	1	0	1	0	0	1	0	1	0	6	
E0000-EFFFF	1	1	1	0	0	1	0	0	1	0	1	0	0	1	7	
E0000-EFFFF	1	1	1	0	0	1	0	0	0	0	1	0	0	0	8	
E0000-EFFFF	1	1	1	0	0	0	1	1	1	1	0	1	1	1		1
E0000-EFFFF	1	1	1	0	0	0	1	1	0	1	0	1	1	0		2
E0000-EFFFF	1	1	1	0	0	0	1	0	1	1	0	1	0	1		3
E0000-EFFFF	1	1	1	0	0	0	1	0	0	1	0	1	0	0		4
E0000-EFFFF	1	1	1	0	0	0	0	1	1	1	0	0	1	1		5
E0000-EFFFF	1	1	1	0	0	0	0	1	0	1	0	0	1	0		6
E0000-EFFFF	1	1	1	0	0	0	0	0	1	1	0	0	0	1		7
E0000-EFFFF	1	1	1	0	0	0	0	0	0	1	0	0	0	0		8

have this: 512 k ROM on 2 256k chips = 8 64k pages.