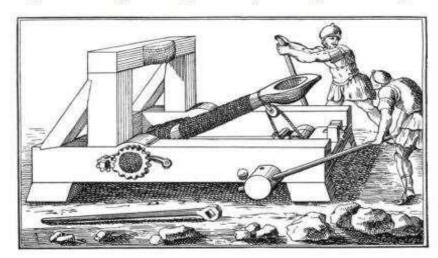






Handout/Assignment-1 for Engineering Design Project-I (UTA013)



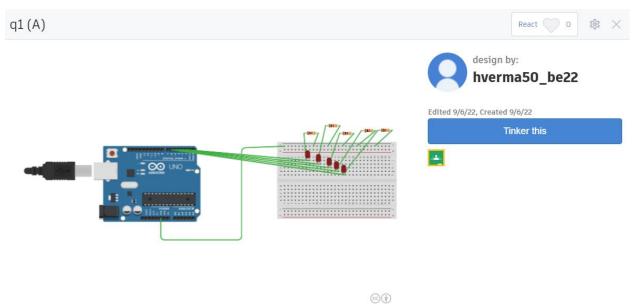
INSTRUCTOR INCHARGE

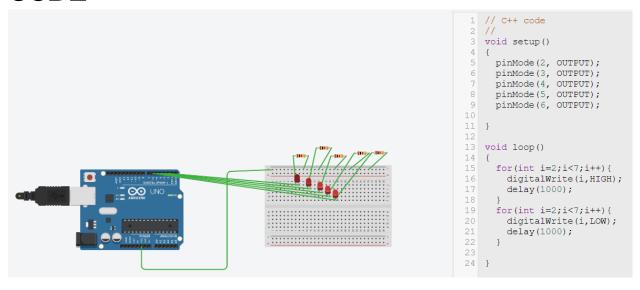
GROUP -2 NAME - Hardik Verma ROLL NO. - 102283044

ASSIGNMENT 1-C

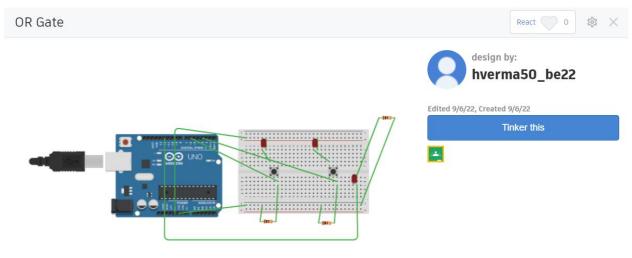
A) Using Tinkercad, hook up 5 LEDs to pins 2 through 6 (with resistors). Modify the code to turn on each one in order and then extinguish them in order

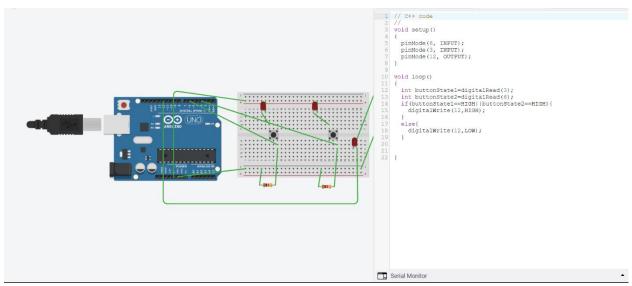
Schematic-



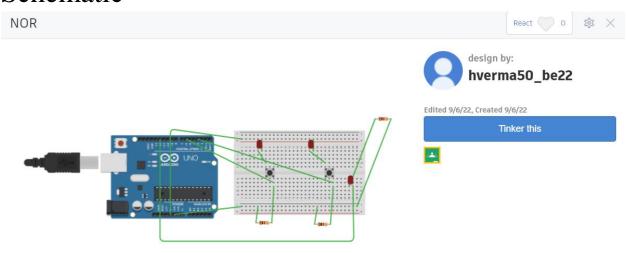


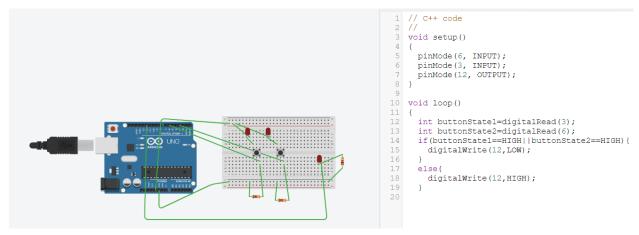
B) With the help of Tinkercad, use push buttons to simulate the behaviour of listed logic gates (Without using ICs of logic gates) (OR gate)
Schematic-



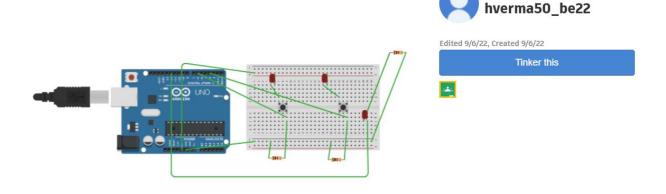


(NOR gate) Schematic-

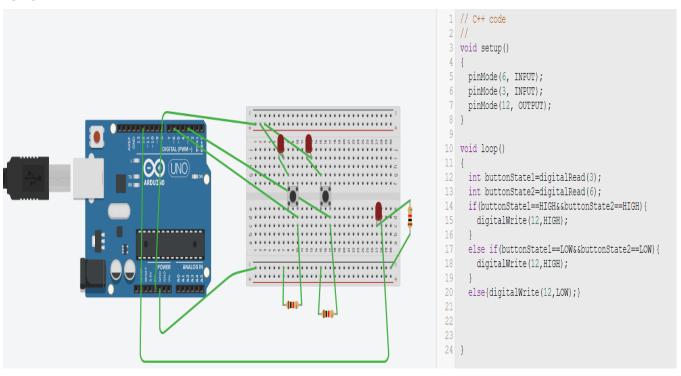




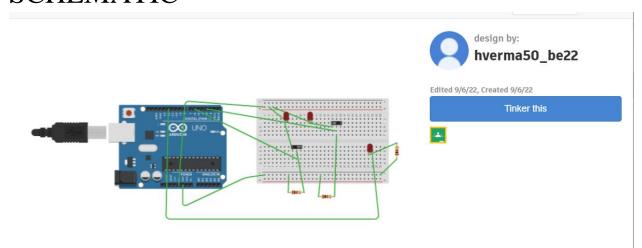
(XNOR gate) Schematic-

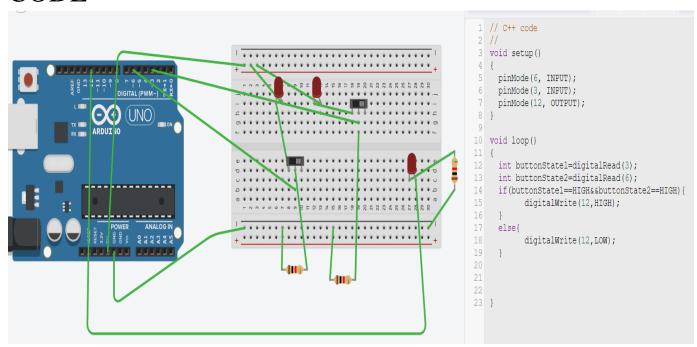


design by:

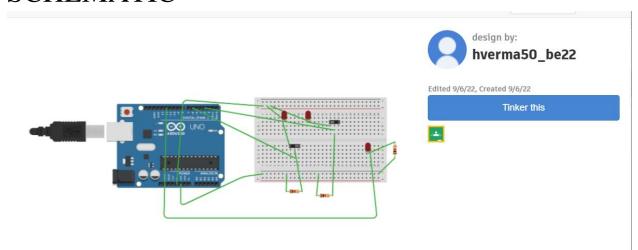


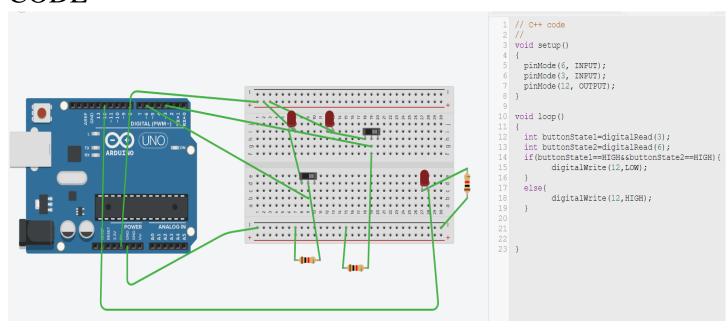
(AND GATE) SCHEMATIC-





(NAND GATE) SCHEMATIC-





(XOR) SCHEMATIC-

