

CONTACT

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SKILLS

Model Order Reduction	1+ yrs
System Identification	1+ yrs
Deep Learning	6+ mon
Python	6+ mon
MATLAB Programming	1+ yrs
Latex	1+ yrs

ACHIEVEMENTS

GATE

Electronics and Communication Engineering
Qualified in 2019 with 407/1000 score in general category.

ASIF HAMID BHAT

Research Scholar - Electrical Engineering

EDUCATION

Ph. D. - Electrical Engineering

2020 - ongoing

Islamic University of Science and Technology, Awantipora, Jammu and Kashmir, India, 192122

Thesis Title: Data-based modeling paradigms for the reduced order modeling of Large-scale dynamical systems

M. Tech. - Control and Instrumentation system, Department of Electrical Engineering

2017 - 2019

Jamia Milia Islamia University, New Delhi, India, 110025,

Passed with **9.3 CGPA**. Thesis title: Implementation of Artificial Neuro-Fuzzy Inference system for the Vector Control of Three-Phase Induction Motor

B. Tech. - Electronics and Communication Engineering.

2012 - 2016

Baba Ghulam Shah Badshah University, Rajouri, Jammu and Kashmir, India, 185234

Passed with **75.6%**. Major project title: MEMS-Based Hand Gesture Controlled Robot

PUBLICATIONS

Discovering low-rank representations of large-scale power-grid models using Koopman theory
Status: Accepted and Presented

IEEE Conference

Power Grid parameter estimation using Sparse Identification of Nonlinear Dynamics
Status: Accepted and Presented

IEEE Conference

Deep learning assisted surrogate modeling of large-scale power-grid networks
Status: Under Review

SCI - IJEPES