

CONTACT

✉ asif.bhat@islamicuniversity.edu.in
in asif-hamid-bhat-184a58208

SKILLS



ACHIEVEMENTS

GATE
Electronics and Communication Engineering
Qualified in 2019 with 407/1000 score in general category.

ASIF HAMID BHAT

Research Scholar - Electrical Engineering

EDUCATION

Ph. D. - Electrical Engineering2020 - ongoing

Islamic University of Science and Technology, Awantipora, Jammu and Kashmir, India, 192122

Thesis Title: Data-based modeling paradigms for the reduced order modeling of Large-scale dynamical systems

M. Tech. - Control and Instrumentation system, Department of Electrical Engineering2017 - 2019

Jamia Milia Islamia University, New Delhi, India, 110025,

Passed with 9.3 CGPA. Thesis title: Implementation of Artificial Neuro-Fuzzy Inference system for the Vector Control of Three-Phase Induction Motor

B. Tech. - Electronics and Communication Engineering.2012 - 2016

Baba Ghulam Shah Badshah University, Rajouri, Jammu and Kashmir, India, 185234

Passed with 75.6%. Major project title; MEMS-Based Hand Gesture Controlled Robot

PUBLICATIONS

- Discovering low-rank representations of large-scale power-grid models using Koopman theory

IEEE Conference

Status: Accepted and Presented
- Power Grid parameter estimation using Sparse Identification of Nonlinear Dynamics

IEEE Conference

Status: Accepted and Presented
- Deep learning assisted surrogate modeling of large-scale power-grid networks

SCI - SEGAN

Status: Accepted
- Hierarchical deep learning based adaptive time stepping for multiscale simulations

NLD Springer

Status: under review