cnn_system:1 ram_templates cnn_processor:1 cnn_alu cnn_state_machine b_line(143:0) bram_address(15:0) i_line(15:0) bram_data_in(15:0) bram_data_in(15:0) **TEMPLATES** u_line(143:0) x_line(143:0) x_new(15:0) ALU ram_generic STATE_MACHINE douta(15:0) bram_data_out(15:0) template data out(15:0) RAM **PROCESSOR** cnn_ram_output

RAM_OUTPUT_SWITCH

<u>dat</u>a(15:0)