,,,,,,	ct Timeline   Task Name	Duration	Start	Finish	Resource Names
1	Begin Project	16 days	Mon 12/16/13	Mon 1/6/14	
2	Initial Team Meeting	1 day	Mon 12/16/13	Mon 12/16/13	
3	Conduct Research and Project Discussions	10 days	Mon 12/16/13	Fri 12/27/13	
4	Write Proposal	4 days	Fri 12/27/13	Wed 1/1/14	Scott Lawson
5	Review Proposal	3 days	Thu 1/2/14	Mon 1/6/14	
6	Present Proposal	0 days	Mon 1/6/14	Mon 1/6/14	
7	Research Phase	15 days	Mon 1/6/14	Fri 1/24/14	
8	Populate Timeline	8 days	Wed 1/8/14	Fri 1/17/14	
9	Write Initial Draft	5 days	Wed 1/8/14	Tue 1/14/14	Max Cope
10	Review	0 days	Tue 1/14/14	Tue 1/14/14	
11	Write Final Draft	3 days	Wed 1/15/14	Fri 1/17/14	Max Cope
12	Software Guidelines	8 days	Mon 1/6/14	Wed 1/15/14	
13	Write Initial Draft	5 days	Mon 1/6/14	Fri 1/10/14	Scott Lawson
14	Review	0 days	Fri 1/10/14	Fri 1/10/14	
15	Write Final Draft	3 days	Mon 1/13/14	Wed 1/15/14	Scott Lawson
16	Sensors Specification Research	13 days	Mon 1/6/14	Wed 1/22/14	
17	Conduct Initial Research	10 days	Mon 1/6/14	Fri 1/17/14	Bryan Button, Max Cope
18	Review Results	0 days	Fri 1/17/14	Fri 1/17/14	
19	Conduct Follow-Up Research	3 days	Mon 1/20/14	Wed 1/22/14	Bryan Button, Max Cope
20	Power Specification Research	13 days	Mon 1/6/14	Wed 1/22/14	
21	Conduct Initial Research	10 days	Mon 1/6/14	Fri 1/17/14	Chris Clary
22	Review Results	0 days	Fri 1/17/14	Fri 1/17/14	
23	Conduct Follow-Up Research	3 days	Mon 1/20/14	Wed 1/22/14	Chris Clary
24	Microcontroller Research	15 days	Mon 1/6/14	Fri 1/24/14	
25	Conduct Initial Research	10 days	Mon 1/6/14	Fri 1/17/14	Scott Lawson
26	Review Results	0 days	Fri 1/17/14	Fri 1/17/14	
27	Conduct Follow-Up Research	5 days	Mon 1/20/14	Fri 1/24/14	Scott Lawson
28	Documentation Construction	5 days	Mon 1/27/14	Fri 1/31/14	
29	Create Initial Design Spec	5 days	Mon 1/27/14	Fri 1/31/14	Max Cope, Scott Lawson
30	Update Proposal	5 days	Mon 1/27/14	Fri 1/31/14	Bryan Button, Chris Clary
31	Design Review One	5 days	Sat 2/1/14	Thu 2/6/14	

lojec	t Timeline  Task Name	Duration	Start	Finish	Resource Names
32	Implement Design Review Feedback	5 days	Sat 2/1/14	Thu 2/6/14	
33	Breadboard Build	63 days	Sat 2/1/14	Tue 4/29/14	
34	Hardware	15 days	Sat 2/1/14	Thu 2/20/14	
35	Sensor Module Build	5 days	Sat 2/1/14	Thu 2/6/14	
36	Signal Conditioning R&D	5 days	Sat 2/1/14	Thu 2/6/14	Max Cope
37	Circuit Protection R&D	5 days	Sat 2/1/14	Thu 2/6/14	Max Cope
38	Power Build	10 days	Fri 2/7/14	Thu 2/20/14	
39	Research Power Demands	5 days	Fri 2/7/14	Thu 2/13/14	Bryan Button
40	Research Battery Life Considerations	5 days	Fri 2/14/14	Thu 2/20/14	Bryan Button
41	Schematic Design	14 days	Fri 2/7/14	Wed 2/26/14	Chris Clary
42	Create 1st revision of Layout	15 days	Thu 2/27/14	Wed 3/19/14	Chris Clary
43	Firmware Development	29 days	Fri 2/7/14	Wed 3/19/14	
44	Implement PSoC Components	18 days	Fri 2/7/14	Tue 3/4/14	
45	USBUART	4 days	Fri 2/7/14	Wed 2/12/14	Scott Lawson
46	ADC	2 days	Thu 2/13/14	Fri 2/14/14	Scott Lawson
47	EEPROM	4 days	Mon 2/17/14	Thu 2/20/14	Scott Lawson
48	Emulated EEPROM	4 days	Fri 2/21/14	Wed 2/26/14	Scott Lawson
49	Real-time Clock	4 days	Thu 2/27/14	Tue 3/4/14	Scott Lawson
50	Interrupt Configuration	1 day	Wed 3/5/14	Wed 3/5/14	Scott Lawson
51	Sleep States	10 days	Thu 3/6/14	Wed 3/19/14	
52	Research Sleep States	5 days	Thu 3/6/14	Wed 3/12/14	Scott Lawson, Bryan Buttor
53	Implement Sleep States	5 days	Thu 3/13/14	Wed 3/19/14	Scott Lawson, Bryan Buttor
54	Assemble System	38 days	Wed 2/26/14	Mon 4/21/14	
55	Order BOM	0 days	Wed 2/26/14	Wed 2/26/14	Chris Clary
56	Wait on Part Arrival	23 days	Thu 2/27/14	Mon 3/31/14	
57	Breadboard Assembly	4 days	Tue 4/1/14	Fri 4/4/14	Max Cope
58	Breadboard Debugging & Review	11 days	Mon 4/7/14	Mon 4/21/14	
59	Functional Prototype Demonstration	0 days	Mon 4/21/14	Mon 4/21/14	
60	Internal Prototype Assesment	6 days	Tue 4/22/14	Tue 4/29/14	
61	Design Review Two	5 days	Tue 4/22/14	Mon 4/28/14	
62	Implement Design Review Feedback	5 days	Tue 4/22/14	Mon 4/28/14	

0,000	Timeline  Task Name	Duration	Start	Finish	Resource Names
63	PCB Build	27 days	Tue 4/29/14	Tue 6/3/14	
64	Final Orders	15 days	Tue 4/29/14	Mon 5/19/14	
65	Review BOM	5 days	Tue 4/29/14	Mon 5/5/14	Chris Clary, Max Cope
66	Order BOM	0 days	Mon 5/5/14	Mon 5/5/14	Chris Clary
67	Await Parts Arrival	12 days	Tue 4/29/14	Wed 5/14/14	
68	Create 2nd Revision of Layout	10 days	Tue 5/6/14	Mon 5/19/14	Chris Clary, Max Cope
69	Order PCBs	0 days	Mon 5/19/14	Mon 5/19/14	Chris Clary
70	Await PCB Arrival	12 days	Tue 4/29/14	Wed 5/14/14	
71	Create Python UI	26 days	Tue 4/29/14	Mon 6/2/14	
72	Establish Serial Communication	4 days	Tue 4/29/14	Fri 5/2/14	Scott Lawson
73	Create Sensor Class	5 days	Mon 5/5/14	Fri 5/9/14	Scott Lawson
74	Layout GUI	2 days	Mon 5/12/14	Tue 5/13/14	Scott Lawson
75	Implement GUI Layout	10 days	Wed 5/14/14	Tue 5/27/14	Scott Lawson
76	Test and Debug GUI	5 days	Wed 5/28/14	Mon 6/2/14	Scott Lawson
77	Device Housing	11 days	Tue 4/29/14	Tue 5/13/14	
78	Research Housing Options	3 days	Tue 4/29/14	Thu 5/1/14	Bryan Button
79	Construct Box Prototype	4 days	Fri 5/2/14	Wed 5/7/14	Bryan Button
80	Review Box Suitability	4 days	Thu 5/8/14	Tue 5/13/14	
81	Final Construction	15 days	Thu 5/15/14	Tue 6/3/14	
82	Populate PCBs	10 days	Thu 5/15/14	Wed 5/28/14	
83	Debug PCB Assembly	5 days	Thu 5/29/14	Tue 6/3/14	
84	Construct Device Boxes	7 days	Thu 5/15/14	Fri 5/23/14	
85	Assemble PCBs and Boxes	5 days	Mon 5/26/14	Fri 5/30/14	
86	Project Conclusion	16 days	Mon 5/19/14	Fri 6/6/14	
87	Documentation Generation	15 days	Mon 5/19/14	Thu 6/5/14	
88	Final Poster Design	8 days	Mon 5/19/14	Wed 5/28/14	Bryan Button
89	Submit Poster	0 days	Wed 5/28/14	Wed 5/28/14	Bryan Button
90	Create Final Report	5 days	Thu 5/29/14	Tue 6/3/14	Bryan Button
91	Create Powerpoint Presentation	2 days	Wed 6/4/14	Thu 6/5/14	Bryan Button
92	Create Test Plan	10 days	Thu 5/22/14	Tue 6/3/14	Max Cope
93	Convert Design Spec to Technical Manual	5 days	Thu 5/29/14	Tue 6/3/14	Scott Lawson

Project	:Timeline				
ID	Task Name	Duration	Start	Finish	Resource Names
94	Create User Guide	8 days	Mon 5/26/14	Tue 6/3/14	Chris Clary
95	Project Presentations	2 days	Thu 6/5/14	Fri 6/6/14	
96	Presentation Practice	1 day	Thu 6/5/14	Thu 6/5/14	
97	Presentation	1 day	Fri 6/6/14	Fri 6/6/14	
98					