	T15-PROJECT SCHEDULE						
ID	Task Mode	Task Name	Duration	Start	Finish		
1	-5	1 Plan	106.38 days	?Mon 11/10/14	Tue 2/24/15		
2	*	1.1 Teaming up and Meeting with Advisor	3.33 days	Mon 11/10/14	Thu 11/20/14		
3	-	1.2 Understanding the project and its requirements	15.56 days	Mon 11/10/14	Wed 1/7/15		
4	-5	1.3 Meeting with sponsors	0.33 days	Thu 12/4/14	Thu 12/4/14		
5	-5	1.4 Reviewing the existing design and gathering info about the requirements	8.67 days	Fri 12/5/14	Thu 1/8/15		
6	-5	1.5 Meeting with sponsors	0.33 days?	Thu 1/8/15	Thu 1/8/15		
7	-5	1.6 Concept Generation and Evaluation	0.33 days?	Fri 1/9/15	Fri 1/9/15		
8	-5	1.7 Write up the preliminary project proposal	0.67 days	Sun 1/11/15	Tue 1/13/15		
9	-5	1.8 Review project proposal	1 day	Wed 1/14/15	Fri 1/16/15		
10	-5	1.9 Present Project Proposal	1 hr	Tue 2/24/15	Tue 2/24/15		
11	-5	1.10 Develop project requirement	47.38 days	Thu 1/8/15	Tue 2/24/15		
12	-5	1.10.1 Develop the general project requirement	3.67 days	Thu 1/8/15	Tue 1/20/15		
13	-5	1.10.2 Write up the detail project requirement	2 days	Sun 1/25/15	Wed 1/28/15		
14	-5	1.10.3 Review project requirement document	1 day	Mon 2/2/15	Tue 2/3/15		
15	-5	1.10.4 Gain Sponsor approval of requirements	0 days	Tue 2/24/15	Tue 2/24/15		
16		2 Design	75.33 days?	Fri 1/9/15	Wed 3/25/15		

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		T15-PROJECT SCHEDULE						
Task Mode	Task Name	Duration	Start	Finish				
-3	2.1 Determine the necessary functional blocks	4.33 days	Fri 1/9/15	Fri 1/23/15				
-5	2.2 Review the block layout	0.33 days	Sun 1/25/15	Mon 1/26/15				
-9	2.3 Determine the design concept of each block: problem to solve and available technology	1.71 days	Sun 2/1/15	Fri 2/6/15				
-5	2.4 Write the test plan	2 days	Wed 2/4/15	Fri 2/6/15				
-5	2.5 Review the test plan	0.33 days	Fri 2/6/15	Sat 2/7/15				
-5	2.6 Design the preliminary circuitry	2.67 days	Mon 2/2/15	Wed 2/11/15				
-5	2.7 Review the circuit design	1 day	Thu 2/12/15	Sat 2/14/15				
-5	2.8 Generate the circuitry schematic and PCB layout on Eagle CAD	1.67 days	Mon 2/16/15	Fri 2/20/15				
-3	2.9 Review and revise preliminary design	0.33 days	Fri 2/20/15	Sat 2/21/15				
-3	2.10 Finalize the preliminary schematic and PCB design	2 days	Wed 3/11/15	Wed 3/18/15				
-5	2.11 Review the design with the sponsors	0.67 days?	Sat 3/21/15	Mon 3/23/15				
-5	2.12 Specify the component for the design	7 days?	Fri 2/6/15	Fri 2/27/15				
-9	2.13 Place order for the components	1 day?	Mon 3/23/15	Wed 3/25/15				
-5	3 Build	101.38 days?	Thu 1/15/15	Sun 4/26/15				
-5	3.1 User Interface	28.38 days?	Thu 3/19/15	Thu 4/16/15				
-3	3.1.1 Finalize configuration parameters	6.38 days?	Thu 3/19/15	Wed 3/25/15				
	Aode	2.1 Determine the necessary functional blocks 2.2 Review the block layout 2.3 Determine the design concept of each block: problem to solve and available technology 2.4 Write the test plan 2.5 Review the test plan 2.6 Design the preliminary circuitry 2.7 Review the circuit design 2.8 Generate the circuitry schematic and PCB layout on Eagle CAD 2.9 Review and revise preliminary design 2.10 Finalize the preliminary schematic and PCB design 2.11 Review the design with the sponsors 2.12 Specify the component for the design 2.13 Place order for the components 3 Build 3.1 User Interface	2.1 Determine the necessary functional blocks 2.2 Review the block layout 2.3 Determine the design concept of each block: problem to solve and available technology 2.4 Write the test plan 2.5 Review the test plan 2.6 Design the preliminary circuitry 2.7 Review the circuit design 2.8 Generate the circuitry schematic and PCB layout on Eagle CAD 2.9 Review and revise preliminary design 2.10 Finalize the preliminary schematic and PCB design 2.11 Review the design with the sponsors 2.12 Specify the component for the design 3 Build 3.1 User Interface 28.38 days?	2.1 Determine the necessary functional blocks 2.2 Review the block layout 2.3 Determine the design concept of each block: problem to solve and available technology 2.4 Write the test plan 2.5 Review the test plan 2.6 Design the preliminary circuitry 2.7 Review the circuit design 2.8 Generate the circuitry schematic and PCB layout on Eagle CAD 2.9 Review and revise preliminary schematic and PCB design 2.10 Finalize the preliminary schematic and PCB design 2.11 Review the design with the sponsors 2.12 Specify the component for the design 3 Build 3.1 User Interface 2.8 Ganerate the circuitrate 2.10 Finalize the preliminary schematic and PCB design 3.1 User Interface 2.8 Build 3.1 User Interface 3.8 Build 3.1 User Interface 3.8 June 2.11 Specify the component for the design 3.1 User Interface 3.8 June 2.11 Specify Thu 3/19/15	2.1 Determine the necessary functional blocks 2.1 Determine the necessary functional blocks 3.2 Review the block layout 3.3 days 3.1 Lyser Interface 3.3 days 4.33 days 5ri 1/9/15 5ri 1/23/15 5ri 1/23/15 5ri 1/23/15 5ri 1/26/15 5ri 2/6/15 5ri 2/20/15			

T15-PROJECT SCHEDULE						3 13
ID	Task Mode	Task Name	Duration	Start	Finish	
33	-5	3.1.2 Load sensor type definitions from file	4.38 days?	Wed 3/25/15	Sun 3/29/15	
34	-5	3.1.3 CSV	7.38 days?	Mon 3/30/15	Mon 4/6/15	
35		3.1.3.1 write sensor -> CSV encoding	5.38 days?	Mon 3/30/15	Sat 4/4/15	
36	-5	3.1.3.2 write CSV -> sensor decoding	3.38 days?	Fri 4/3/15	Mon 4/6/15	
37	*	3.1.4 Add filesystem functions	19.38 days	Sat 3/28/15	Thu 4/16/15	
38	-5	3.1.4.1 Save configurator	3.38 days?	Tue 4/7/15	Fri 4/10/15	
39	-5	3.1.4.2 Load configurator	3.38 days?	Tue 4/7/15	Fri 4/10/15	
40	-5	3.1.5 Add serial functionality	5.38 days?	Sat 4/11/15	Thu 4/16/15	
41	-	3.2 Hack the serial IAP driver	10.38 days?	Thu 4/16/15	Sun 4/26/15	
42	-	3.2.1 Add a menu option for downloading a configuration	2.38 days?	Thu 4/16/15	Sat 4/18/15	
43	-5	3.2.2 Implement functionality of downloading the configurator	2.38 days?	Sat 4/18/15	Mon 4/20/15	
44	-5	3.2.3 Parse the configurator into an array of sensor structs	2.38 days?	Tue 4/21/15	Thu 4/23/15	
45	-5	3.2.4 Save to flash	2.38 days?	Fri 4/24/15	Sun 4/26/15	
46	-5	3.3 Program the MCU	20.33 days?	Thu 1/15/15	Tue 4/7/15	
47		3.4 Send the boards to fab	4 days?	Mon 3/23/15	Tue 4/7/15	
48		4 Working on the poster	60.38 days?	Wed 4/1/15	Sun 5/31/15	

	T15-PROJECT SCHEDULE					
ID	Task Mode	Task Name	Duration	Start	Finish	
49	-5	5 Test	38.38 days?	Wed 4/8/15	Sat 5/16/15	
50	-5	5.1 Debug the codes	5 days?	Wed 4/8/15	Wed 4/22/15	
51		5.2 Build the system on the PCB	1.67 days	Fri 4/10/15	Thu 4/16/15	
52	-5	5.3 Test the hardware	4.56 days	Fri 4/10/15	Fri 4/24/15	
53	-5	5.4 Test the Software	4.33 days?	Fri 4/10/15	Fri 4/24/15	
54	-5	5.5 Test the GUI	1.67 days?	Mon 4/27/15	Wed 4/29/15	
55	-	5.6 Integration test	1.67 days?	Thu 4/30/15	Mon 5/4/15	
56	-5	5.7 Functionality test	1.67 days?	Tue 5/5/15	Thu 5/7/15	
57	-5	5.8 Design and build the device closure (box)	6.33 days	Fri 4/17/15	Sun 5/10/15	
58	-5	5.9 Perform the final test	17.96 days	Tue 4/28/15	Sat 5/16/15	
59	-5	6 Write the operation manual	5.46 days	Sun 11/9/14	Sat 11/15/14	
60	-5	7 Review the operation manual	3.17 days	Wed 5/13/15	Tue 5/26/15	
61	*	8 Present	26.38 days	Sat 5/9/15	Thu 6/4/15	
62		8.1 Complete the presentation documents (slides)	5.67 days	Sat 5/9/15	Fri 5/29/15	
63	-5	8.2 Review the presentation documents	0.33 days	Fri 5/29/15	Mon 6/1/15	
64	-5	8.3 Final presentation of the system	1.33 days	Mon 6/1/15	Thu 6/4/15	

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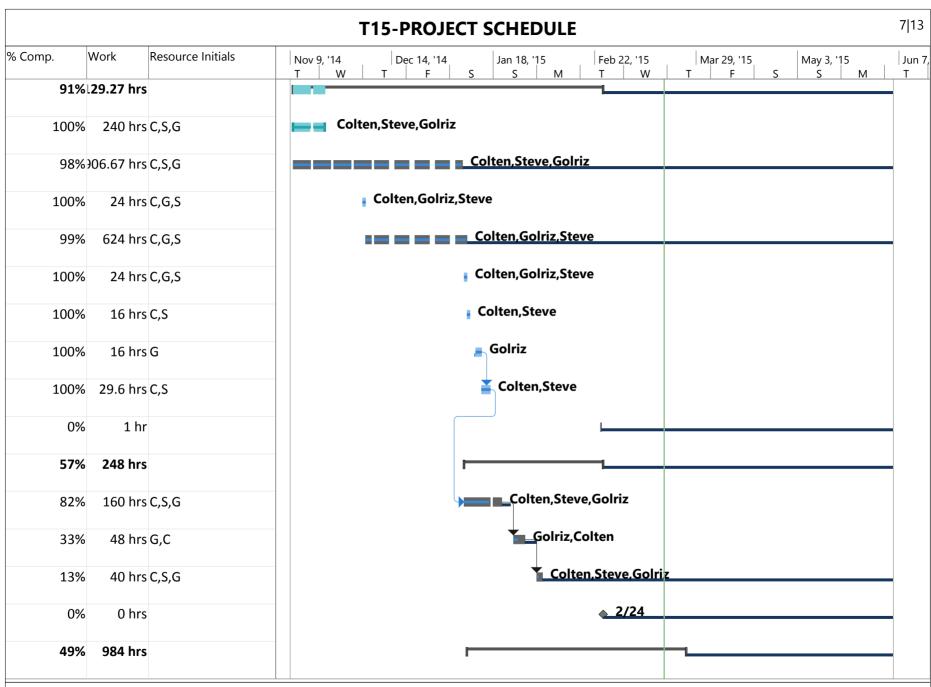
D	Task Mode	Task Name	Duration	Start	Finish
65	-5	9 WPR	203.01 days	Thu 11/13/14	Thu 6/4/15
66	-9	9.1 WPR 1	15 mins	Thu 11/13/14	Thu 11/13/14
67	-5	9.2 WPR 2	15 mins	Thu 11/20/14	Thu 11/20/14
68	-5	9.3 WPR 3	15 mins	Thu 11/27/14	Thu 11/27/14
69	-5	9.4 WPR 4	15 mins	Thu 12/4/14	Thu 12/4/14
70	-5	9.5 WPR 5	15 mins	Thu 12/11/14	Thu 12/11/14
71	-5	9.6 WPR 6	15 mins	Thu 12/18/14	Thu 12/18/14
72	-5	9.7 WPR 7	15 mins	Thu 12/25/14	Thu 12/25/14
73	-9	9.8 WPR 8	15 mins	Thu 1/1/15	Thu 1/1/15
74	-9	9.9 WPR 9	15 mins	Thu 1/8/15	Thu 1/8/15
75	-9	9.10 WPR 10	15 mins	Thu 1/15/15	Thu 1/15/15
76	-9	9.11 WPR 11	15 mins	Thu 1/22/15	Thu 1/22/15
77	-5	9.12 WPR 12	15 mins	Thu 1/29/15	Thu 1/29/15
78	-5	9.13 WPR 13	15 mins	Thu 2/5/15	Thu 2/5/15
79	-9	9.14 WPR 14	15 mins	Thu 2/12/15	Thu 2/12/15
80		9.15 WPR 15	15 mins	Thu 2/19/15	Thu 2/19/15

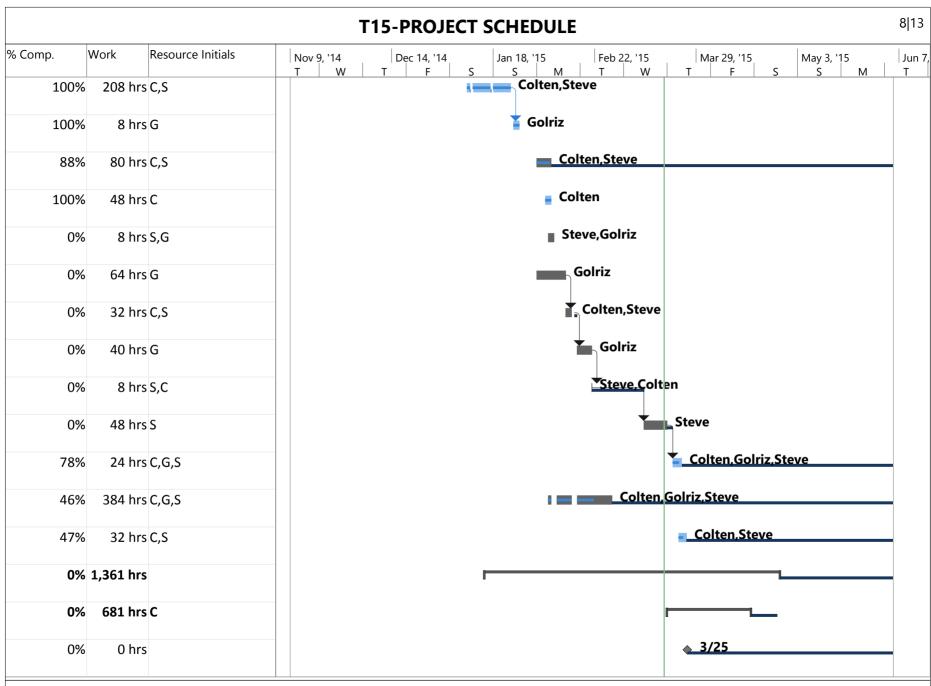
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		T15-PRO	JECT SCHEDULE		
ID	Task Mode	Task Name	Duration	Start	Finish
81	-5	9.16 WPR 16	15 mins	Thu 2/26/15	Thu 2/26/15
82	-5	9.17 WPR 17	15 mins	Thu 3/5/15	Thu 3/5/15
83	-5	9.18 WPR 18	15 mins	Thu 3/12/15	Thu 3/12/15
84	-5	9.19 WPR 19	15 mins	Thu 3/19/15	Thu 3/19/15
85	-5	9.20 WPR 20	15 mins	Thu 3/26/15	Thu 3/26/15
86	-5	9.21 WPR 21	15 mins	Thu 4/2/15	Thu 4/2/15
87	-5	9.22 WPR 22	15 mins	Thu 4/9/15	Thu 4/9/15
88	-5	9.23 WPR 23	15 mins	Thu 4/16/15	Thu 4/16/15
89	-5	9.24 WPR 24	15 mins	Thu 4/23/15	Thu 4/23/15
90	-5	9.25 WPR 25	15 mins	Thu 4/30/15	Thu 4/30/15
91	-5	9.26 WPR 26	15 mins	Thu 5/7/15	Thu 5/7/15
92	-5	9.27 WPR 27	15 mins	Thu 5/14/15	Thu 5/14/15
93	-5	9.28 WPR 28	15 mins	Thu 5/21/15	Thu 5/21/15
94	-5	9.29 WPR 29	15 mins	Thu 5/28/15	Thu 5/28/15
95	-5	9.30 WPR 30	15 mins	Thu 6/4/15	Thu 6/4/15

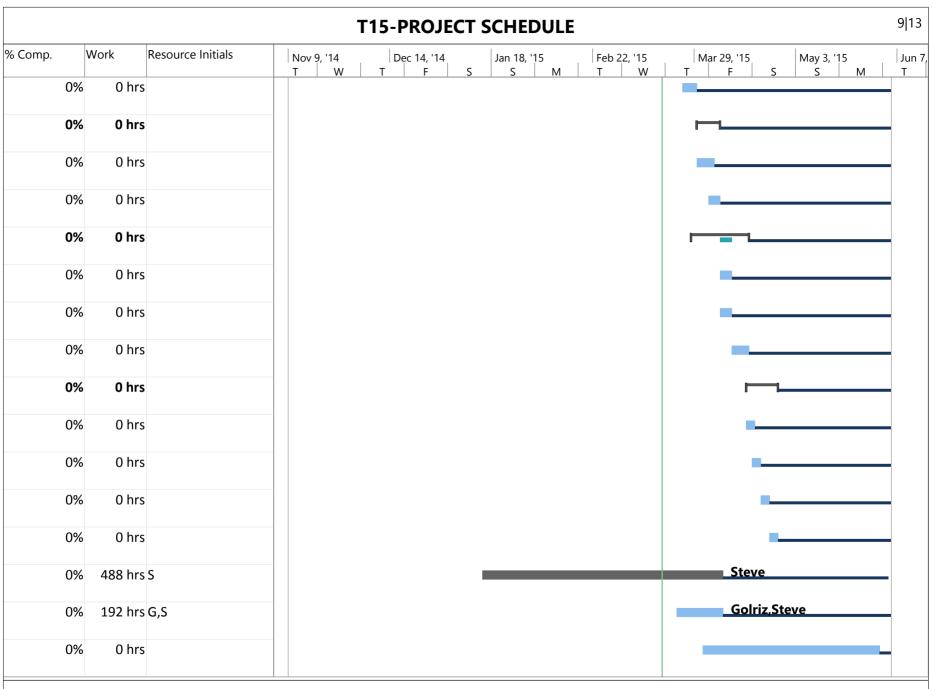
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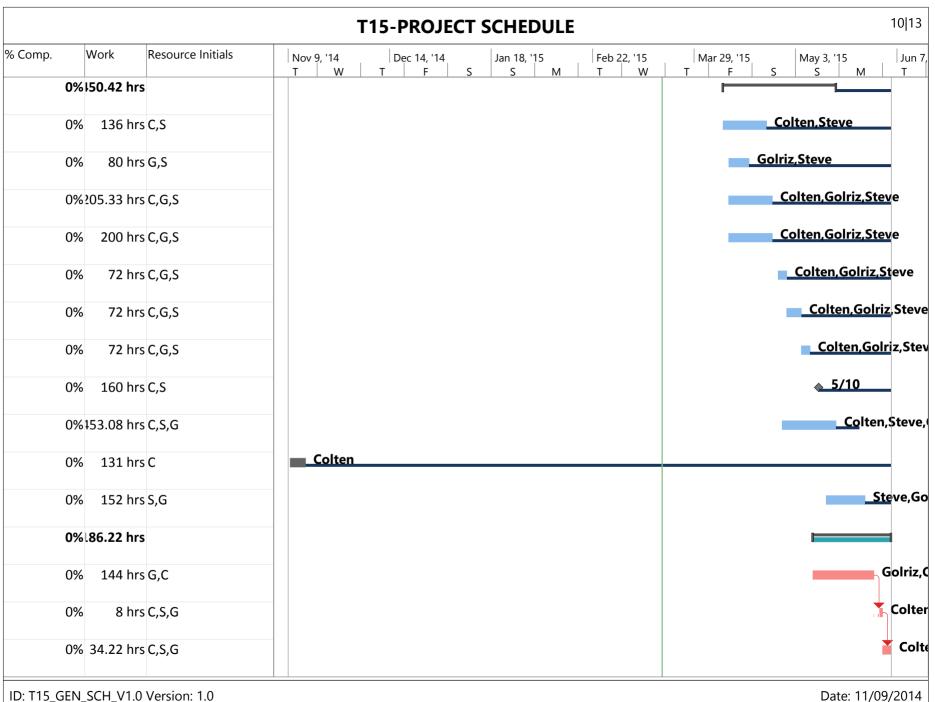
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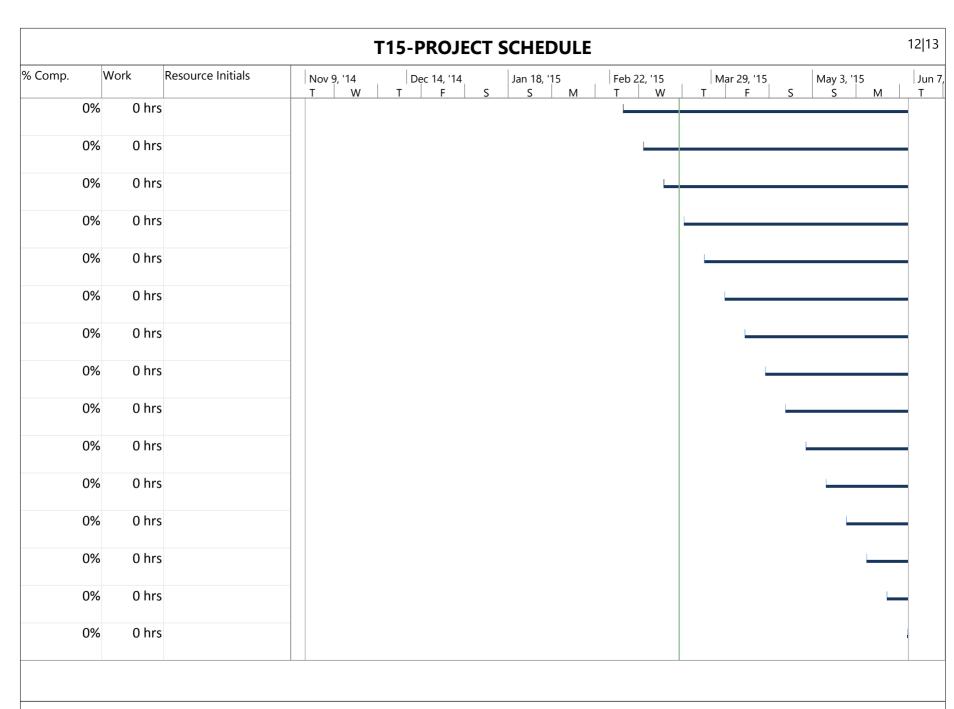


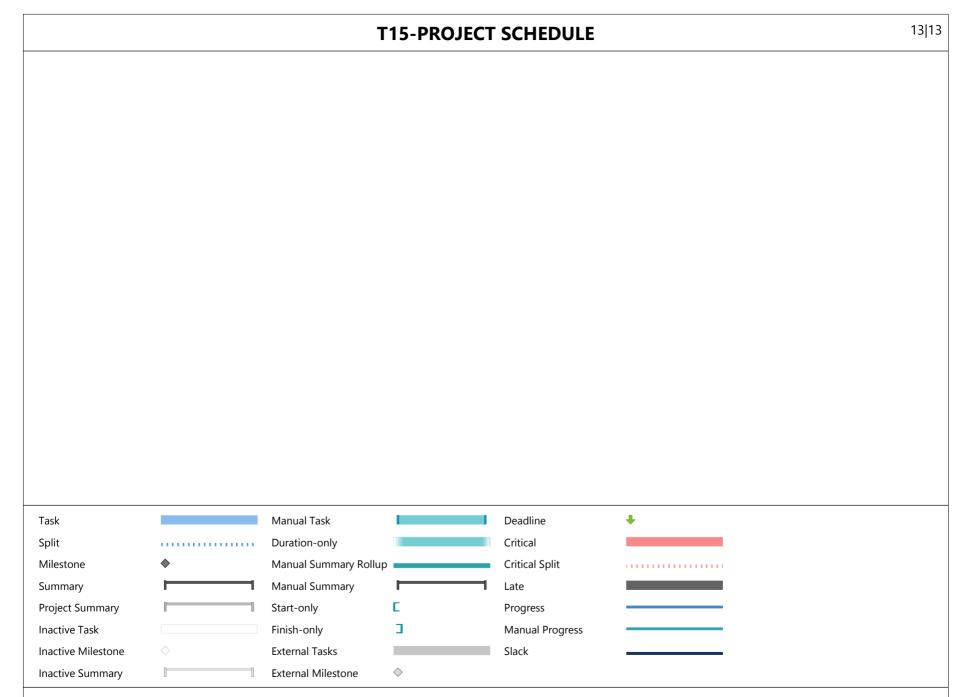
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			T15-PROJECT SCHEDULE	11 13
% Comp.	Work	Resource Initials	Nov 9, '14	Jun 7
37%	0 hrs			
100%	0 hrs			
100%	0 hrs			
100%	0 hrs			
100%	0 hrs			
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