

































T15-PROJECT SCHEDULE

1|15

ID	Task Mode	Task Name	Duration	Start	Finish
1		1 Plan	106.38 days	Mon 11/10/14	Tue 2/24/15
2		1.1 Teaming up and Meeting with Advisor	4 days	Mon 11/10/14	Fri 11/21/14
3		1.2 Understanding the project and its requirements	16 days	Mon 11/10/14	Thu 1/8/15
4		1.3 Meeting with sponsors	1 day	Thu 12/4/14	Fri 12/5/14
5		1.4 Reviewing the existing design and gathering info about the requirements	9 days	Fri 12/5/14	Fri 1/9/15
6		1.5 Meeting with sponsors	1 day	Thu 1/8/15	Fri 1/9/15
7		1.6 Concept Generation and Evaluation	1 day	Fri 1/9/15	Sun 1/11/15
8		1.7 Write up the preliminary project proposal	1 day	Sun 1/11/15	Wed 1/14/15
9		1.8 Review project proposal	1 day	Wed 1/14/15	Fri 1/16/15
10		1.9 Present Project Proposal	1 hr	Tue 2/24/15	Tue 2/24/15
11		1.10 Develop project requirement	47.38 days	Thu 1/8/15	Tue 2/24/15
12		1.10.1 Develop the general project requirement	4 days	Thu 1/8/15	Wed 1/21/15
13		1.10.2 Write up the detail project requirement	2 days	Sun 1/25/15	Wed 1/28/15
14		1.10.3 Review project requirement document	1 day	Mon 2/2/15	Tue 2/3/15
15		1.10.4 Gain Sponsor approval of requirements	1 day	Mon 2/23/15	Tue 2/24/15
16		2 Circuit Design	75.33 days	Fri 1/9/15	Wed 3/25/15

















T15-PROJECT SCHEDULE

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ID	Task Mode	Task Name	Duration	Start	Finish
17		2.1 Determine the necessary functional blocks	5 days	Fri 1/9/15	Sun 1/25/15
18		2.2 Review the block layout	1 day	Sun 1/25/15	Wed 1/28/15
19		2.3 Determine the design concept of each block: problem to solve and available technology	2 days	Sun 2/1/15	Mon 2/9/15
20		2.4 Write the test plan	2 days	Wed 2/4/15	Fri 2/6/15
21		2.5 Review the test plan	1 day	Fri 2/6/15	Tue 2/10/15
22		2.6 Design the preliminary circuitry	3 days	Fri 1/30/15	Wed 2/11/15
23		2.7 Review the circuit design	1 day	Thu 2/12/15	Sat 2/14/15
24		2.8 Generate the circuitry schematic and PCB layout on Eagle CAD	2 days	Mon 2/16/15	Mon 2/23/15
25		2.9 Review and revise preliminary design	1 day	Fri 2/20/15	Tue 2/24/15
26		2.10 Finalize the preliminary schematic and PCB design	2 days	Wed 3/11/15	Wed 3/18/15
27		2.11 Review the design with the sponsors	1 day	Sat 3/21/15	Tue 3/24/15
28		2.12 Specify the component for the design	7 days	Fri 2/6/15	Fri 2/27/15
29		2.13 Place order for the components	1 day	Mon 3/23/15	Wed 3/25/15
30		3 Build User Interface	29 days	Thu 3/19/15	Fri 4/17/15
31		3.1 User Interface	29 days	Thu 3/19/15	Fri 4/17/15
32		3.1.1 Finalize configuration parameters	2 days	Thu 3/19/15	Sat 3/21/15

















T15-PROJECT SCHEDULE

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ID	Task Mode	Task Name	Duration	Start	Finish
33		3.1.2 Load sensor type definitions from file	5 days	Wed 3/25/15	Mon 3/30/15
34		3.1.3 CSV	26.63 days	Sat 3/21/15	Fri 4/17/15
35		3.1.3.1 write (sensor -> CSV) encoding	14 days	Sat 3/21/15	Sat 4/4/15
36		3.1.3.2 write (CSV -> sensor) decoding	14 days	Fri 4/3/15	Fri 4/17/15
37		3.1.4 Add filesystem functions	19.38 days	Sat 3/28/15	Thu 4/16/15
38		3.1.4.1 Save configurator	3 days	Tue 4/7/15	Fri 4/10/15
39		3.1.4.2 Load configurator	3 days	Tue 4/7/15	Fri 4/10/15
40		3.1.5 Add serial functionality	4 days	Sat 4/11/15	Wed 4/15/15
41		4 Write the firmware	4 days	Sun 11/9/14	Sat 11/22/14
42		5 Send the boards to fab	14 days	Mon 3/23/15	Mon 5/18/15
43		6 Working on the poster	48 days	Sun 4/19/15	Sat 6/6/15
44		7 Test	59.33 days	Fri 4/3/15	Tue 6/2/15
45		7.1 Test the hardware	21.63 days	Fri 4/3/15	Sat 4/25/15
46		7.1.1 Inspect the boards and verify the connections	1 day	Fri 4/3/15	Sat 4/4/15
47		7.1.2 Populate the boards	20 days	Sat 4/4/15	Fri 4/24/15
48		7.1.3 Verify soldered connections	2 days	Mon 4/13/15	Wed 4/15/15

















T15-PROJECT SCHEDULE

4|15

ID	Task Mode	Task Name	Duration	Start	Finish
49		7.1.4 Verify power levels, clock signal presence and frequency	4 days	Wed 4/15/15	Sun 4/19/15
50		7.1.5 Upload "Hello World" program on the main board	3 days	Fri 4/17/15	Mon 4/20/15
51		7.1.6 Upload real firmware on the main board	4 days	Tue 4/21/15	Sat 4/25/15
52		7.1.7 Connect sensor boards to "known good" device such as Arduino and verify readings	3 days	Wed 4/22/15	Sat 4/25/15
53		7.2 Integration test	4 days	Sun 4/26/15	Thu 4/30/15
54		7.2.1 Interface main board and ADC sensor	4 days	Sun 4/26/15	Thu 4/30/15
55		7.2.2 Interface I2C sensor board with main board	4 days	Sun 4/26/15	Thu 4/30/15
56		7.3 Hardware full assembly test	7 days	Fri 5/1/15	Fri 5/8/15
57		7.4 Software full assembly test	7 days	Fri 5/1/15	Fri 5/8/15
58		7.5 Design and build the device closure (box)	5 days	Mon 5/11/15	Wed 5/27/15
59		7.6 Alfa testing	3 days	Mon 5/18/15	Thu 5/21/15
60		7.7 Perform the acceptance test	3 days	Thu 5/21/15	Tue 6/2/15
61		8 Write the operation manual	6 days	Sun 11/9/14	Sat 11/15/14
62		9 Review the operation manual	3 days	Thu 5/14/15	Tue 5/26/15
63		10 Present	26.38 days	Sat 5/9/15	Thu 6/4/15
64		10.1 Complete the presentation documents (slides)	6 days	Fri 5/8/15	Fri 5/29/15

















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ID	Task Mode	Task Name	Duration	Start	Finish
65		10.2 Review the presentation documents	1 day	Fri 5/29/15	Wed 6/3/15
66		10.3 Final presentation of the system	2 days	Wed 6/3/15	Wed 6/10/15
67		11 WPR	203.01 days	Thu 11/13/14	Thu 6/4/15
68		11.1 WPR 1	15 mins	Thu 11/13/14	Thu 11/13/14
69		11.2 WPR 2	15 mins	Thu 11/20/14	Thu 11/20/14
70		11.3 WPR 3	15 mins	Thu 11/27/14	Thu 11/27/14
71		11.4 WPR 4	15 mins	Thu 12/4/14	Thu 12/4/14
72		11.5 WPR 5	15 mins	Thu 12/11/14	Thu 12/11/14
73		11.6 WPR 6	15 mins	Thu 12/18/14	Thu 12/18/14
74		11.7 WPR 7	15 mins	Thu 12/25/14	Thu 12/25/14
75		11.8 WPR 8	15 mins	Thu 1/1/15	Thu 1/1/15
76		11.9 WPR 9	15 mins	Thu 1/8/15	Thu 1/8/15
77		11.10 WPR 10	15 mins	Thu 1/15/15	Thu 1/15/15
78		11.11 WPR 11	15 mins	Thu 1/22/15	Thu 1/22/15
79		11.12 WPR 12	15 mins	Thu 1/29/15	Thu 1/29/15
80		11.13 WPR 13	15 mins	Thu 2/5/15	Thu 2/5/15

T15-PROJECT SCHEDULE

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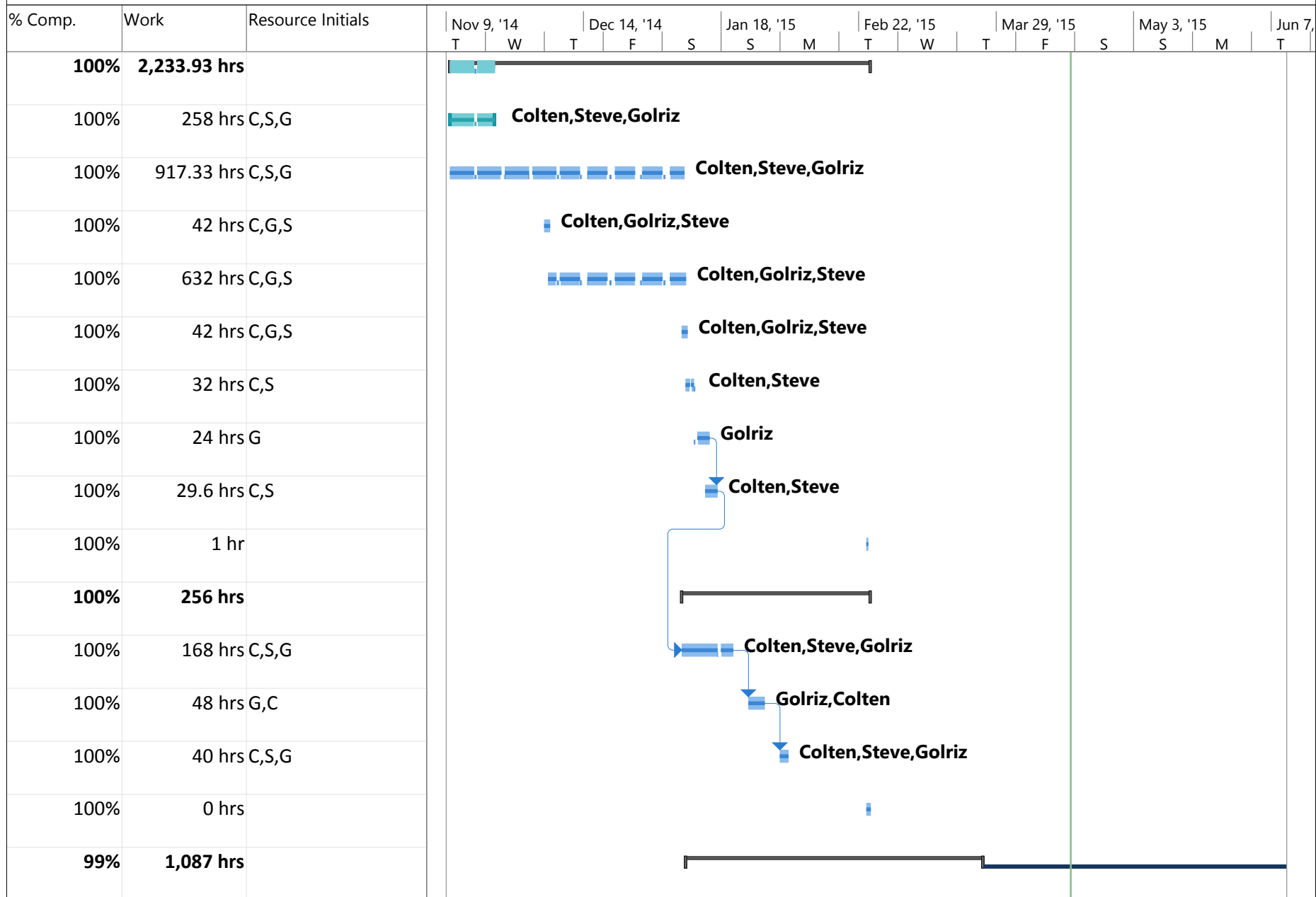
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82		11.15 WPR 15	15 mins	Thu 2/19/15	Thu 2/19/15
83		11.16 WPR 16	15 mins	Thu 2/26/15	Thu 2/26/15
84		11.17 WPR 17	15 mins	Thu 3/5/15	Thu 3/5/15
85		11.18 WPR 18	15 mins	Thu 3/12/15	Thu 3/12/15
86		11.19 WPR 19	15 mins	Thu 3/19/15	Thu 3/19/15
87		11.20 WPR 20	15 mins	Thu 3/26/15	Thu 3/26/15
88		11.21 WPR 21	15 mins	Thu 4/2/15	Thu 4/2/15
89		11.22 WPR 22	15 mins	Thu 4/9/15	Thu 4/9/15
90		11.23 WPR 23	15 mins	Thu 4/16/15	Thu 4/16/15
91		11.24 WPR 24	15 mins	Thu 4/23/15	Thu 4/23/15
92		11.25 WPR 25	15 mins	Thu 4/30/15	Thu 4/30/15
93		11.26 WPR 26	15 mins	Thu 5/7/15	Thu 5/7/15
94		11.27 WPR 27	15 mins	Thu 5/14/15	Thu 5/14/15
95		11.28 WPR 28	15 mins	Thu 5/21/15	Thu 5/21/15
96		11.29 WPR 29	15 mins	Thu 5/28/15	Thu 5/28/15

T15-PROJECT SCHEDULE

ID	Task Mode	Task Name	Duration	Start	Finish
97		11.30 WPR 30	15 mins	Thu 6/4/15	Thu 6/4/15

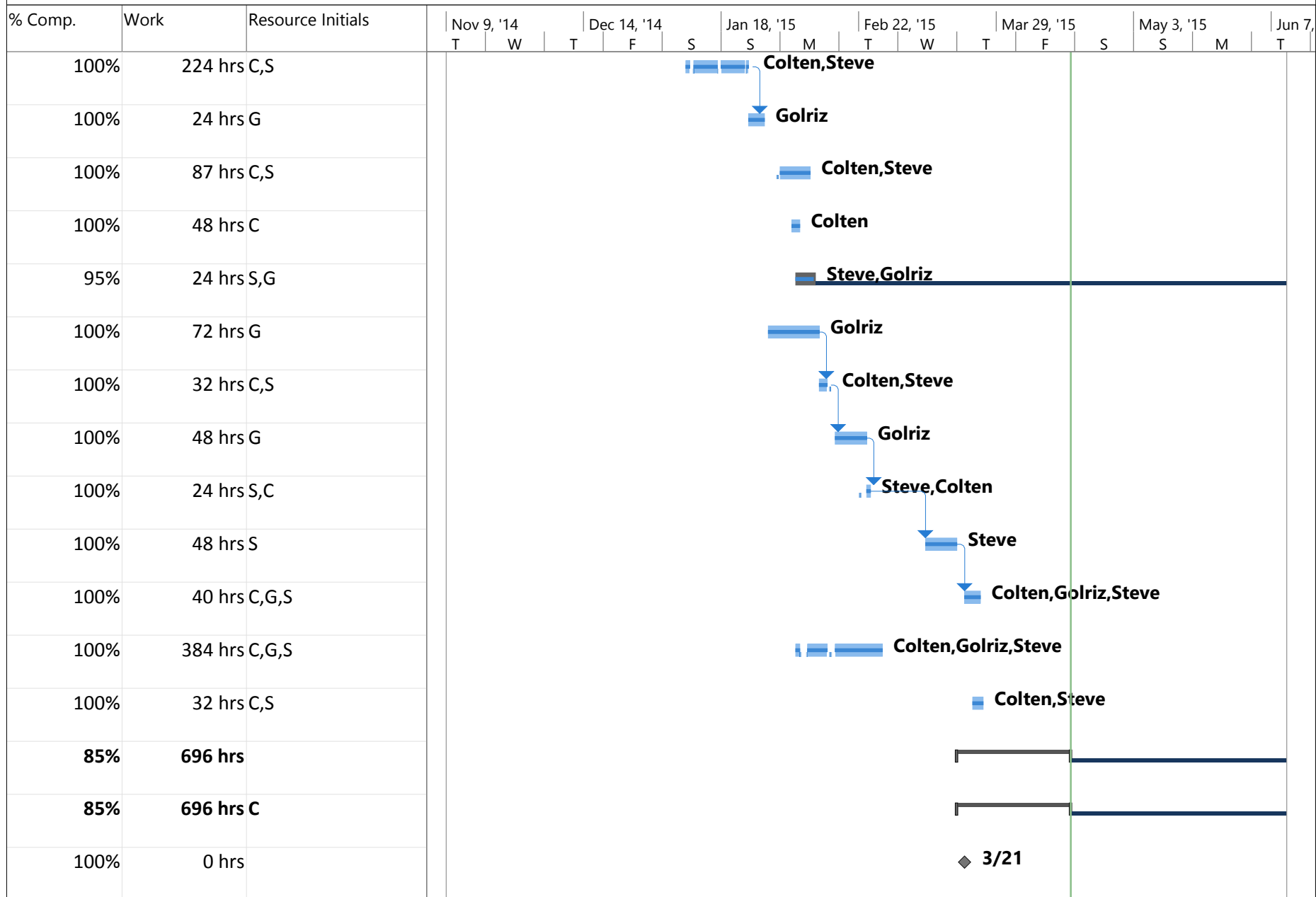
T15-PROJECT SCHEDULE

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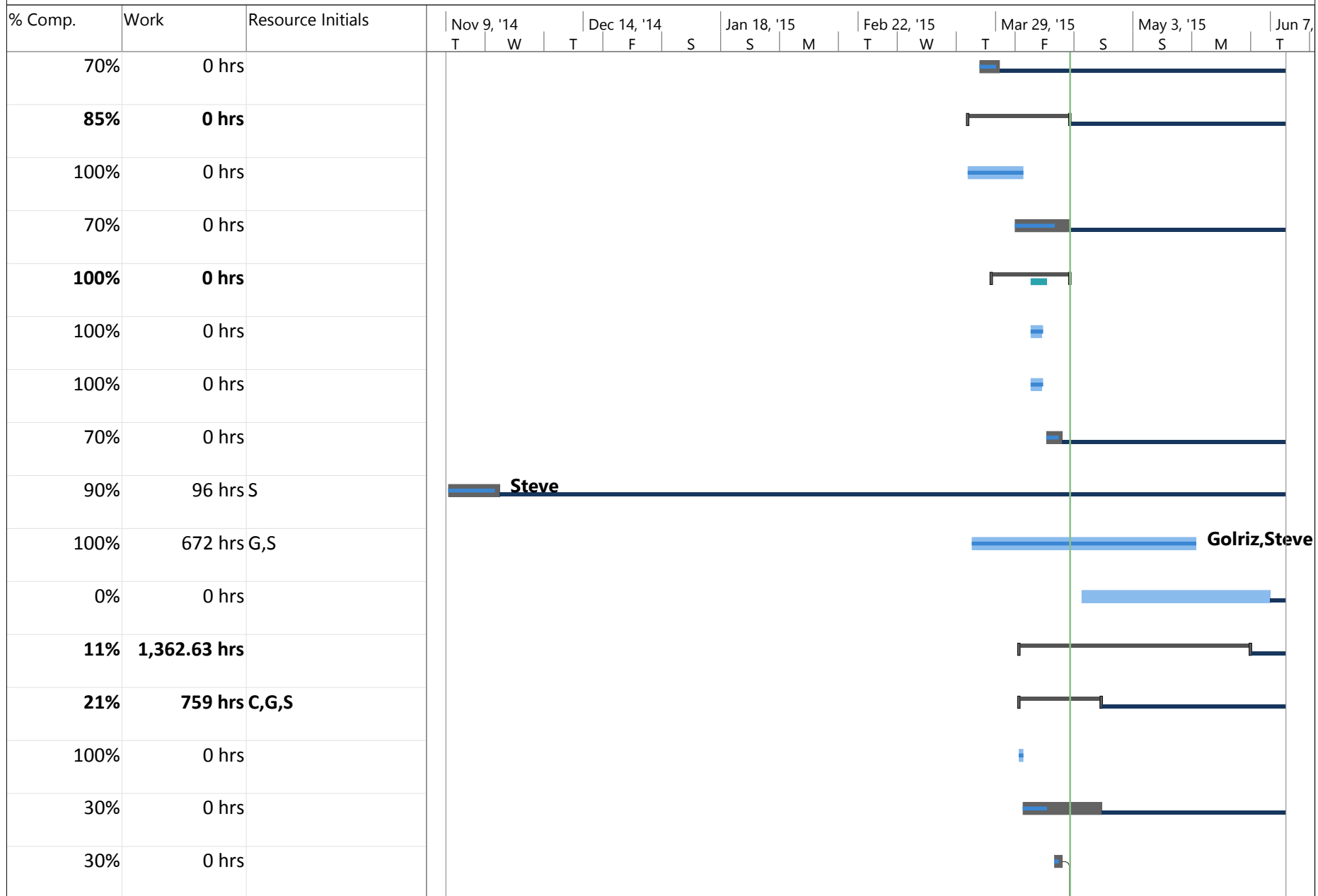
T15-PROJECT SCHEDULE

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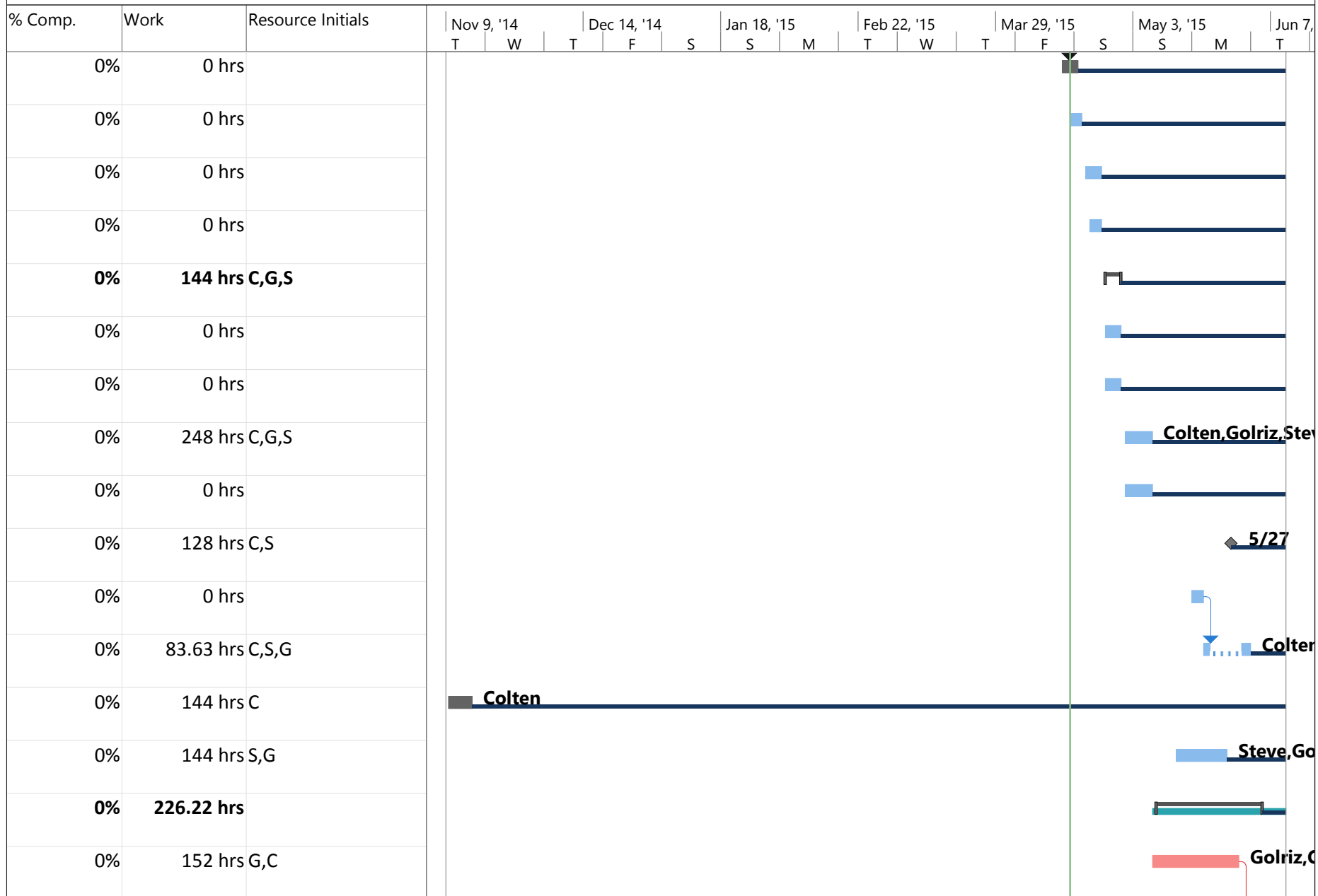
T15-PROJECT SCHEDULE

10|15



T15-PROJECT SCHEDULE

11/15



T15-PROJECT SCHEDULE

12|15

% Comp.	Work	Resource Initials	Nov 9, '14			Dec 14, '14			Jan 18, '15			Feb 22, '15			Mar 29, '15			May 3, '15			Jun 7, '15
			T	W	T	F	S	S	M	T	W	T	F	S	S	M	T				
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hrs	C,S,G	<div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><d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T15-PROJECT SCHEDULE

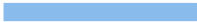




















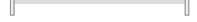
13|15

% Comp.	Work	Resource Initials	Nov 9, '14					Dec 14, '14			Jan 18, '15		Feb 22, '15			Mar 29, '15			May 3, '15		Jun 7, '15
			T	W	T	F	S	S	M	T	W	T	F	S	S	M	T				
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T15-PROJECT SCHEDULE

14|15

% Comp.	Work	Resource Initials	Nov 9, '14			Dec 14, '14			Jan 18, '15		Feb 22, '15			Mar 29, '15			May 3, '15		Jun 7, '15
			T	W	T	F	S	S	M	T	W	T	F	S	S	M	T		
0%	0 hrs																		

Task		Manual Task		Deadline	
Split		Duration-only		Critical	
Milestone		Manual Summary Rollup		Critical Split	
Summary		Manual Summary		Late	
Project Summary		Start-only		Progress	
Inactive Task		Finish-only		Manual Progress	
Inactive Milestone		External Tasks		Slack	
Inactive Summary		External Milestone	