DATE / /   SUBJECT:

7	4	3	0

Memory - Block : | OP - Code | | $\Rightarrow$ memory: $8 \times 8 = 2^3 \times 8$

3 bit address required.

operation	:	opcode		The rest of register
Add	:	0001	–	data (3 - 0)
Sub	:	0010	–	data (3 - 0)
OR	:	0011	–	data (3 - 0)
XOR	:	0100	–	data (3 - 0)
AND	:	0101	–	data (3 - 0)
NOT	:	0110	–	data (3 - 0)
brz	:	0111	– condition (3) –	target (2 - 0)
brzn	:	1000	– condition (3) –	target (2 - 0)
load	:	1001	– don't care (3) –	address (2 - 0)
Store	:	1010	– don't care (3) –	address (2 - 0)

Sahand

Ror : 1011 — data (3-0)

RoL : 1100 — data (3-0)

Shr : 1101 — data (3-0)

shL : 1110 — data (3-0)

halt : 1111 — Don't care