

CS 110 Computer Architecture Everything is a Number

Instructors:

Siting Liu & Chundong Wang

Course website: https://toast-lab.sist.shanghaitech.edu.cn/courses/CS110@ShanghaiTech/ Spring-2023/index.html

School of Information Science and Technology (SIST)
ShanghaiTech University

Course Info

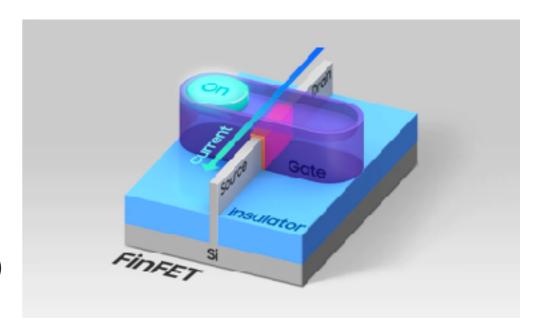
- HW1 is available, Due Feb. 16th!
- Team (Lab & project) partners are required to be within the same lab session! Decide before Feb. 11th!
- Acknowledgement: UC Berkeley's CS61C: https://cs61c.org/; 国科大一生一芯: https://ysyx.oscc.cc/
- https://piazza.com/shanghaitech.edu.cn/spring2023/cs110 (access code: uutib6ruvql)
- Textbooks: Average 15 pages of reading/week
 - Patterson & Hennessey, Computer Organization and Design **RISC-V edition**!
 - Kernighan & Ritchie, The C Programming Language, 2nd Edition
 - RTFM: C & RISC-V
- Materials this year are similar to previous years, but there might be differences!
- https://robotics.shanghaitech.edu.cn/courses/ca/22s/

Outline

- Binary system
- Everything is a number
- Signed and Unsigned integers
- Tow's-complement representation

Binary System

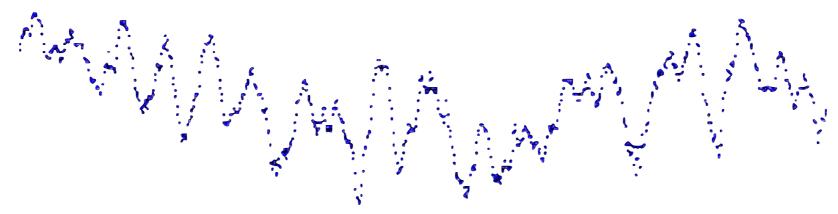
- o and 1
- Decided by the characteristic of semiconductor devices (bi-stable states)
- Resilient to noise (threshold)
 - Two branches of math theory
 - Can do logic and arithmetic
- Analogy to decimal (to represent values)
- Positionally weighted coding
- Binary-decimal conversion
- Extend to Hexadecimal (Base 16)/Octal (Base 8)



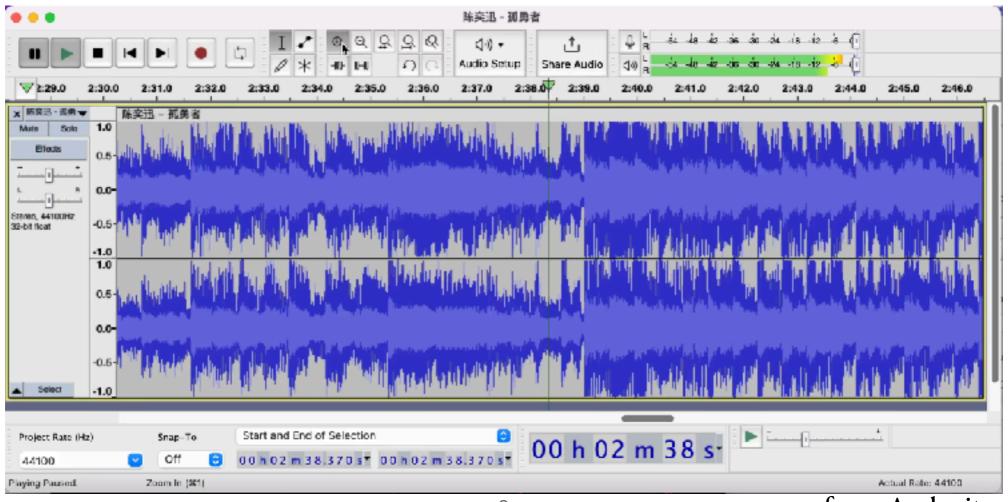
Arithmetic

- Inside computers, everything is a number
- But numbers usually stored with a fixed size
 - 4-bit nibbles (rarely used), 8-bit bytes, 16-bit half words, 32-bit words, 64-bit double words, ...

- Inside computers, everything is a number
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- Integer and floating-point operations can lead to results too big/small to store within their representations: overflow
- To avoid overflow, use more bits or extend the range by interpreting a number differently



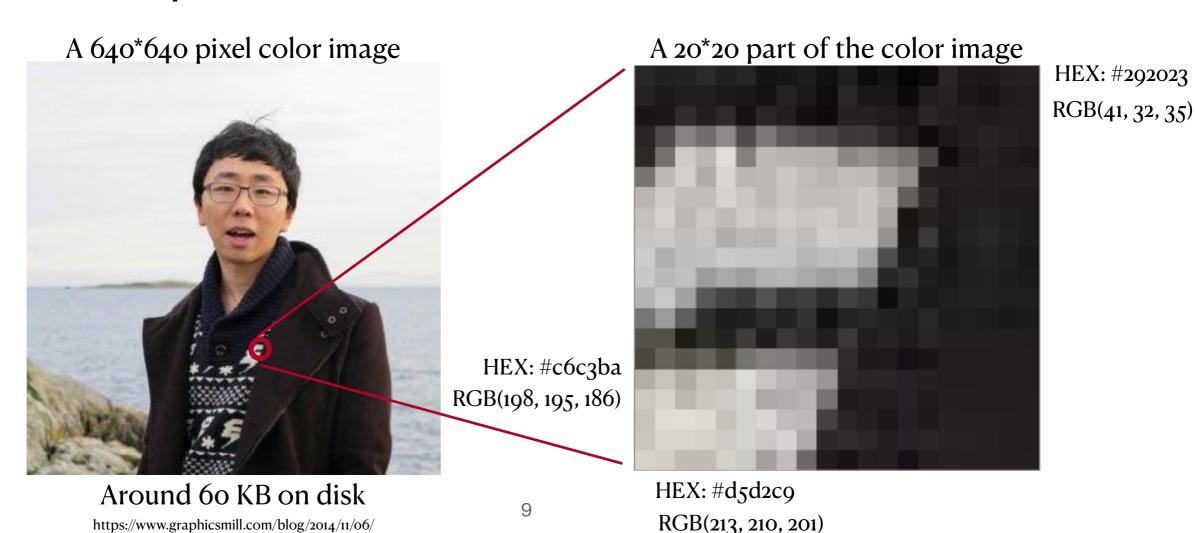
Soundtrack sampled at 44.1 kHz



• Inside computers, everything is a number

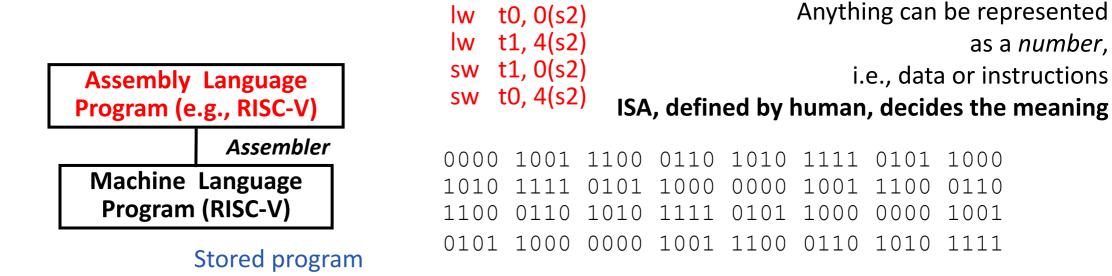
Compression-ratio-for-different-JPEG-quality-values

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- Inside computers, everything is a number (but not necessary the value)
- But numbers usually stored with a fixed size
 - 4-bit nibbles (rarely used), 8-bit bytes, 16-bit half words, 32-bit words, 64-bit double words, ...
- Identity, bank account, profile, ...
 - ID number, DoB (date of birth), criminal record, mobile, etc.
 - Bank account numbers, balance, loan, transaction records, etc.
 - Game account, coins, equipments, ...

- Inside computers, everything is a number (but not necessary the value)
- But numbers usually stored with a fixed size
 - 4-bit nibbles (rarely used), 8-bit bytes, 16-bit half words, 32-bit words, 64-bit double words, ...
- Instructions: e.g., move direction: forward, backward, left, right; use $(00)_2$, $(01)_2$, $(10)_2$ and $(11)_2$



as a number,

• It is how you interpret the numbers decides the meaning

Signed and Unsigned Integers

- Commonly used in computers to represent integers
- C, C++ have signed integers, e.g., 7, -255:
 - int x, y, z;
- C, C++ also have unsigned integers, e.g. for addresses
- Unsigned integers use their values to represent numbers directly
- Unsigned integers in 32 bit word represent o to 2³²-1 (4,294,967,295) (4 Gibi)

Unsigned Integers

```
0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000_{two} = 0_{ten}
0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0001_{two} = 1_{ten}
0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0010_{two} = 2_{ten}
0111 1111 1111 1111 1111 1111 1111 1101<sub>two</sub> = 2,147,483,645_{ten}
1000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000_{two} = 2,147,483,648_{ten}
1000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0001_{two} = 2,147,483,649_{ten}
1000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0010_{two} = 2,147,483,650_{ten}
(a_n a_{n-1} \dots a_1 a_0)_2 = a_n \cdot 2^n + a_{n-1} \cdot 2^{n-1} + \dots + a_1 \cdot 2^1 + a_0 \cdot 2^0
```

Signed Integers

- A straight-forward method: add a sign bit (sign-magnitude)
- Most-significant bit (MSB, leftmost) is the sign bit, o means positive, 1 means negative; the other bits remain unchanged

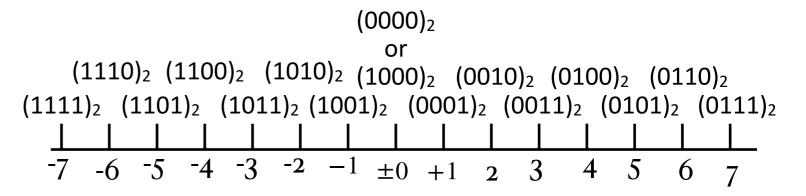
 $0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0011_{two} = 3_{ten}$ $1000\ 0000\ 0000\ 0000\ 0000\ 0001$ $1_{two} = -3_{ten}$

Sign bit

• Range:

• Positive: $0 \sim 2^{(n-1)}-1$

- Negative: $-0 \sim -(2^{(n-1)}-1)$
- Arithmetically unfriendly



Signed Integers

One's-& Two's-Complement Representation

- One's-complement representation
 - Positive numbers, stay unchanged; Negative numbers, toggle all bits

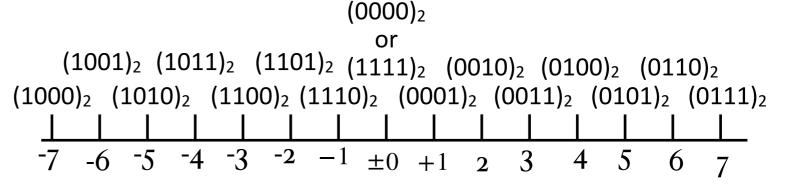
$$0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 00011_{two} = 3_{ten}$$
 1111 1111 1111 1111 1111 1111 11100_{two} = -3_{ten}

Sign

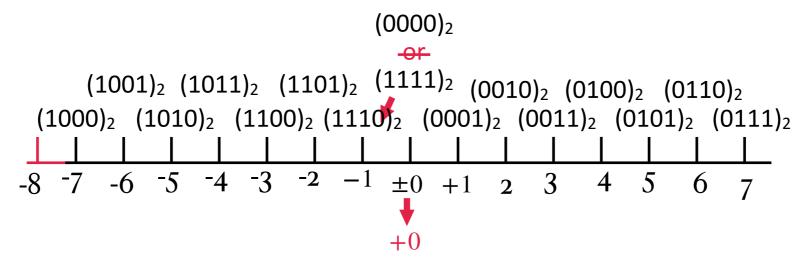
bit



- Positive: $0 \sim 2^{(n-1)}-1$
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- Arithmetically unfriendly

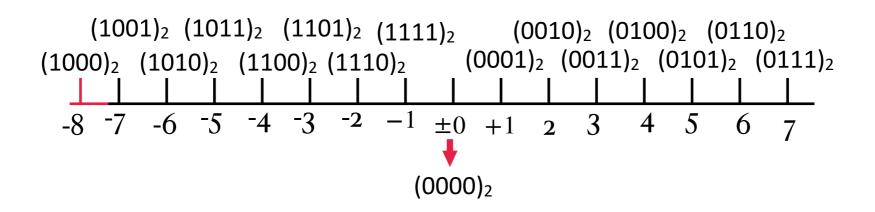


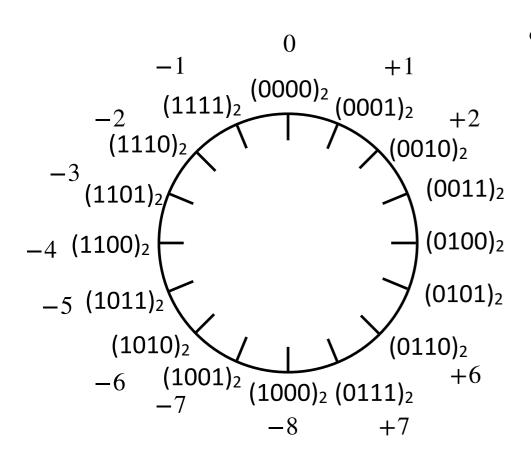
Two's-Complement Representation (Signed Integer)



- Two's-complement representation:
 - Positive numbers, stay unchanged; Negative numbers, apply two's complement (for an n-bit number A, complement to 2ⁿ is 2ⁿ-A, or toggling all bits and adding 1)

Two's-Complement Representation (Signed Integer)





2'complement number $(a_n a_{n-1} ... a_1 a_0)_2$ represents

$$(a_n a_{n-1} \dots a_1 a_0)_2 = -a_n \cdot 2^n + a_{n-1} \cdot 2^{n-1} + \dots + a_1 \cdot 2^1 + a_0 \cdot 2^0$$

- Sign extension
- Arithmetics

Two's-Complement Arithmetic (Addition & Subtraction)

Overflow check!

Comparison

Sign-magnitude

Two's-complement







Two's-Complement Representation (Signed Integer)

- Two's complement treats o as positive, so 32-bit word represents 2³² integers from -2³¹ (-2,147,483,648) to 2³¹-1 (2,147,483,647)
 - Note: one negative number with no positive version
 - Every computer uses two's complement today
- Most-significant bit (MSB) (leftmost) is the sign bit, since o means positive (including o), 1 means negative
 - Bit 31 is most significant (MSB), bit o is least significant (LSB)

Two's-Complement Representation (Signed Integer)

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CS 110 Computer Architecture Intro to C I

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School of Information Science and Technology (SIST)
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Introduction to C "The Universal Assembly Language"

SECOND EDITION

THE



BRIAN W. KERNIGHAN DENNIS M. RITCHIE

Intro to C

- C is not a "very high-level" language, nor a "big" one, and is not specialized to any particular area of application. But its absence of restrictions and its generality make it more convenient and effective for many tasks than supposedly more powerful languages.
- Enabled first operating system not written in assembly language: UNIX A portable OS!

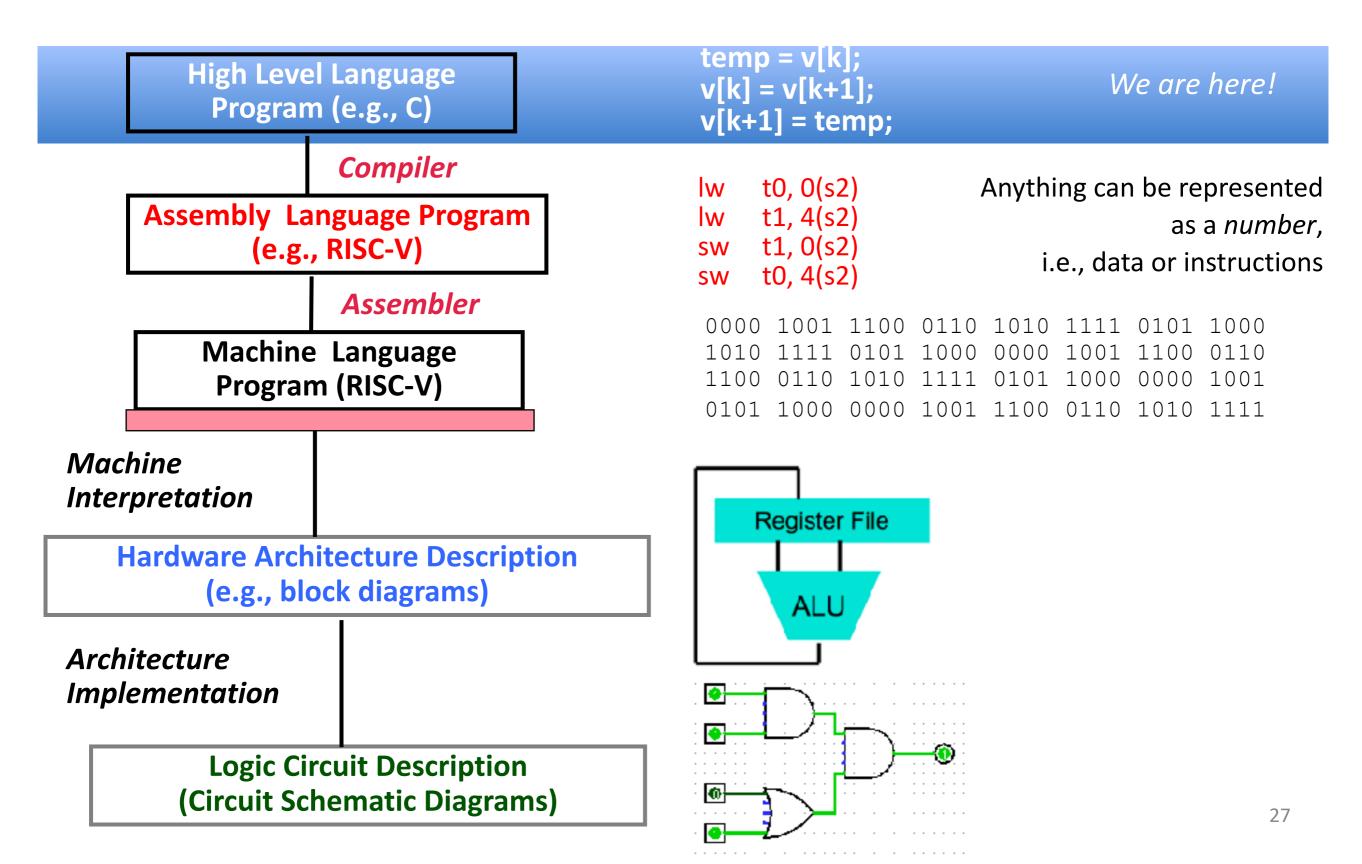
Intro to C

- Why C?: we can write programs that allow us to exploit underlying features of the architecture memory management, special instructions, parallelism
- C and derivatives (C++/Obj-C/C#) still one of the most popular application programming languages after >40 years!

Disclaimer

- You will not learn how to fully code in C in these lectures! You'll still need your C reference for this course
 - K&R C is a recommendation
 - Check online for more sources
 - $\bullet \ ANSI/ISOC\ standard\ manual\ (RTFM)\ (\underline{https://web.archive.org/web/20200909074736if_/https://www.pdf-archive.com/2014/10/02/ansi-iso-9899-1990-1/ansi-iso-9899-1990-1.pdf; \underline{https://web.archive.org/web/20200909074736if_/20030222051144/https://home.earthlink.net/~bobbitts/c89.txt)$
- Key C concepts: Pointers, Arrays, Implications for Memory management
- We will use ANSI C89 original "old school" C
 - Because it is closest to Assembly

How C program works?



Compilation: Overview

- C compilers map C programs into architecture (OS & ISA)-specific machine code (strings of 1s and os)
 - Unlike Java, which converts to architecture-independent bytecode
 - Unlike Python environments, which interpret the code
 - These differ mainly in exactly when your program is converted to low-level machine instructions ("levels of interpretation")
 - For C, generally a two part process of compiling .c files to .o files, then linking the .o files into executables;
 - Assembling is also done (but is hidden, i.e., done automatically, by default); we'll talk about that later

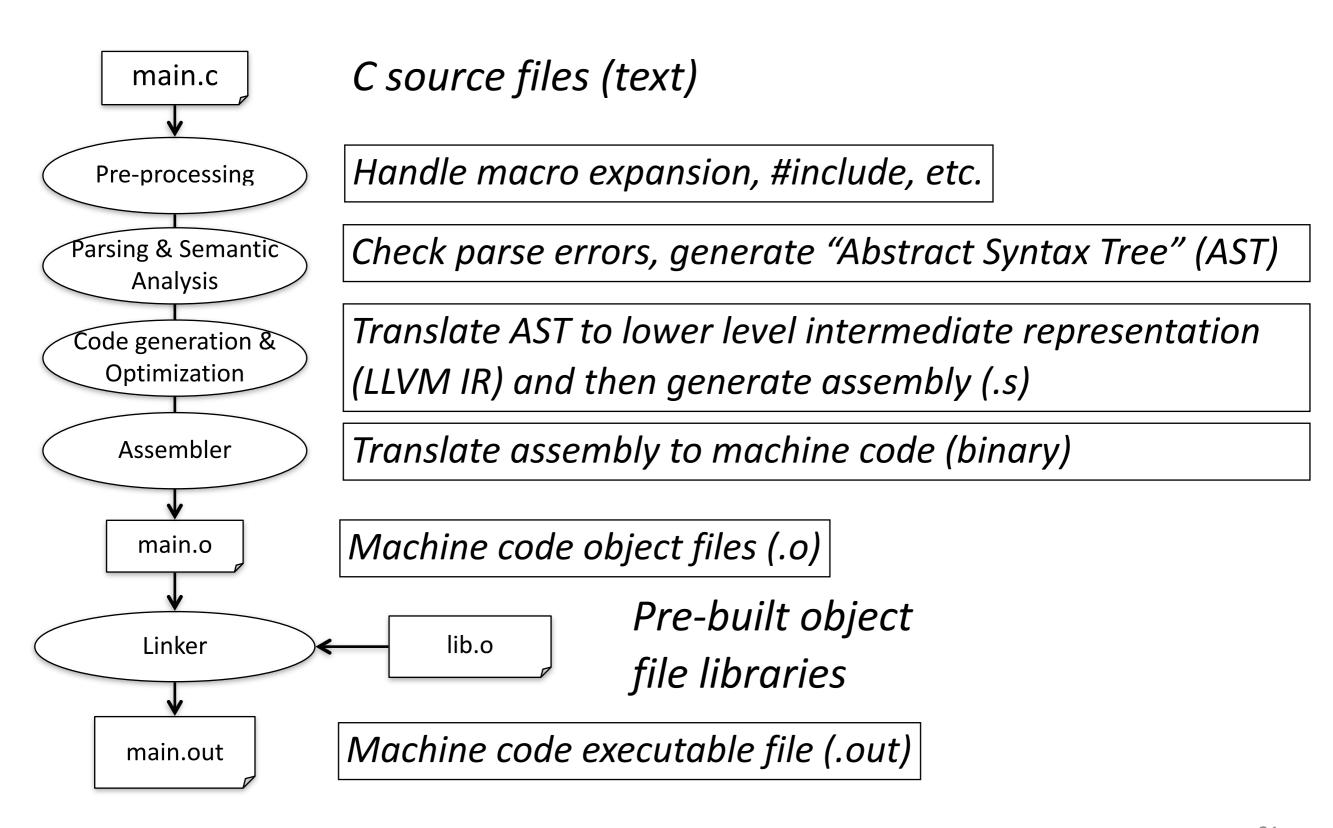
Compilation: Advantages

- Excellent run-time performance: generally much faster than Scheme or Java for comparable code (because it optimizes for a given architecture)
- Reasonable compilation time: enhancements in compilation procedure (Makefiles) allow only modified files to be recompiled

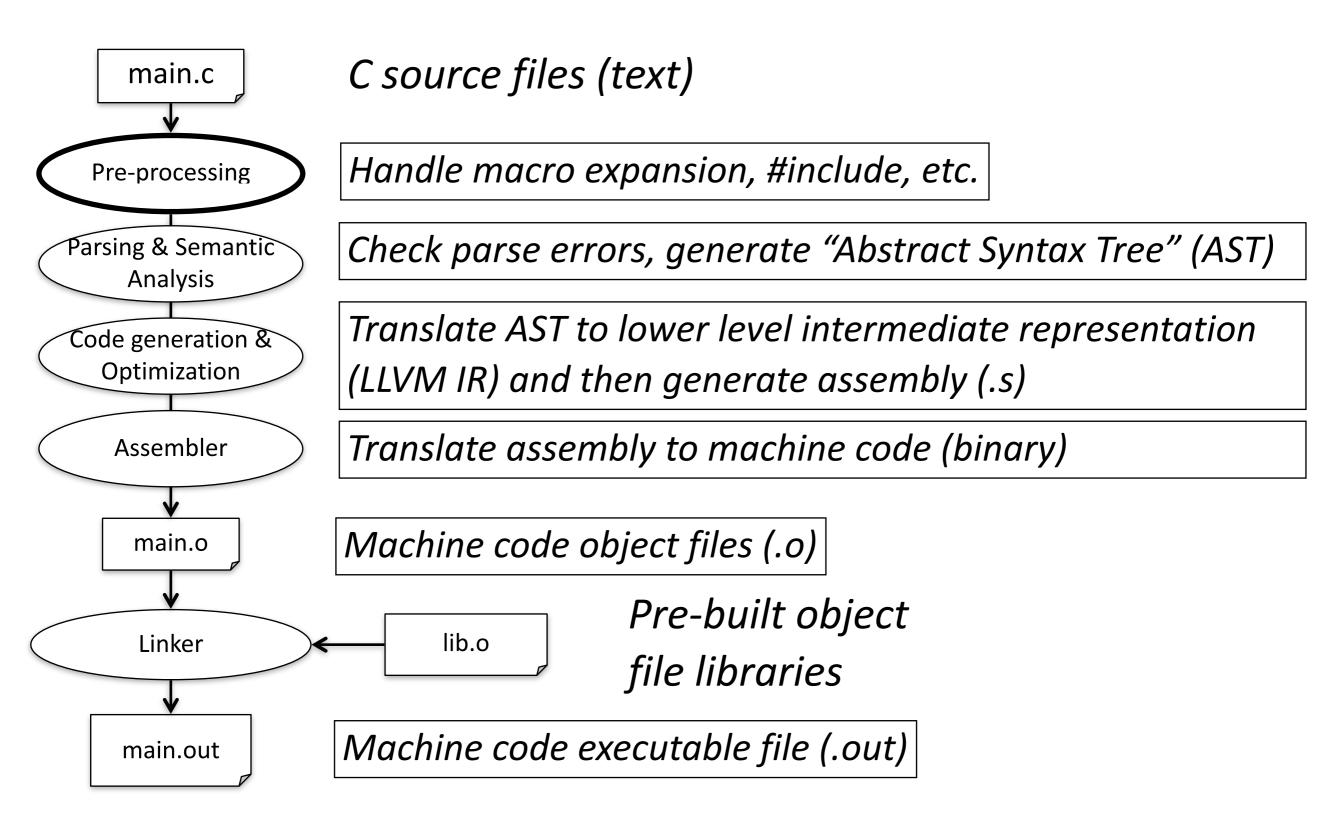
Compilation

- Mainstream C compiler in Linux:
 - GNU Complier Collection (gcc, not only for C family);
 - clang/LLVM (for C language family)
 - In terminal/command line tool/shell, "man clang/gcc"

C Compilation Simplified Overview



C Compilation Simplified Overview



C Pre-Processing (CPP)

- C source files first pass through CPP, before compiler sees code (mainly text editing)
- CPP replaces comments with a single space
- CPP commands begin with "#"
- #include "file.h" /* Inserts file.h into output */
- #include <stdio.h> /* Looks for file in standard location */
- #define M_PI (3.14159) /* Define constant */
- #if/#endif /* Conditional inclusion of text */
- Use -save-temps (-E) option to gcc to see result of preprocessing

Function-Like Macro

• #define MAG(x, y) (sqrt((x)*(x) + (y)*(y)))

```
#include <stdio.h>
                                           %clang/gcc -E introC_1_0.c
#include <math.h>
#define MAG0(x, y) sqrt(x*x + y*y)
#define MAG(x, y) (sqrt((x)*(x) + (y)*(y)))
#define MAG2(x,y) ({double a=x; double b=y; sqrt(a*a + b*b);})
#define MSG "Hello \
World!\n"
int main() {
#ifdef MSG
  printf(MSG /* "hi!\n" */);
#endif
    printf("%f\n",MAG(3.0,4.0));
    double i=2, j=3, k0, k1, k2, k3;
    double c=2, d=3;
    k0 = MAGO(i+1, j+1);
    k1=MAG(i+1,j+1);
    k2=MAG(++i,++j);
    k3=MAG2(++c,++d);
    printf("%f\n",k0);
    printf("%f\n",k1);
    printf("%f\n",k2);
    printf("%f\n",k3);
                             => Convention: put parenthesis EVERYWHERE!
  return 0;
}
```

CPP Macro II

- Avoid using macros whenever possible
- NO or very tiny speedup.
- Instead use C functions e.g. inline function:

double mag(double x, double y);

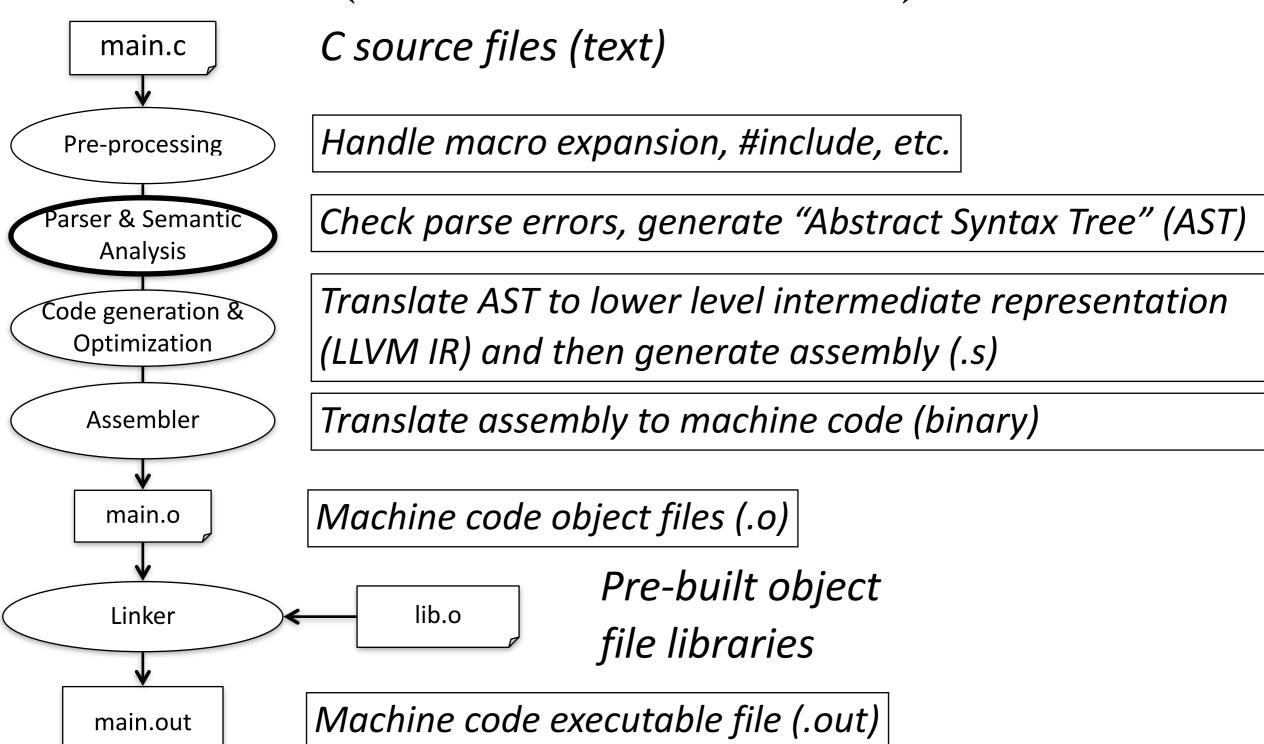
double inline mag(double x, double y)

{ return sqrt(x*x + y*y); }

Read more...

RTFM: https://gcc.gnu.org/onlinedocs/cpp/Macros.html

C Compilation Simplified Overview (more later in course)



Parser & Semantic Analysis

- Recognize each code word as a "token" (identifiers/symbols,
 C keywords, constant, comma, semicolon, etc.)
- Record the location of each token

```
%clang -fsyntax-only -Xclang -dump-tokens introC_1_1_1.c
```

int 'int'

egual '='

identifier 'x'

numeric_constant '0'

semi ';'

r_brace '}'

```
#include <stdio.h>
int main() {//compute 1234 + 4321
  int x = 1234, y = 4321;
  int z = x+y;
  printf("z=%d/n",z);
  return 0;
}
```

```
comma ','
                        Loc=<introC_1_1.c:3:15>
identifier 'y'
                 [LeadingSpace] Loc=<introC_1_1.c:3:17>
equal '='
                 [LeadingSpace] Loc=<introC_1_1.c:3:19>
numeric_constant '4321' [LeadingSpace] Loc=<introC_1_1.c:3:21>
semi ';'
                        Loc=<introC_1_1.c:3:25>
int 'int'
                 [StartOfLine] [LeadingSpace] Loc=<introC_1_1.c:4:3>
identifier 'z'
                 [LeadingSpace] Loc=<introC_1_1.c:4:7>
equal '='
                 [LeadingSpace] Loc=<introC_1_1.c:4:9>
identifier 'x'
                 [LeadingSpace] Loc=<introC_1_1.c:4:11>
plus '+'
                        Loc=<introC_1_1.c:4:12>
identifier 'y'
                        Loc=<introC_1_1.c:4:13>
semi ';'
                       Loc=<introC_1_1.c:4:14>
identifier 'printf'
                        [StartOfLine] [LeadingSpace] Loc=<introC_1_1.c:5:3>
1_paren '('
                       Loc=<introC_1_1.c:5:9>
string_literal '"z=%d/n"'
                                        Loc=<introC 1 1.c:5:10>
comma ','
                       Loc=<introC_1_1.c:5:18>
identifier 'z'
                       Loc=<introC_1_1.c:5:19>
r_paren ')'
                        Loc=<introC_1_1.c:5:20>
semi ';'
                        Loc=<introC_1_1.c:5:21>
return 'return' [StartOfLine] [LeadingSpace] Loc=<introC_1_1.c:6:3>
```

Loc=<introC_1_1.c:6:11>

[StartOfLine] Loc=<introC_1_1.c:7:1>

[LeadingSpace] Loc=<introC_1_1.c:3:7>

[LeadingSpace] Loc=<introC_1_1.c:3:9>

numeric_constant '1234' [LeadingSpace] Loc=<introC_1_1.c:3:11>

[StartOfLine] [LeadingSpace] Loc=<introC_1_1.c:3:3>

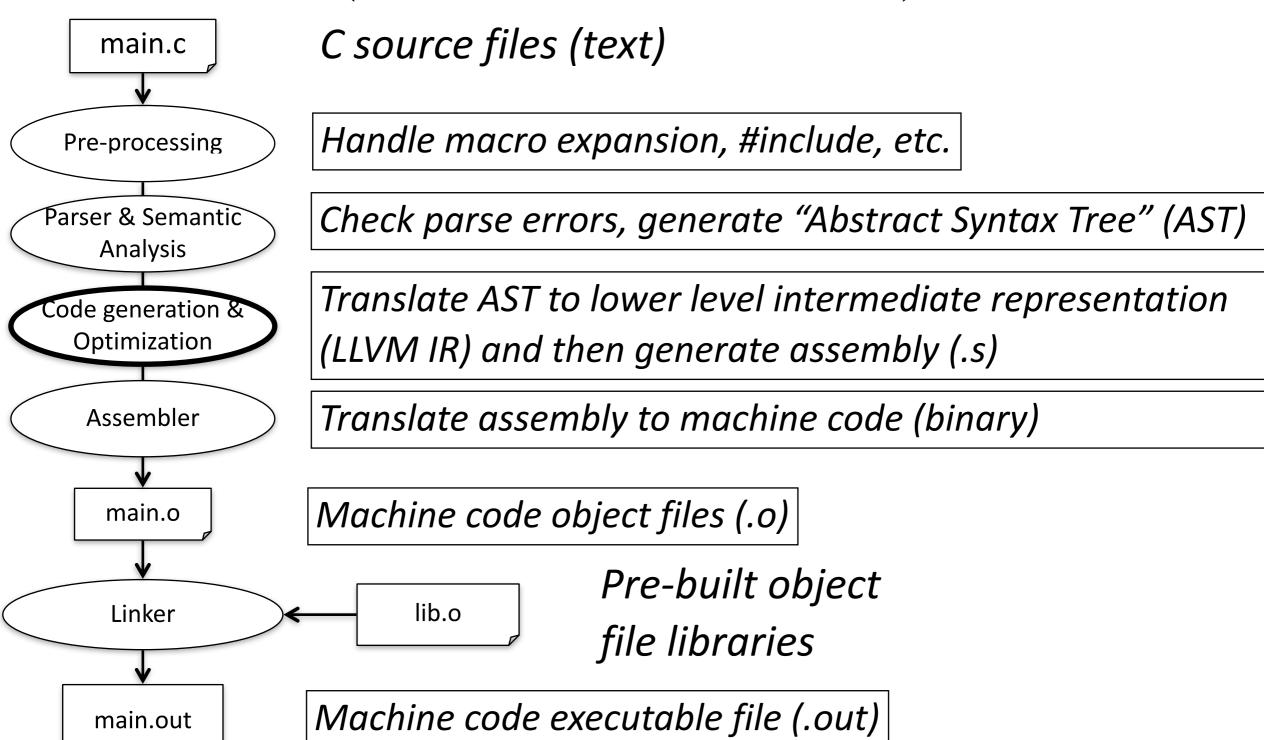
[LeadingSpace] Loc=<introC_1_1.c:6:10>

Parser & Semantic Analysis

- Organize tokens as "AST" tree
- Report errors

```
% clang -fsyntax-only -Xclang -ast-dump introC_1_1_1.c
#include <stdio.h>
int main() {//compute 1234 + 4321
    int x = 1234, y = 4321;
    int z = x+y;
    printf("z=%d/n",z);
                                                      -FunctionDecl 8x1590f8608 <introC_1_1.c:2:1, line:7:1> line:2:5 main 'int ()'
                                                        -CompoundStmt 0x1590f8aa0 <col:12, line:7:1>
    return 0;
                                                         -Dec19tmt 0x1590f87f8 e:3:3, col:25>
                                                           |-VarDecl 9x1590f86b8 <col:3, col:11> col:7 used x 'int' cinit
                                                            '-IntegerLiteral 0x1590f8720 <col:11> 'int' 1234
                                                           '-VarDecl 0x1590f8758 <col:3, col:21> col:17 used y 'int' cinit
                                                            `-IntegerLiteral 0x1590f87c0 <col:21> 'int' 4321
                                                          -DeclStmt 0x1590f8920 e:4:3, col:14>
                                                           '-VarDecl 0x1590f8828 <col:3, col:13> col:7 used z 'int' cinit
                                                            `-BinaryOperator 9x1590f8930 <col:11, col:13> 'int' '+'
                                                              |-ImplicitCastExpr 0x1590f88d0 <col:11> 'int' <LValueToRValue>
                                                                '-DeclRefExpr 8x1590f8898 <col:11> 'int' lvalue Var 0x1590f86b8 'x' 'int'
                                                               -ImplicitCastExpr 0x1590f88e8 <ccl:13> 'int' <LValueToRValue>
                                                                '-DeclRefExpr 0x1590f88b0 <col:13> 'int' lvalue Var 0x1590f8758 'y' 'int'
                                                          -CallExpr 9x1590f89f8 <line:5:3, col:20> 'int'
                                                          |-ImplicitCastExpr 0x1590f89e0 <col:3> 'int (*)(const char *, ...)' <FunctionToPointerDeca
                                                         | | '-DeclRefExpr 0x1590f8938 <col:3> 'int (const char *, ...)' Function 0x1590dd388 'printf
                                                     ' 'int (const char *, ...)'
```

C Compilation Simplified Overview (more later in course)



Code Generation & Optimization

- Generate intermediate representation (IR)
 - LLVM IR for clang/LLVM
 - GIMPLE for gcc

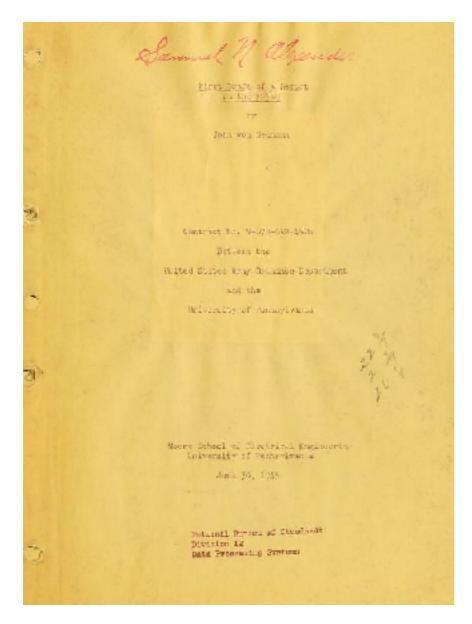
```
%clang -S -emit-llvm introC_1_1.c -o introC_1_1.ll
```

```
#include <stdio.h>
                                                     ; ModuleID = 'introC_1_1.c'
                                                     source_filename = "introC_1_1.c"
int main() {//compute 1234
                                                  + target datalayout = "e-m:o-i64:64-i128:128-n32:64-S128"
                                                     target triple = "arm64-apple-macosx12.0.0"
   int x = 1234, y = 4321;
                                                     @.str = private unnamed_addr constant [7 x i8] c"z=%d/n\00", align 1
   int z = x+y;
                                                     ; Function Attrs: noinline nounwind optnone ssp uwtable
   printf("z=%d/n",z);
                                                     define i32 @main() #0 {
                                                      %1 = alloca i32, align 4
    return 0;
                                                      %2 = alloca i32, align 4
                                                      %3 = alloca i32, align 4
                                                      %4 = alloca i32, align 4
                                                       store i32 0, i32* %1, align 4
                                                       store i32 1234, i32* %2, align 4
                                                       store i32 4321, i32* %3, align 4
                                                      %5 = load i32, i32* %2, align 4
                                                      %6 = load i32, i32* %3, align 4
                                                      %7 = add nsw i32 %5, %6
                                                      store i32 %7, i32* %4, align 4
                                                      %8 = load i32, i32* %4, align 4
                                                      %9 = call i32 (i8*, ...) @printf(i8* getelementptr inbounds ([7 x i8]
                                                     0), i32 %8)
```

ret i32 0

Components of Computers

- Von Neumann Architecture
 - First Draft of a Report on the EDVAC



By John von Neumann - https://archive.org/stream/firstdraftofrepooovonn#page/n1/mode/2up, Public Domain, https://commons.wikimedia.org/w/index.php?curid=26685284

Central arithmetic (CA)

Central control (CC)

Central Processing Unit (CPU)

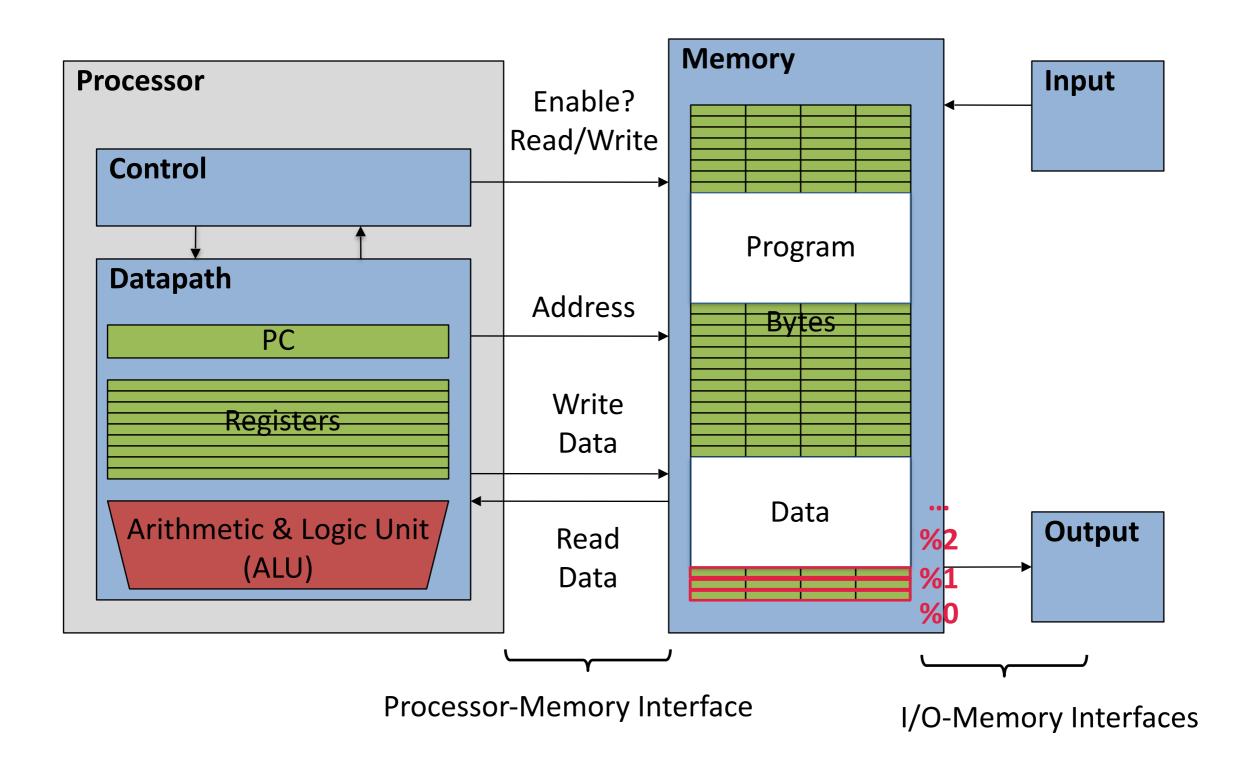
Memory (M) (Data & Program/Instructions)

Input (I)

Output (O)

External memory (R)

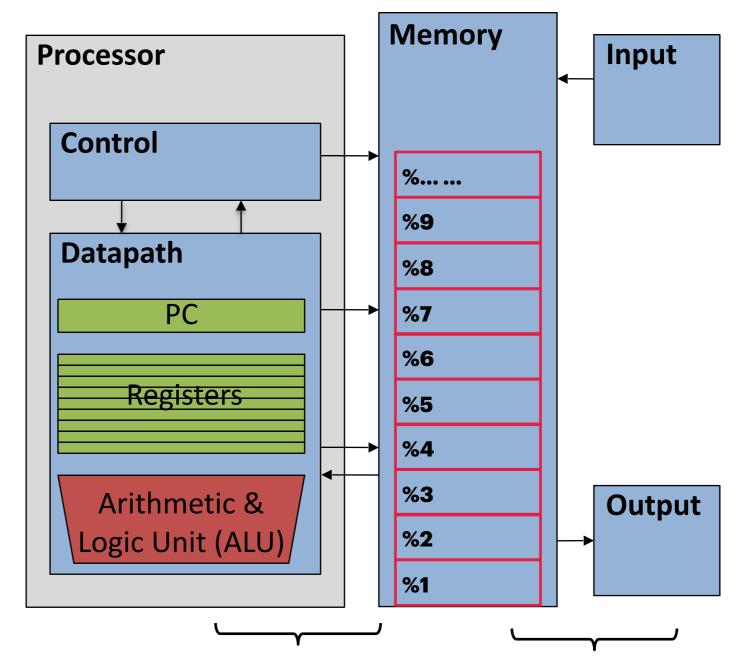
Components of Computers



IR Implication on Hardware

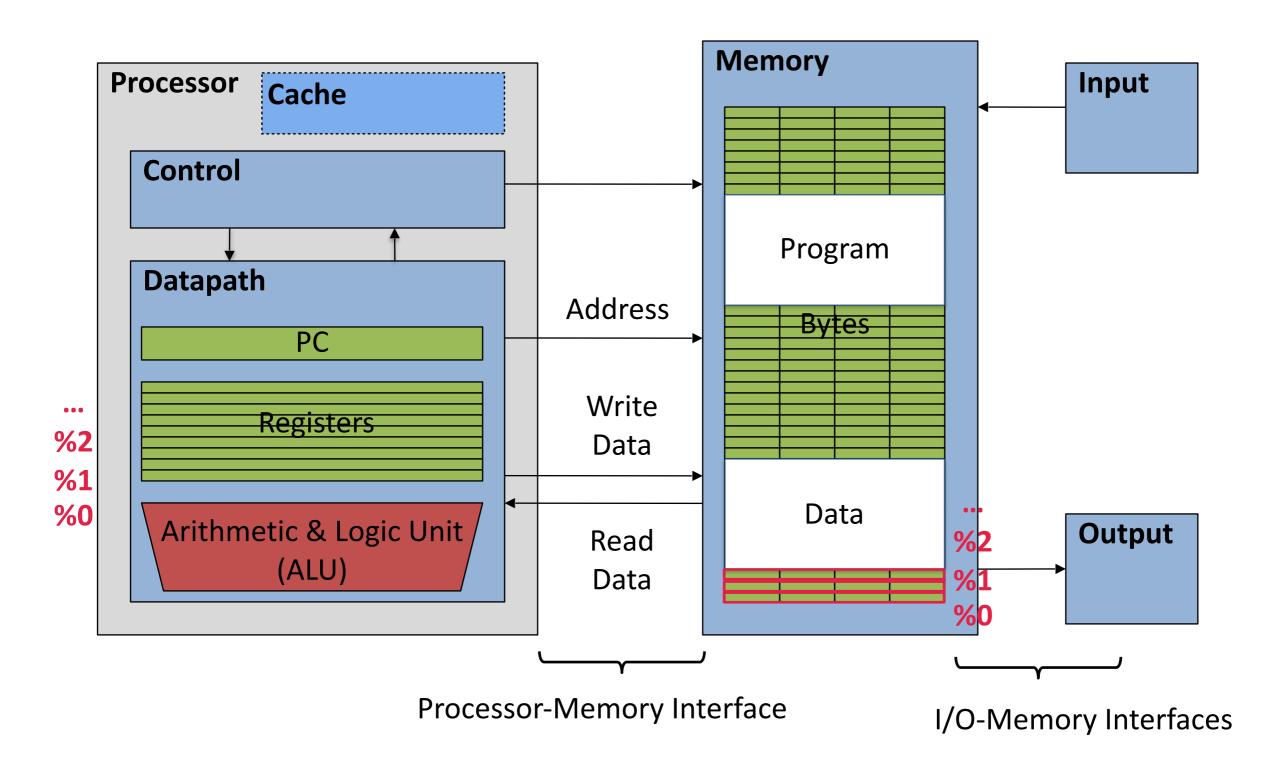
```
#include <stdio.h>
int main() {//compute 1234 + 4321
  int x = 1234, y = 4321;
  int z = x+y;
  printf("z=%d/n",z);
  return 0;
define i32 @main() #o {
 %1 = alloca i32, align 4
                            LLVM IR
 %2 = alloca i32, align 4
 %3 = alloca i32, align 4
 %4 = alloca i32, align 4
 store i32 o, i32* %1, align 4
 store i32 1234, i32* %2, align 4
 store i32 4321, i32* %3, align 4
 %5 = load i32, i32* %2, align 4
 %6 = load i32, i32* %3, align 4
%7 = add nsw i32 %5, %6
 store i32 %7, i32* %4, align 4
 %8 = load i32, i32* %4, align 4
 %9 = call i32 (i8*, ...) @printf(i8*
getelementptr inbounds ([7 x i8], [7
x i8]* @.str, i64 o, i64 o), i32 %8)
ret i32 o
```

Original code



Processor-Memory Interface I/O-Memory Interfaces

Optimization



IR to Assembly to Machine Code

% clang -S introC_1_1.c -o introC_1_1.s

```
define i32 @main() #o {
                            LLVM IR
%1 = alloca i32, align 4
%2 = alloca i32, align 4
%3 = alloca i32, align 4
%4 = alloca i32, align 4
store i32 o, i32* %1, align 4
store i32 1234, i32* %2, align 4
store i32 4321, i32* %3, align 4
%5 = load i32, i32* %2, align 4
%6 = load i32, i32* %3, align 4
%7 = add nsw i32 %5, %6
store i32 %7, i32* %4, align 4
%8 = load i32, i32* %4, align 4
%9 = call i32 (i8*, ...) @printf(i8*
getelementptr inbounds ([7 x i8], [7 x i8]*
@.str, i64 o, i64 o), i32 %8)
ret i32 o
```

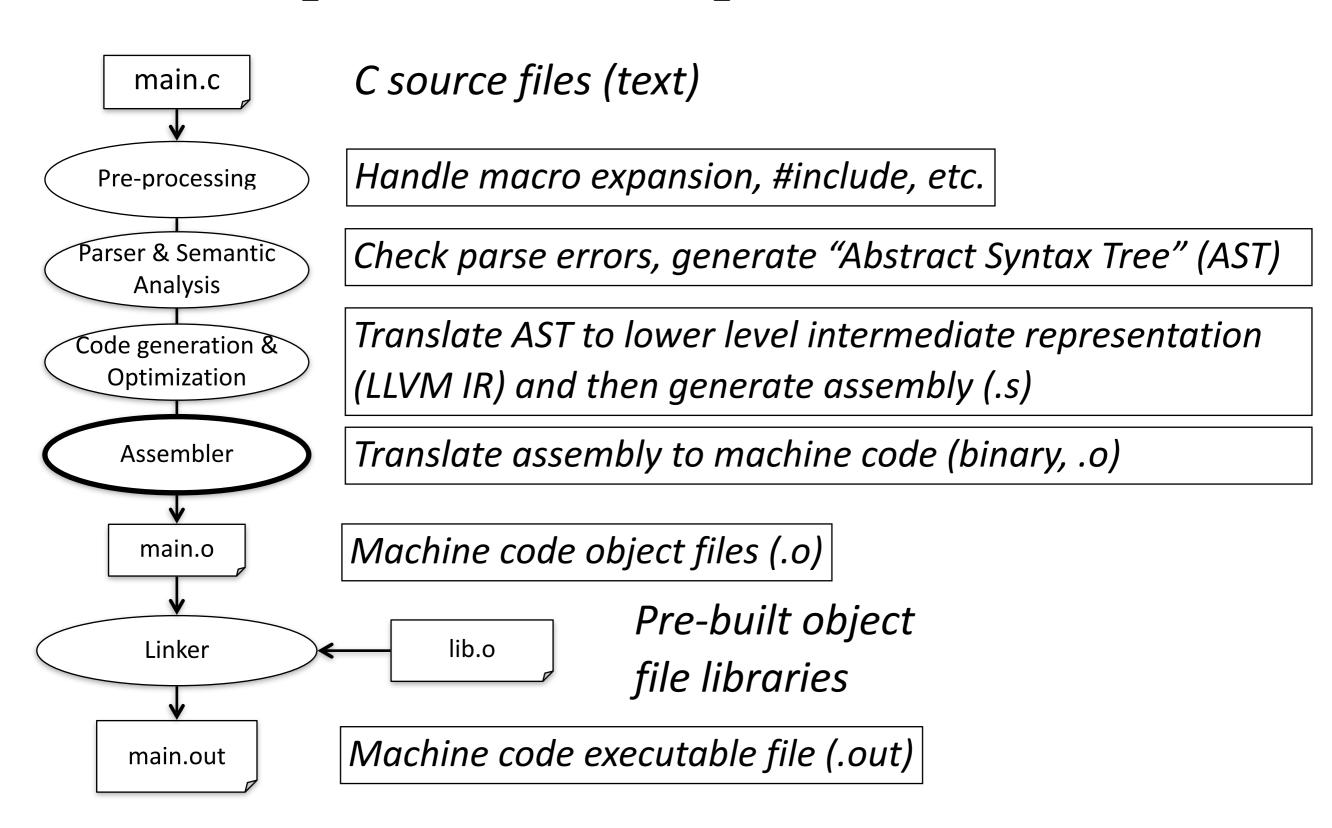
```
.section __TEXT,__text,regular,pure_instructions
    .build_version macos, 12, 0 sdk_version 13, 1
                                   ; -- Begin function
    .globl main
main
    .p2align 2
main:
                                   ; @main
    .cfi_startproc
; %bb.0:
    sub sp, sp, #48
    stp x29, x30, [sp, #32]
                                   ; 16-byte Folded Spill
    add x29, sp, #32
    .cfi_def_cfa w29, 16
    .cfi offset w30, -8
    .cfi_offset w29, -16
    mov w8, #0
    str w8, [sp, #12]
                                   ; 4-byte Folded Spill
    sturwzr, [x29, #-4]
    mov w8, #1234
    sturw8, [x29, #-8]
    mov w8, #4321
                                  ARM Assembly
    sturw8, [x29, #-12]
    ldurw8, [x29, #-8]
    ldurw9, [x29, #-12]
    add w8, w8, w9
                            Translated to machine code
    str w8, [sp, #16]
    ldr w9, [sp, #16]
                            defined by ISA
```

IR to Assembly to Machine Code

```
% clang -c introC_1_1.c -o introC_1_1.o % objdump -d introC_1_1.o
```

```
Disassembly of section __TEXT,__text:
                      00000000000000000 <ltmp0>:
                             0: ff c3 00 d1 sub sp, sp, #48
                             4: fd 7b 02 a9 stp x29, x30, [sp, #32]
                             8: fd 83 00 91
                                             add x29, sp, #32
                                             mov w8, #0
                             c: 08 00 80 52
                            10: e8 0f 00 b9
                                             str w8, [sp, #12]
                            14: bf c3 1f b8
                                             stur wzr, [x29, #-4]
                                             mov w8, #1234
                            18: 48 9a 80 52
Machine Code
                            1c: a8 83 1f b8
                                             stur w8, [x29, #-8]
                            20: 28 1c 82 52
                                             mov w8, #4321
                                            stur w8, [x29, #-12]
                            24: a8 43 1f b8
    (Stored
                            28: a8 83 5f b8
                                            ldur w8, [x29, #-8]
                                                                           ARM Assembly
                            2c: a9 43 5f b8
                                            ldur w9, [x29, #-12]
   program/
                            30: 08 01 09 0b
                                             add w8, w8, w9
                                             str w8, [sp, #16]
                            34: e8 13 00 b9
 instructions)
                            38: e9 13 40 b9
                                            ldr w9, [sp, #16]
                            3c: e8 03 09 aa
                                             mov x8, x9
                                             mov x9, sp
                            40: e9 03 00 91
                            44: 28 01 00 f9
                                             str x8, [x9]
                                             adrp x0, 0x0 < ltmp0+0x48>
                            48: 00 00 00 90
                                             add x0, x0, \#0
                            4c: 00 00 00 91
                            50: 00 00 00 94
                                             bl 0x50 < ltmp0+0x50>
                            54: e0 0f 40 b9
                                            ldr w0, [sp, #12]
                            58: fd 7b 42 a9
                                             ldp x29, x30, [sp, #32]
                            5c: ff c3 00 91
                                             add sp, sp, #48
                            60: c0 03 5f d6
                                             ret
```

C Compilation Simplified Overview



Organization of Computers

- Von Neumann Architecture
 - a.k.a. Princeton architecture
 - Uniform memory for data
 & program/instruction

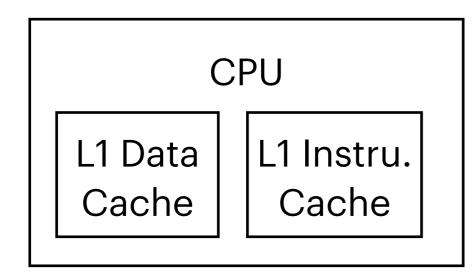
- Harvard Architecture
 - Separated memory for data & program
 - E.g. MCU, DSP, L1 Cache

Data memory

CPU

Instruction memory

I/O

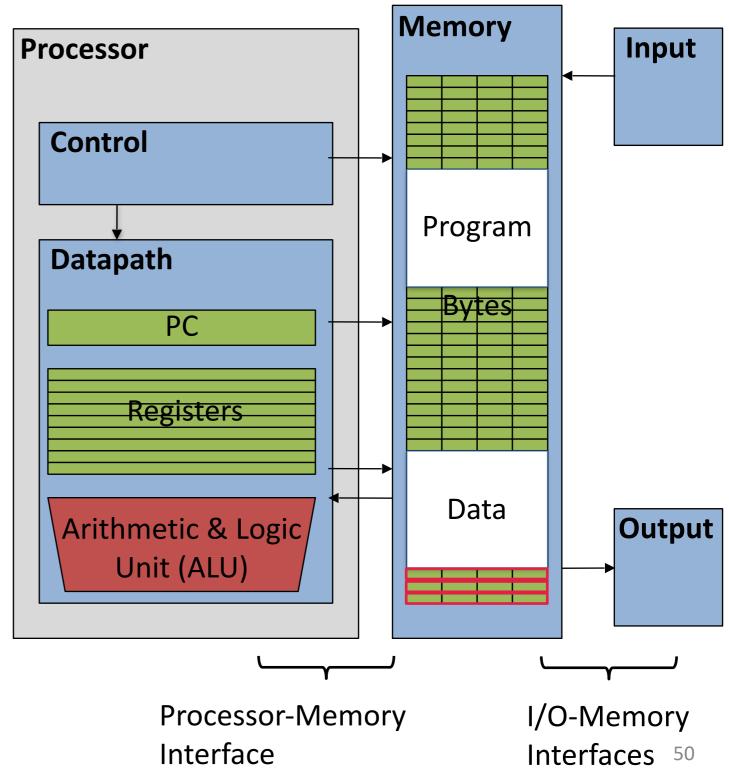


Wrap-it-up

- From C to machine code (clang *.c \rightarrow *.out & ./*.out)
 - Pre-processing (macro, function-like macro, text editing, #include)
 - Use "()" whenever necessary, or use "function" directly
 - Parser & Semantic Analysis (tokenization & generate AST, basic operations)
 - Translate to IR & optimize (computer components)
 - Translate to assembly and then machine code, executed by hardware (Covered in future lectures)
 - Clang manual: https://releases.llvm.org/14.0.0/tools/clang/docs/ UsersManual.html
 - GCC: https://gcc.gnu.org/

Wrap-it-up

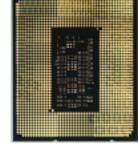
- Von Neumann Architecture
- Harvard Architecture
- Stored-program computer

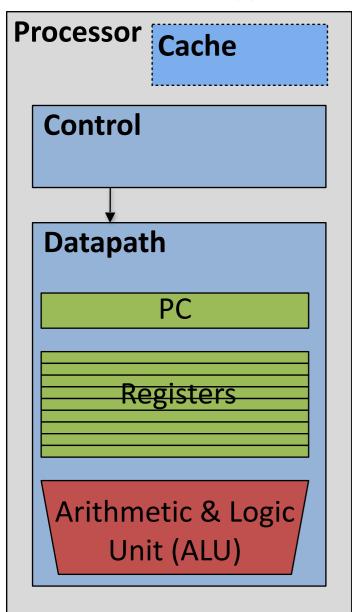


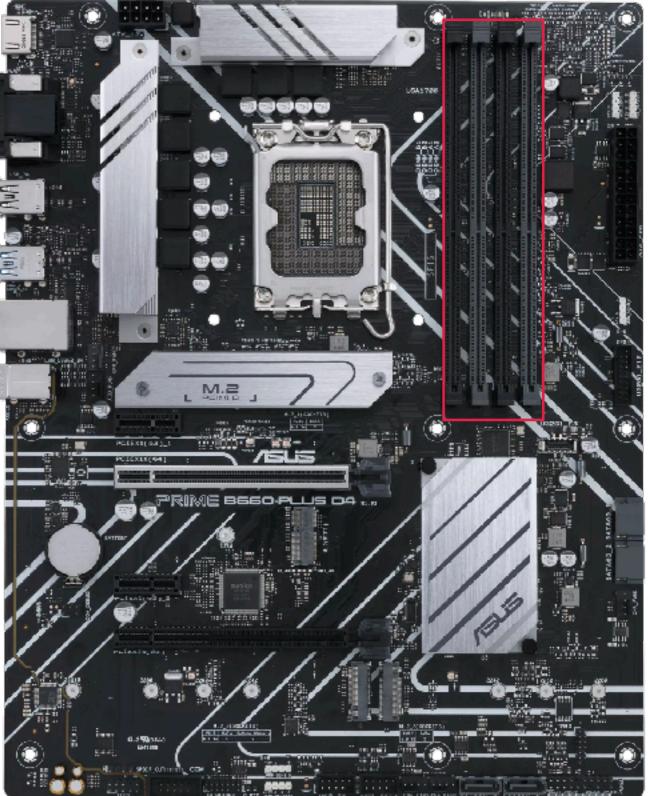
Intel i₇ 12700 4.90 GHz

Real Stuff





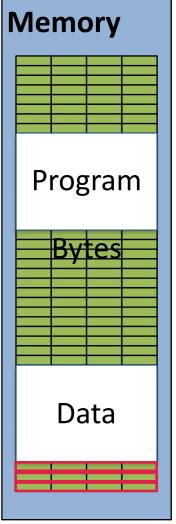




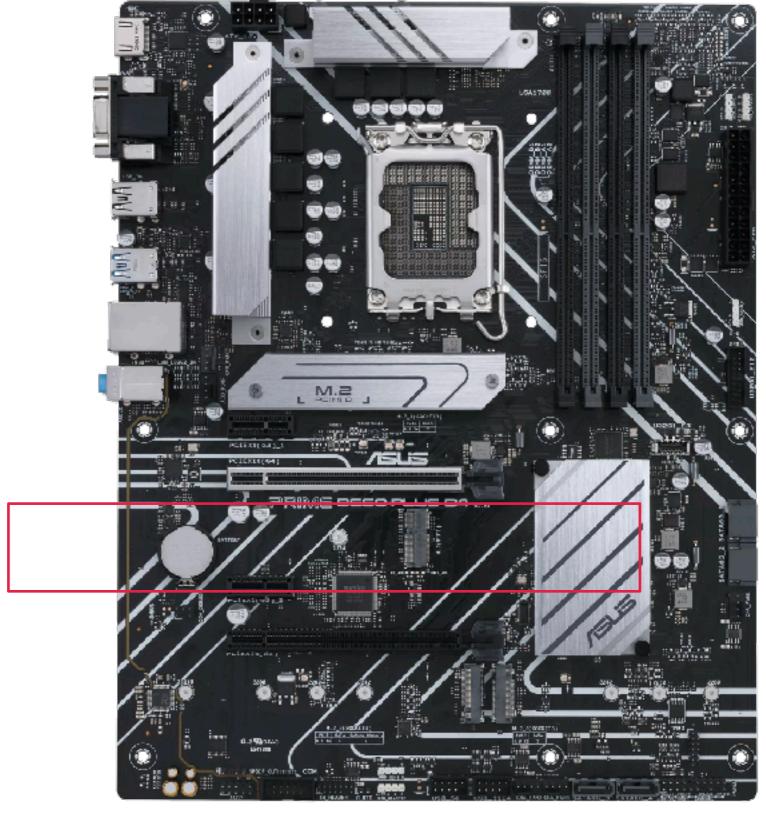
DIMM DDR4 5066 MHz

Kingston 8/16/32G SDRAM





Real Stuff



52

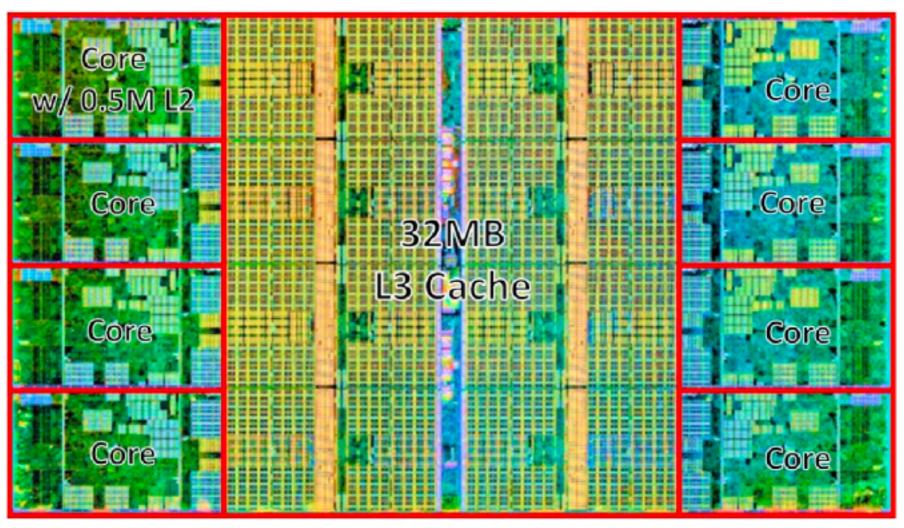
Real Stuff



https://www.samsung.com/us/computing/memory-storage/solid-state-drives/ssd-970-pro-nvme-m2-512gb-mz-v7p512bw/https://www.seagate.com/in/en/products/hard-drives/barracuda-hard-drive/

Real Stuff—Inside a CPU

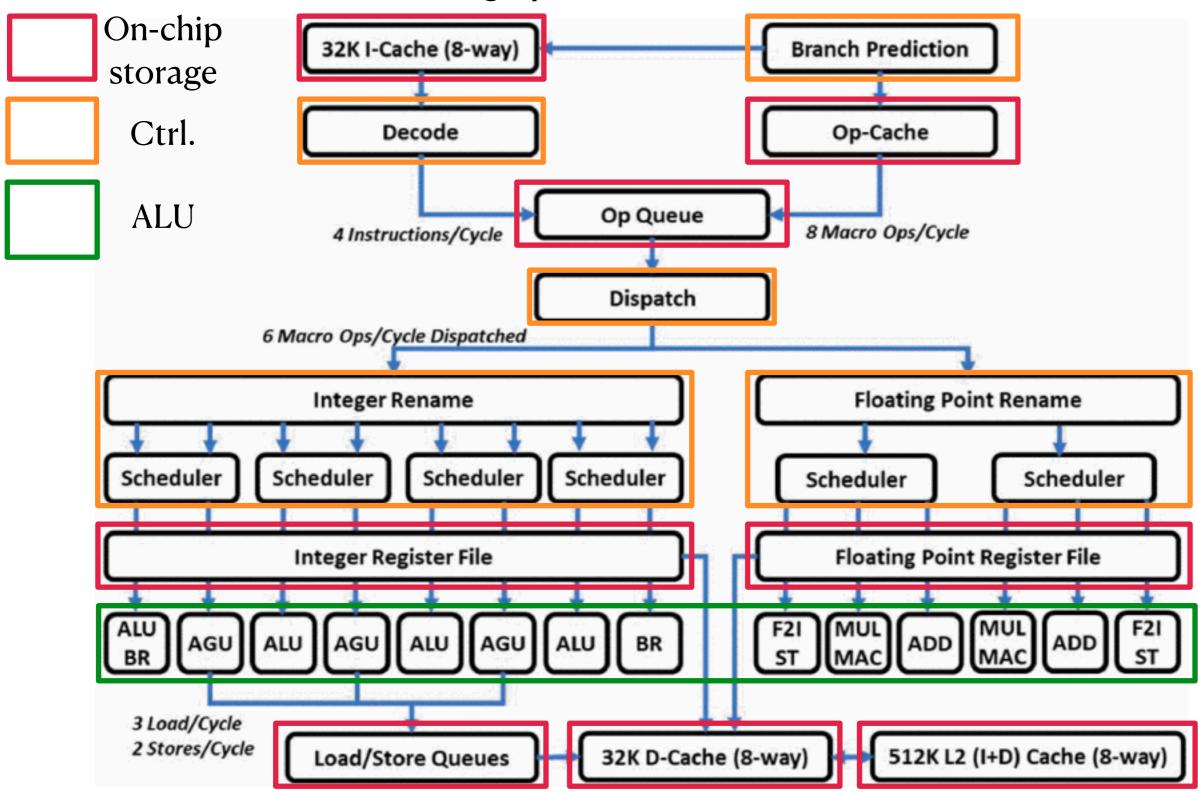
AMD Zen 3 8-core CPU, 7 nm process, 4.08B transistors in 68 mm²



T. Burd *et al.*, "Zen3: The AMD 2nd-Generation 7nm x86-64 Microprocessor Core," *2022 IEEE International Solid- State Circuits Conference (ISSCC)*, San Francisco, CA, USA, 2022, pp. 1-3.

Real Stuff—Inside a CPU

AMD Zen 3, 7 nm process, a single core



Back to C

Typical C program

```
// Created by Siting Liu on 2023/2/5.

// Preprocessing elements (header/macro)

#include <stdio.h>

Variables

int main(int argc, const char * argv[]) {
    // insert code here...
    printf("Hello, World!\n");
    return 0;

}

Comments

Preprocessing elements

(header/macro)

Variables

Functions

Statements
```

• Must C program start with main()? (RTFM/C standard)

Variables

• Typed Variables in C

Type	Description	Examples
int	integer numbers, including negatives	0, 78, -1400
unsigned int	integer numbers (no negatives)	0, 46, 900
long	larger signed integer	-6,000,000,000
(un)signed chai	r single text character or symbol	'a', 'D', '?'
float	floating point decimal numbers	0.0, 1.618, -1.4
double	greater precision/big FP number	10E100

Integers

Typed Variables in C

Language	sizeof(int)	
Python	>=32 bits (plain ints), infinite (long ints)	
Java	32 bits	
С	Depends on computer; 16 or 32 or 64 bits	

- C: int should be integer type that target processor works with most efficiently
- Generally: $sizeof(long long) \ge sizeof(long) \ge sizeof(int) \ge sizeof(short)$
 - Also, short >= 16 bits, long >= 32 bits
 - All could be 64 bits

Integer Constants

```
#include <stdio.h>
int main() {
    printf((6-2147483648)>(6)?"T\n":"F\n");
    printf((6-0x80000000)>(6)?"T\n":"F\n");
    return 0;
}
```

Semantics: The value of a decimal constant is computed base 10; that of an octal constant base 8; that of a hexadecimal constant base 16. The lexically first digit is the most significant.

The type of an integer constant is the first of the corresponding list in which its value can be represented. Unsuffixed decimal **int, long int, unsigned long int;** unsuffixed octal or hexadecimal: **int, unsigned int, long int, unsigned long int;** suffixed by the letter u or U: **unsigned int, unsigned long int;** suffixed by the letter l or L: **long int, unsigned long int;** suffixed by both the letters u or U and 1 or L: **unsigned long int.**

Range of each type defined in limits.h> (INT_MAX, INT_MIN)

Consts. and Enums. in C

Constant is assigned a typed value once in the declaration;
 value can't change during entire execution of program

```
const float golden_ratio = 1.618;
const int days_in_week = 7;
```

- You can have a constant version of any of the standard C variable types
- Enums: a group of related integer constants. Ex:

```
enum cardsuit {CLUBS,DIAMONDS,HEARTS,SPADES};
enum color {RED, GREEN, BLUE};
```

Compare "#define PI 3.14" and "const float pi=3.14" — which is true?

A: Constants "PI" and "pi" have same type

B: Can assign to "PI" but not "pi"

C: Code runs at about the same speed using "PI" or "pi"

D: "pi" takes more memory space than "PI"

E: Both behave the same in all situations

C Syntax: Variable Declarations

- All variable declarations must appear before they are used (e.g., at the beginning of the block)
- A variable may be initialized in its declaration; if not, it holds garbage!
- Examples of declarations:

```
- Correct: {
    int a = 0, b = 10;
    ...
-Incorrect: for (int i = 0; i < 10; i++)
}
```

Newer C standards are more flexible about this...

C Syntax: True or False

- What evaluates to FALSE in C?
 - o (integer)
 - NULL (a special kind of pointer: more on this later)
- No explicit Boolean type
- What evaluates to TRUE in C?
 - Anything that isn't false is true
 - Same idea as in Python: only os or empty sequences are false, anything else is true!

Coperators

- arithmetic: +, -, *, /, %
- assignment: =
- augmented assignment: +=, -=, increment and decrement: ++ *=,/=,%=, &=, |=, ^=, <<=, >>=
- bitwise logic: ~, &, |, ^
- bitwise shifts: <<, >>
- boolean logic: !, &&, ||
- equality testing: ==, !=

- subexpression grouping: ()
- order relations: <, <=, >, >=
- and --
- member selection: ., ->
- conditional evaluation: ?:

Summary

- C preprocessing
- C variables
- C operators