TIME TABLE FOR ODD SEMESTER 2019-20

ROOM NO : A 349
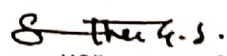
SEMESTER V 'A'   modified timetable.

DAY/TIME	8-9am	9-10am	10.30-11.30am	11-30-12.30pm	2-3 pm	3-4 pm	4-5 pm	5-6(Remedial)
MON	17EC563 RPN	17ECL57 A1 (SWS+ KGH) 17ECL58 A2 (Dr.GHL+ MC)			17EC552/553 SGR(A349)/ PGN(A251)	17ES51 KGH	17EC54 MKO/KMP	17EC52 SWS
TUE	17EC54 MKO/KMP	17EC563 RPN	17EC552/553 SGR(A349)/ PGN(A354)	17ES51 KGH	17EC52 SWS	TC 17ECL58 Dr.GHL	TC 17ECL57 SWS	17ES51 KGH
WED	17EC52 SWS	17EC54 MKO	17EC563 RPN	17EC552/553 SGR(A349)/ PGN(A354)	17EC53 Dr.GHL	17ES51 KGH	17EC54 MKO (A251)	17EC552/553 SGR/PGN (Remedial) (A254/349)
THU	17ES51 KGH	17EC52 SWS	17EC53 Dr.GHL	17EC563 RPN	17ECL57 A2 (SWS+Dr.BMJ) 17ECL58 A3 (Dr.GHL+KGH)			17EC54 MKO
FRI	17EC53 Dr.GHL	17ECL57 A3 (SWS+ GK) 17ECL58 A1 (Dr.GHL+ MKO)			17EC53 Dr.GHL	17EC563 RPN	17EC52 SWS	17EC52 SWS
SAT	17EC552/553 SGR(A349)/ PGN(A254)	17EC53 Dr.GHL						

Sl.No.	Subject Code	Name of the Subject	Initials of the Staff	Name of the Staff		
1	17ES51	Management and Entrepreneurship Development	KGH	Kiran Kumar G. H.	17.12.19	9.30-12.30
2	17EC52	Digital Signal Processing	SWS	Shwetha S.	20.12.19	postponed 28.12.19
3	17EC53	Verilog HDL	Dr.GHL	Dr. G. H. Leela	26.12.19	
4	17EC54	Information Theory and Coding	MKO	Manjesh K. O.	30.12.19	
5		Professional Elective 1				
	17EC552	Switching and Finite Automata Theory	SGR	Savithri G. R.	02.01.20	
	17EC553	Operating System	PGN	Poornima G. N.		
6		Open Elective 1				
	17EC563	8051 Microcontroller	RPN	Ranjit Patil N.	06.01.20	
7	17ECL57	Digital Signal Processing Lab	SWS	Shwetha S.		
8	17ECL58	HDL Lab	Dr.GHL	Dr. G. H. Leela		

Time Table Incharge

HOD   22.08.19