

BAPUJI EDUCATIONAL ASSOCIATION (Regd)
BAPUJI INSTITUTE OF ENGINEERING & TECHNOLOGY, DAVANGERE
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TIME TABLE FOR EVEN SEMESTER 2019-20

SEMESTER VI 'B'


ROOM NO : A 349

DAY/TIME	8-9am	9-10am	10.30-11.30am	11-30-12.30pm	2-3 pm	3-4 pm	4-5 pm	5-6(Remedial)
MON	17EC663 Dr. NSO	17EC62 Dr. GHL	17EC61 GK	17EC63 BKL				17EC62 Remedial Dr. GHL
TUE		17EC663 Dr. NSO	17EC62 Dr. GHL	17EC653/5 KGHLR A349/A 354	17EC63 BKL	17EC64 RYR	17ECL67 TC Dr.GHL/MKO	17EC61 GK
WED	17EC61 GK	17EC64 RYR	17EC663 Dr. NSO	17EC653/5 KGHLR A 251/A 354	17ECL67 B2 (MKO+GK) 17ECL68 B3 (BS+RYR)			17EC63 BKL
THU	17EC63 BKL	17ECL67 B3 (MKO+DP) 17ECL68 B1 (BS+PGN)			17EC62 Dr. GHL	17EC61 GK	17EC64 RYR	17EC651/3/5 LR/KGH/RYR
FRI	17EC62 Dr. GHL	17EC63 BKL	17EC653/5 A133/A 354 KGHLR	17ECL68 TC BS/PGN	17ECL67 B1 (MKO+Dr.GHL) 17ECL68 B2 (BS+PGN)			17EC64 RYR
SAT	17EC64 RYR	17EC61 GK						

Sl.No.	Course Code	Course Title	Initials of the Staff	Course Coordinator
1	17EC61	Digital Communication	GK	Ganesh K.
2	17EC62	ARM Microcontroller & Embedded Systems	Dr. GHL	Dr. G. H. Leela
3	17EC63	VLSI Design	BKL	Banumathi K. L.
4	17EC64	Computer Communication Networks	RYR	Radhikapriya Y R
		Professional Elective-2		
5	17EC653	Artificial Neural Networks	KGH	Kiran Kumar G. H.
	17EC655	Microelectronics	LR	Lingaraj
		Open Elective-2		
6	17EC663	Digital System Design using Verilog	Dr. NSO	Dr. Nirmala S. O.
7	17ECL67	Embedded Controller Lab	MKO	Manjes K. O.
8	17ECL68	Computer Networks Lab	BS	Bhagya S.


Time Table Coordinator


Program Coordinator


Principal