TIME TABLE FOR EVEN SEMESTER 2019-20
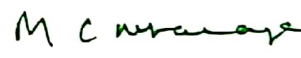
SEMESTER VI 'A'                                                      ROOM NO : A 251/A354

| DAY/TIME | 8-9am | 9-10am | 10.30-11.30am | 11-30-12.30pm | 2-3 pm | 3-4 pm | 4-5 pm | 5-6(Remedial) |
|---|---|---|---|---|---|---|---|---|
| MON | 17EC64 NG (A 251) | 17EC61 Dr. AKG (A 251) | 17EC663 SGR (A 251) | 17ECL68 TC NG (A 251) | 17ECL67 A1 (BKL+Dr.AKG) 17ECL68 A2 (NG+SGR) | | | 17EC61 Dr. AKG |
| TUE | 17EC663 SGR (A 251) | 17EC61 Dr. AKG (A 251) | 17EC62 BKL (A 251) | 17EC653/5 KGH/LR A349/A 354 | 17EC63 Dr. NSO (A 354) | 17EC64 NG (A 354) | 17EC63 Remedial Dr. NSO | 17EC62 BKL |
| WED | 17EC62 BKL (A 354) | 17EC63 Dr. NSO (A 354) | 17EC663 SGR (A 354) | 17EC653/5 KGH/LR A 251/A 354 | | | | |
| THU | 17EC61 Dr. AKG (A 354) | 17EC63 Dr. NSO (A 354) | 17EC62 BKL (A 251) | 17EC64 NG (A 251) | 17ECL67 A2 (BKL+MKO) 17ECL68 A3 (NG+KGH) | | | 17EC651/3/5 LR/KGH/RYR |
| FRI | | 17EC61 Dr. AKG (A 251) | 17EC653/5 A133/A 354 KGH/LR | 17EC63 Dr. NSO (A 354) | 17EC62 BKL (A 251) | 17EC64 NG (A 251) | 17ECL67 TC BKL (A 251) | 17EC64 NG |
| SAT | | | 17ECL67 A3 (BKL+DP) 17ECL68 A1 (NG+SGR) | | | | | |

| Sl.No. | Course Code | Course Title | Initials of the Staff | Course Coordinator |
|---|---|---|---|---|
| 1 | 17EC61 | Digital Communication | Dr. AKG | Dr. Avinash K. G. |
| 2 | 17EC62 | ARM Microcontroller & Embedded Systems | BKL | Banumathi K. L. |
| 3 | 17EC63 | VLSI Design | Dr. NSO | Dr. Nirmala S. O. |
| 4 | 17EC64 | Computer Communication Networks | NG | Nirmala G. |
| 5 | | Professional Elective-2 | | |
| | 17EC653 | Artificial Neural Networks | KGH | Kiran Kumar G. H. |
| | 17EC655 | Microelectronics | LR | Lingaraj |
| | | Open Elective-2 | | |
| 6 | 17EC663 | Digital System Design using Verilog | SGR | Savithri G. R. |
| 7 | 17ECL67 | Embedded Controller Lab | BKL | Banumathi K. L. |
| 8 | 17ECL68 | Computer Networks Lab | NG | Nirmala G. |

Time Table Coordinator       Program Coordinator       Principal