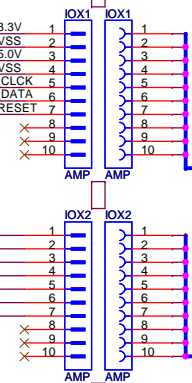
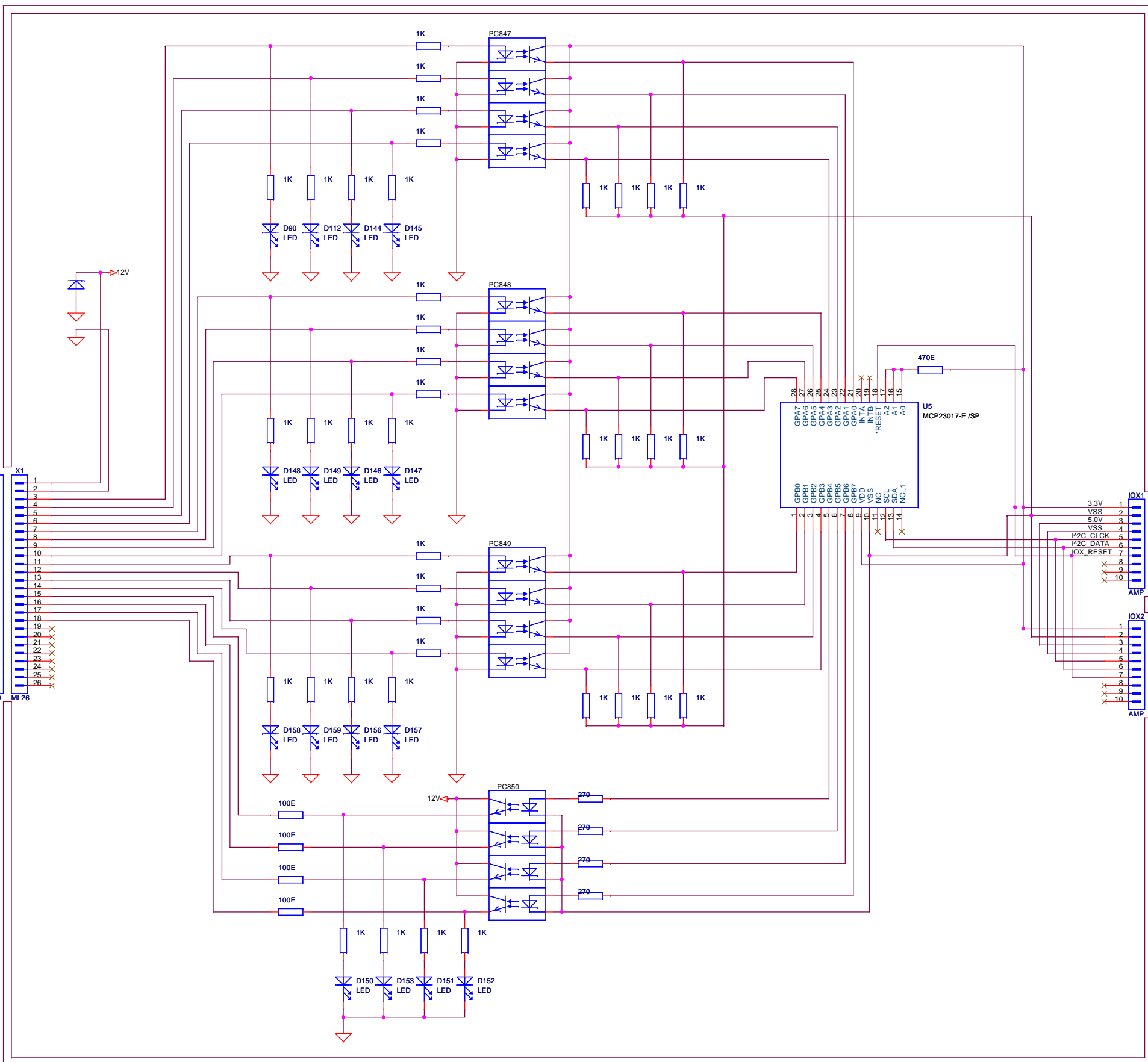
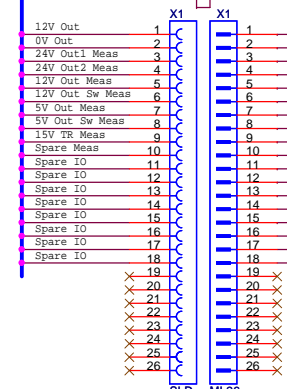


FY - PWM_SUPPLY_BOARD_PG1_66



FY - OPTO_BOARD_2_MISC_PG5_67

SPARE I^2C CONNECTION

FY - OPTO_BOARD_2_MISC_PG5

OPTO_BOARD_2_PG1

PORTBbits.RA0 = ETHERNET LED1
PORTBbits.RA1 = ETHERNET LED2
PORTBbits.RA2 = ADCONVERTER ENC TOP
PORTBbits.RA3 = ADCONVERTER ENC BOTTOM
PORTBbits.RA4 = Led1
PORTBbits.RA5 = Led2
3.3V, 2mA

PORTBbits.RB0 = SPARE_SWITCH_INPUT_TOP
PORTBbits.RB1 = SPARE_SWITCH_INPUT_BOTTOM
PORTBbits.RB2 = Led3
PORTBbits.RB3 = IO_Expander_Enable I²C
PORTBbits.RB4 = Output_Enable
PORTBbits.RB5 = Externall_WDT
PORTBbits.RB6 = INCIRCUIT PROGRAM/DEBUG
PORTBbits.RB7 = INCIRCUIT PROGRAM/DEBUG
5.0V, 25mA

PORTDbits.RD0 = M10_ Top
PORTDbits.RD1 = M11_ Top
PORTDbits.RD2 = Bezet_Weerstand_ Top
PORTDbits.RD3 = M10_ Bottom
PORTDbits.RD4 = M11_ Bottom
PORTDbits.RD5 = SERIAL CLOCK I²C
PORTDbits.RD6 = SERIAL DATA I²C
PORTDbits.RD7 = Bezet_Weerstand_ Bottom
5.0V, 8mA

FY - OPTO_BOARD_1_SWBOT_PG4

OPTO_BOARD_1_BOT_PG1

PORTCbits.RC0 = Brake Bottom
PORTCbits.RC1 = Bridge_10L_ Bottom
PORTCbits.RC2 = PWM Bottom
PORTCbits.RC3 = Bridge_10R_ Bottom
PORTCbits.RC4 = F10_ Bottom
PORTCbits.RC5 = F11_ Bottom
PORTCbits.RC6 = F12_ Bottom
PORTCbits.RC7 = EOS_10_ Bottom
5.0V, 25mA

PORTFbits.RF0 = CL_10_ bit1 Bottom
PORTFbits.RF1 = CL_10_ bit2 Bottom
PORTFbits.RF2 = CL_10_ bit3 Bottom
PORTFbits.RF3 = CL_10_ bit4 Bottom
PORTFbits.RF4 = CL_10_Heart_ Bottom
PORTFbits.RF5 = BM_10_ Bottom
PORTFbits.RF6 = BM_11_ Bottom
PORTFbits.RF7 = EOS_11_ Bottom
3.3V, 2mA

FY - OPTO_BOARD_1_SWTOP_PG3

OPTO_BOARD_1_TOP_PG1

PORTGbits.RG0 = PWM Top
PORTGbits.RG1 = Bridge_10L_ Top
PORTGbits.RG2 = Brake Top
PORTGbits.RG3 = Bridge_10R_ Top
PORTGbits.RG4 = F10_ Top
PORTGbits.RG5 = F11_ Top
PORTGbits.RG6 = F12_ Top
PORTGbits.RG7 = EOS_10_ Top
5.0V, 2mA

PORTJbits.RJ0 = CL_10_ bit1 Top
PORTJbits.RJ1 = CL_10_ bit2 Top
PORTJbits.RJ2 = CL_10_ bit3 Top
PORTJbits.RJ3 = CL_10_ bit4 Top
PORTJbits.RJ4 = CL_10_Heart_ Top
PORTJbits.RJ5 = BM_10_ Top
PORTJbits.RJ6 = BM_11_ Top
PORTJbits.RJ7 = EOS_11_ Top
5.0V, 8mA

FY - OPTO_BOARD_3_OCC_PG7

OPTO_BOARD_3_PG1

PORTEbits.RE0 = OCC_IN_5B
PORTEbits.RE1 = OCC_IN_6
PORTEbits.RE2 = OCC_IN_7
PORTEbits.RE3 = OCC_IN_SPARE
PORTEbits.RE4 = OCC_IN_16B
PORTEbits.RE5 = OCC_IN_17
PORTEbits.RE6 = OCC_IN_18
PORTEbits.RE7 = OCC_IN_SPARE
5.0V, 8mA

PORTHbits.RH0 = OCC_OUT_5B
PORTHbits.RH1 = OCC_OUT_6
PORTHbits.RH2 = OCC_OUT_7
PORTHbits.RH3 = OCC_OUT_8A
PORTHbits.RH4 = OCC_OUT_16B
PORTHbits.RH5 = OCC_OUT_17
PORTHbits.RH6 = OCC_OUT_18
PORTHbits.RH7 = OCC_OUT_19A
3.3V, 2mA

Title			
FY - IO_MAPPING_OF_uC			
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A4	<Doc>		1.0
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