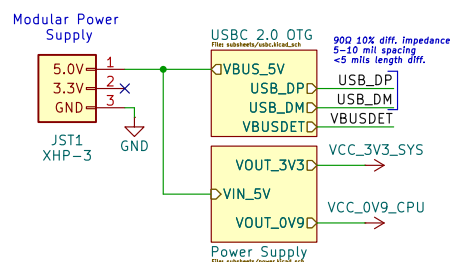
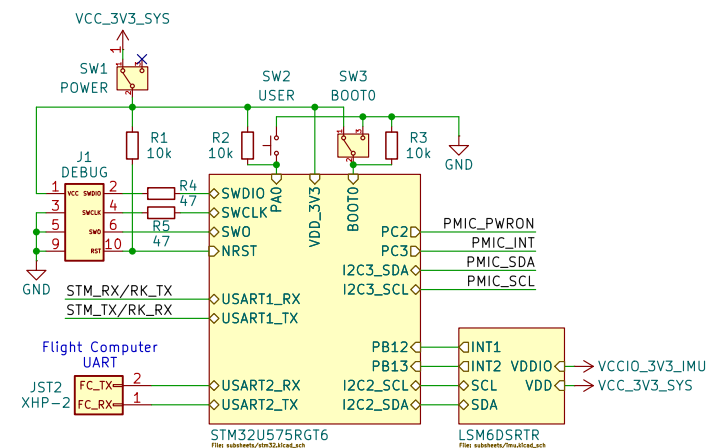


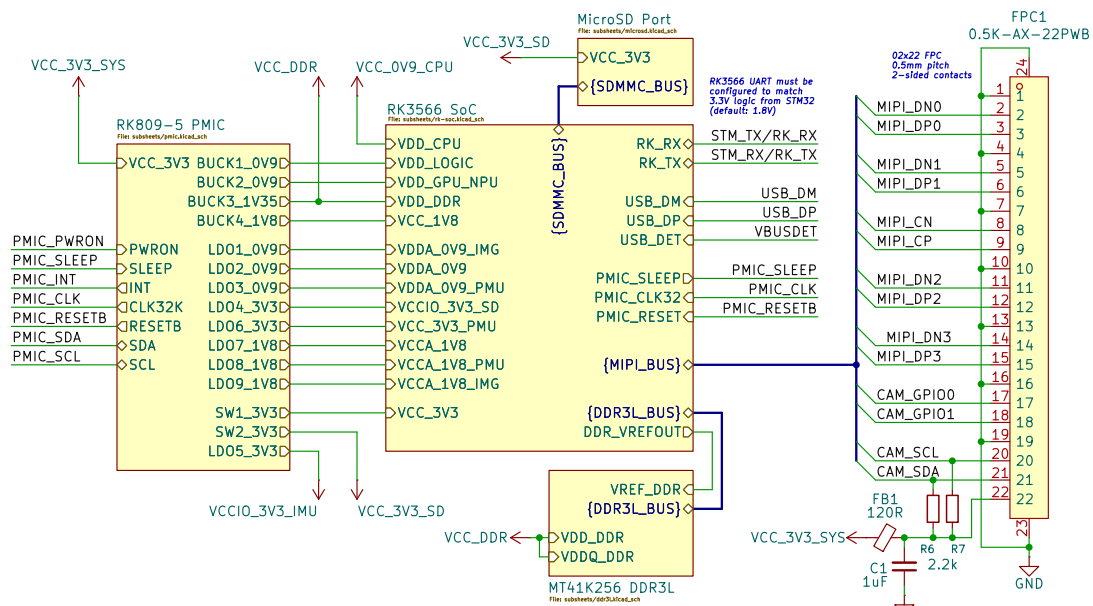
USB PORT & POWER SUPPLY



STM32 MCU & INERTIAL MEASUREMENT UNIT



RK809-5 PMIC, RK3566 SOC & PERIPHERALS



Author: Eric Handley

UVic Rocketry

Sheet: /

File: srad-camera.kicad_sch

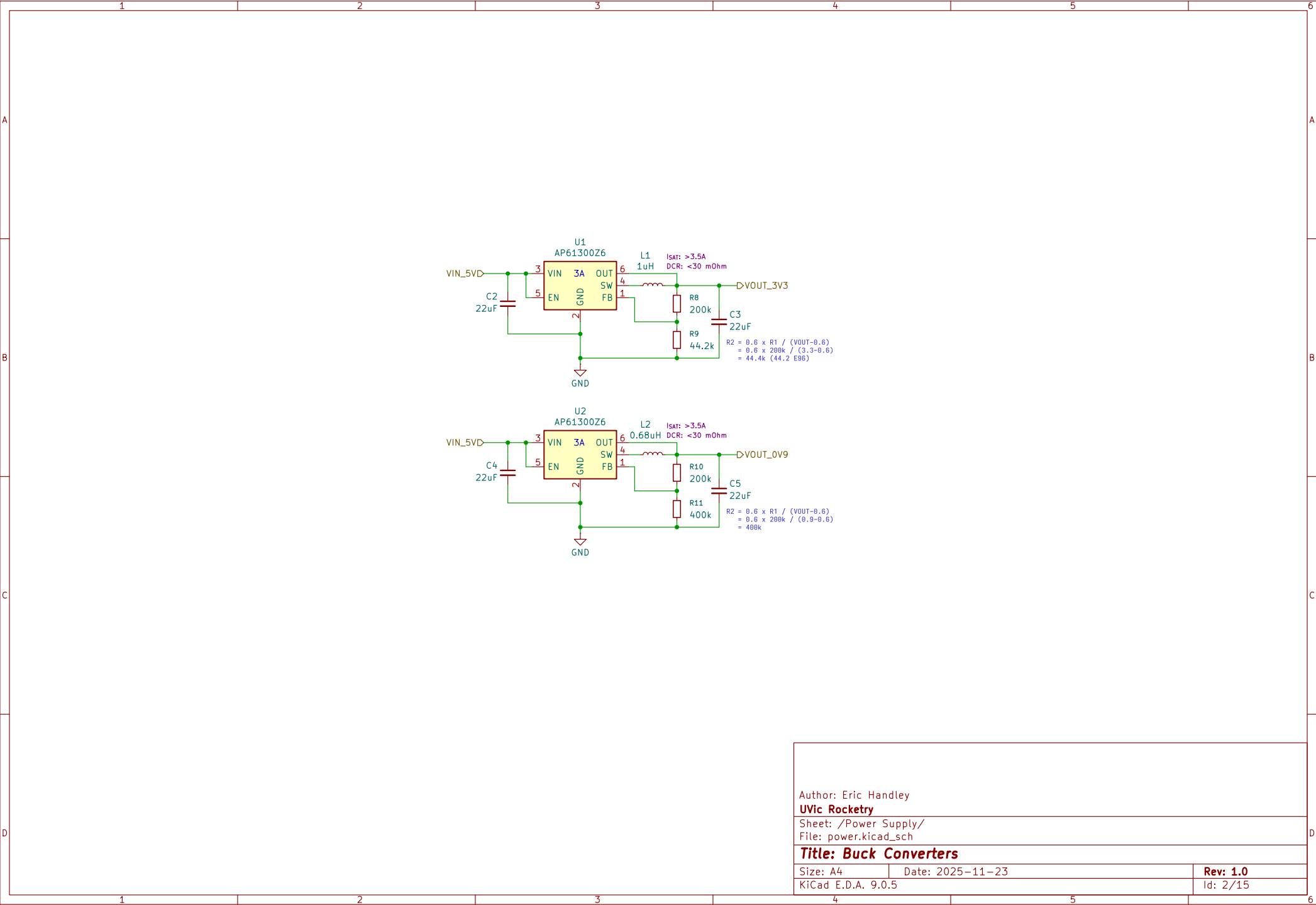
Title: SRAD Camera

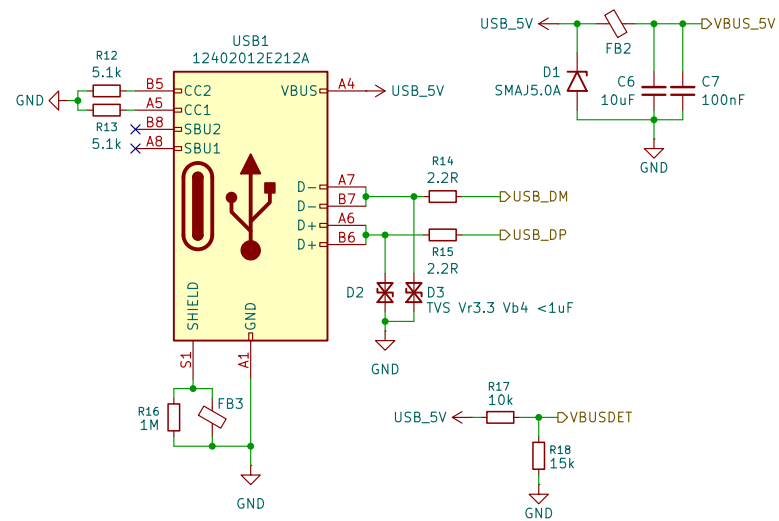
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| Size: A4 | Date: 2025-11-23 |
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Rev: 1.0

Id: 1/15



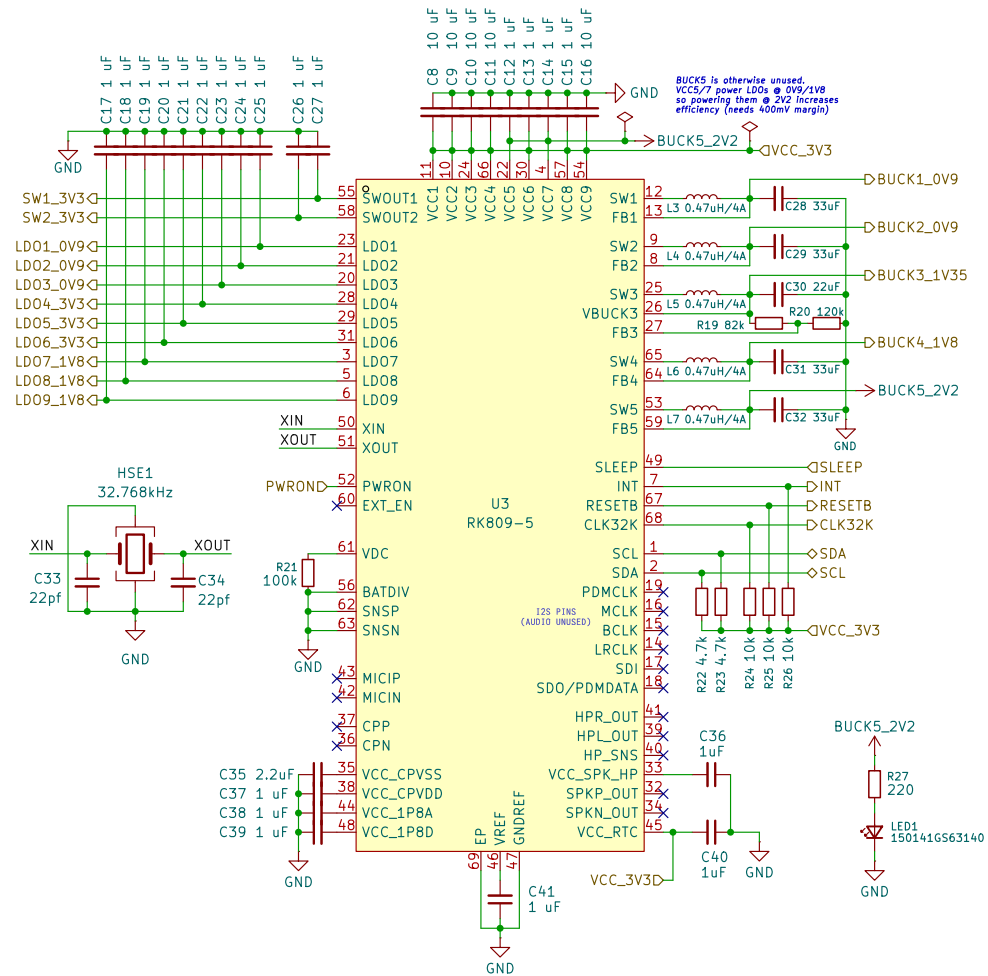


Author: Eric Handley
UVic Rocketry

Sheet: /USBC 2.0 OTG/
 File: usbc.kicad_sch

Title: USB C Port

| | | |
|--------------------|------------------|----------|
| Size: A4 | Date: 2025-11-23 | Rev: 1.0 |
| KiCad E.D.A. 9.0.5 | | Id: 3/15 |



BUCK5 is otherwise unused.
VCC5/7 power LDOs @ 0V9/1V8
so powering them @ 2V2 increases
efficiency (needs 400mV margin)

Author: Eric Handley

UVic Rocketry

Sheet: /RK809-5 PMIC/

File: pmic.kicad_sch

Title: Power Management IC

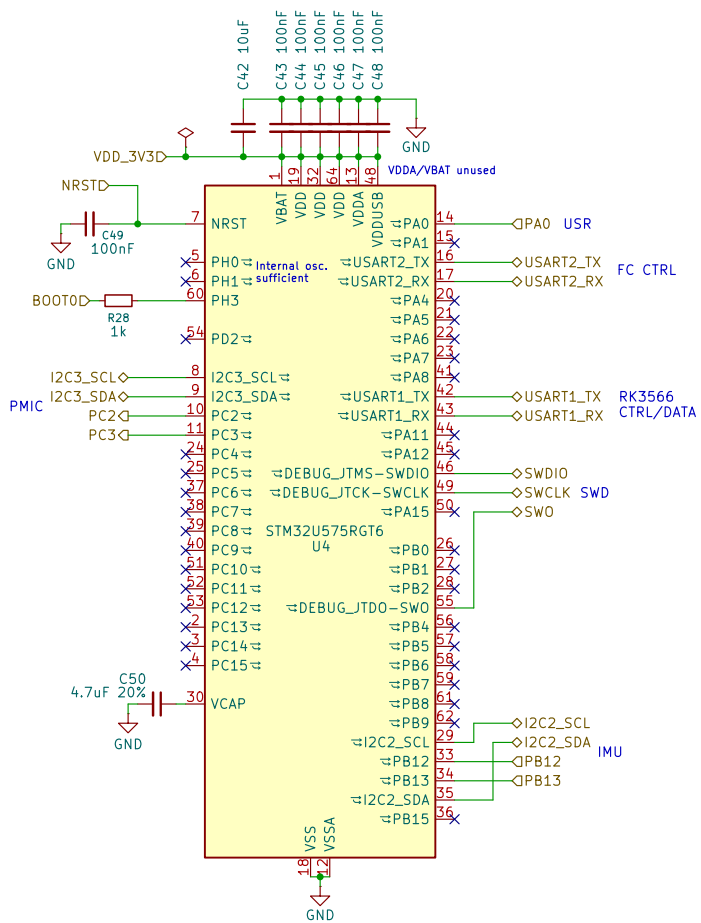
Size: A4

Date: 2025-11-23

Rev: 1.0

KiCad E.D.A. 9.0.5

Id: 4/15



Author: Eric Handley

UVic Rocketry

Sheet: /STM32U575RGT6/

File: stm32.kicad_sch

Title: STM32U575RGT6 Microcontroller

Size: A4

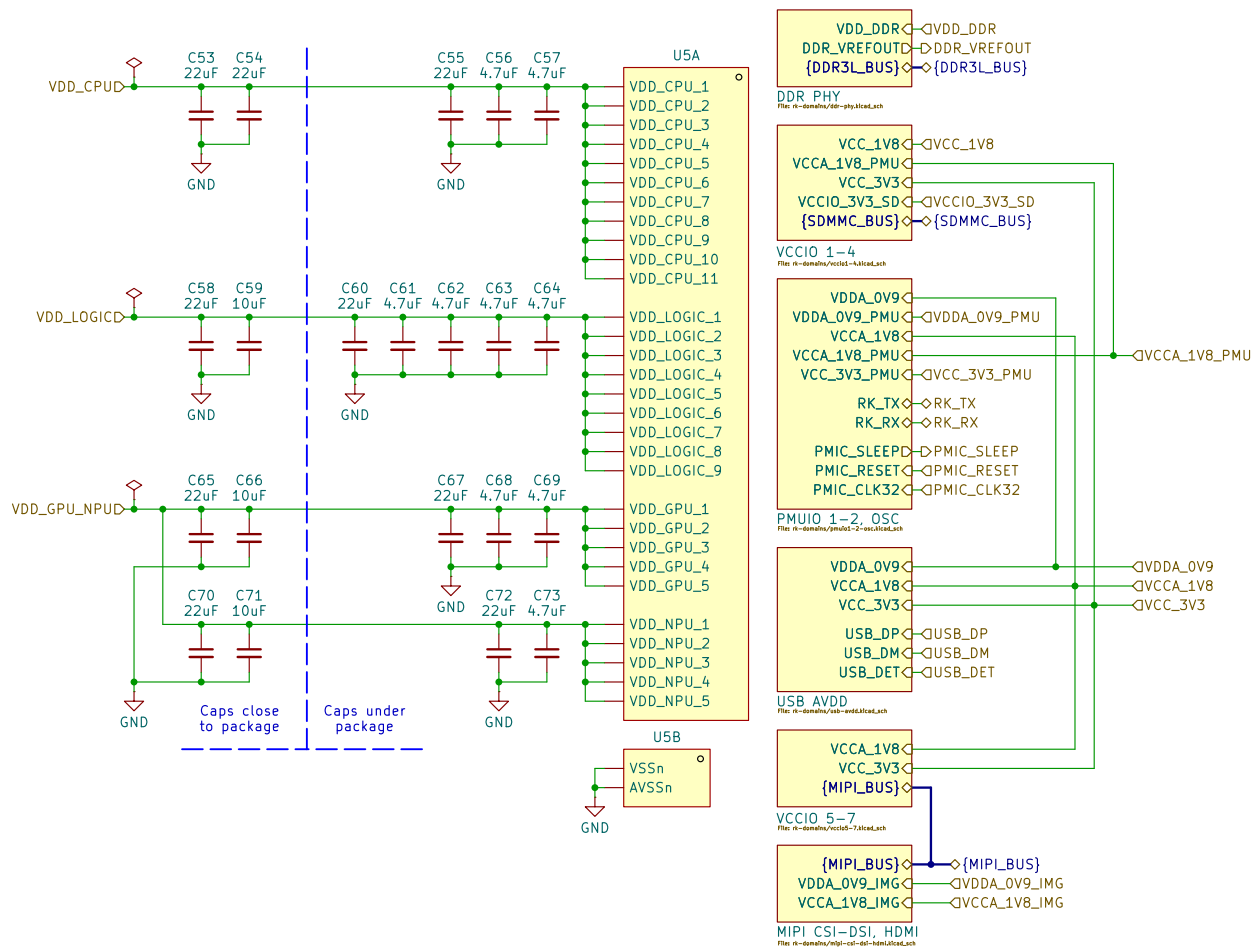
Date: 2025-11-23

Rev: 1.0

KiCad E.D.A. 9.0.5

Id: 5/15

| | | |
|--------------------|------------------|----------|
| Size: A4 | Date: 2025-11-23 | Rev: 1.0 |
| KiCad E.D.A. 9.0.5 | | Id: 6/15 |



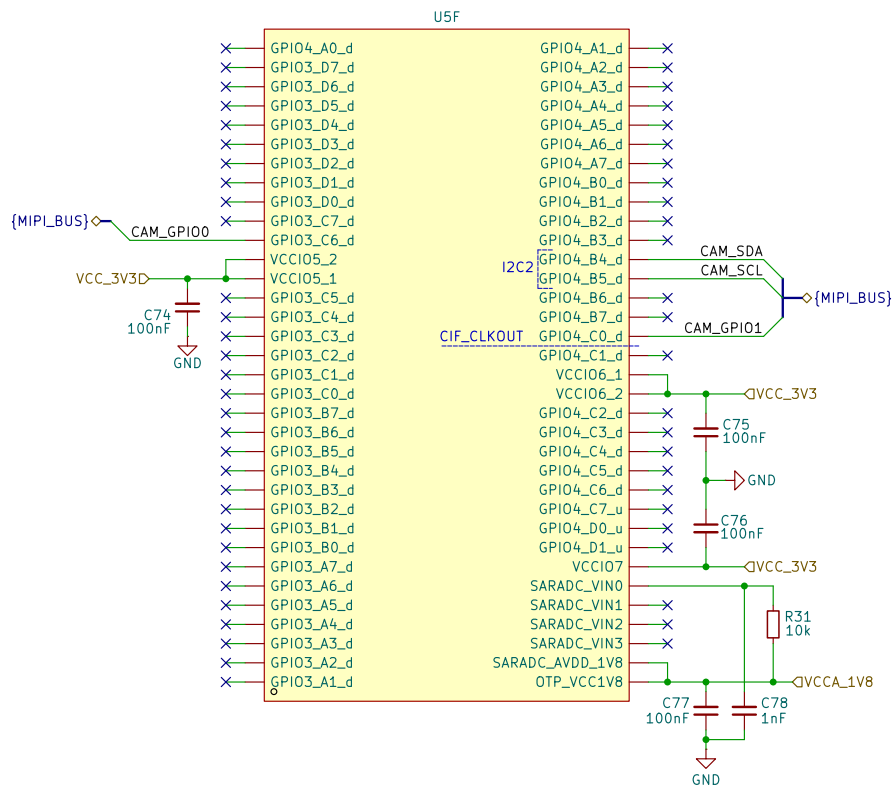
Author: Eric Handley
UVic Rocketry

Sheet: /RK3566 SoC/
 File: rk-soc.kicad_sch

Title: RK3566 SoC

Size: A4 Date: 2025-11-23
 KiCad E.D.A. 9.0.5

Rev: 1.0
 Id: 7/15



Author: Eric Handley

UVic Rocketry

Sheet: /RK3566 SoC/VCCIO 5-7/

File: vccio5-7.kicad_sch

Title: VCCIO 5/6/7

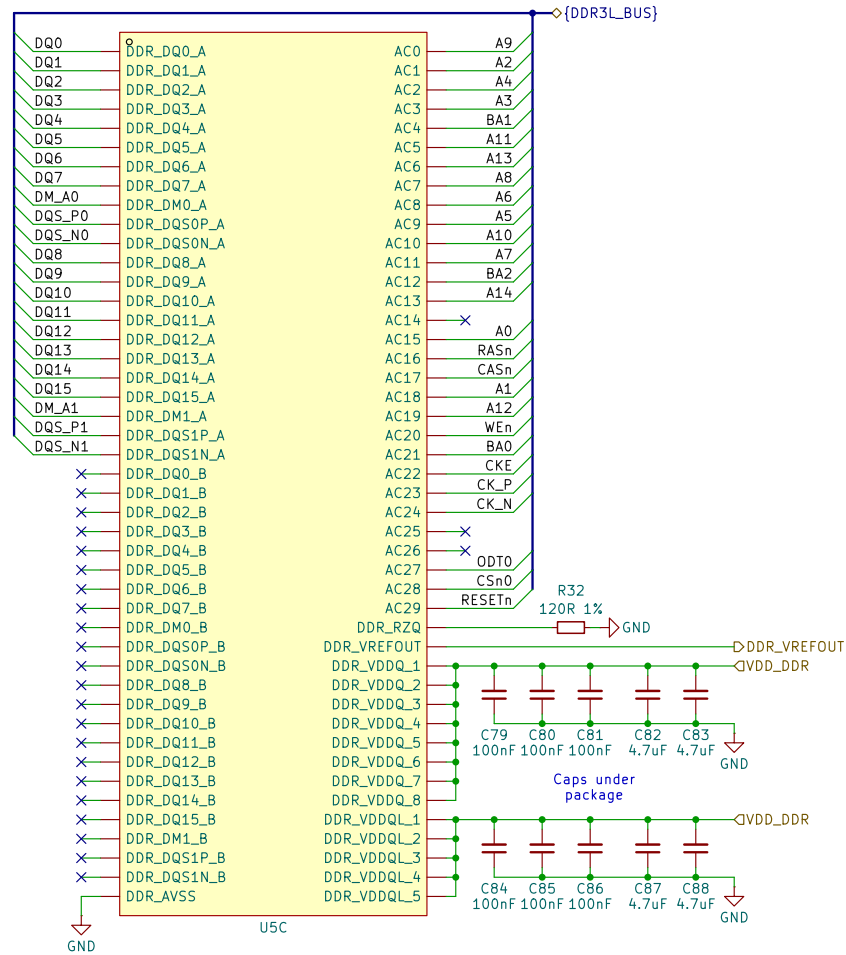
Size: A4

Date: 2025-11-23

KiCad E.D.A. 9.0.5

Rev: 1.0

Id: 8/15



Author: Eric Handley

UVic Rocketry

Sheet: /RK3566 SoC/DDR PHY/

File: ddr-phy.kicad_sch

Title: DDR PHY

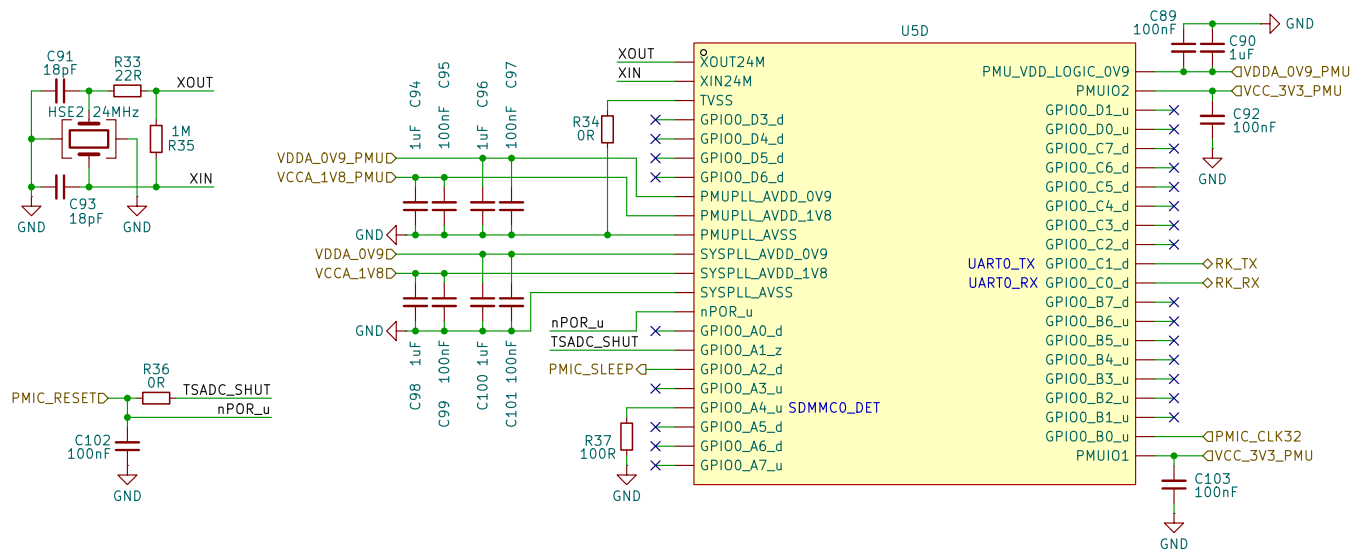
Size: A4

Date: 2025-11-23

KiCad E.D.A. 9.0.5

Rev: 1.0

Id: 9/15



Author: Eric Handley

UVic Rocketry

Sheet: /RK3566 SoC/PMUIO 1-2, OSC/

File: pmuio1-2-osc.kicad_sch

Title: PMUIO 1/2, OSC

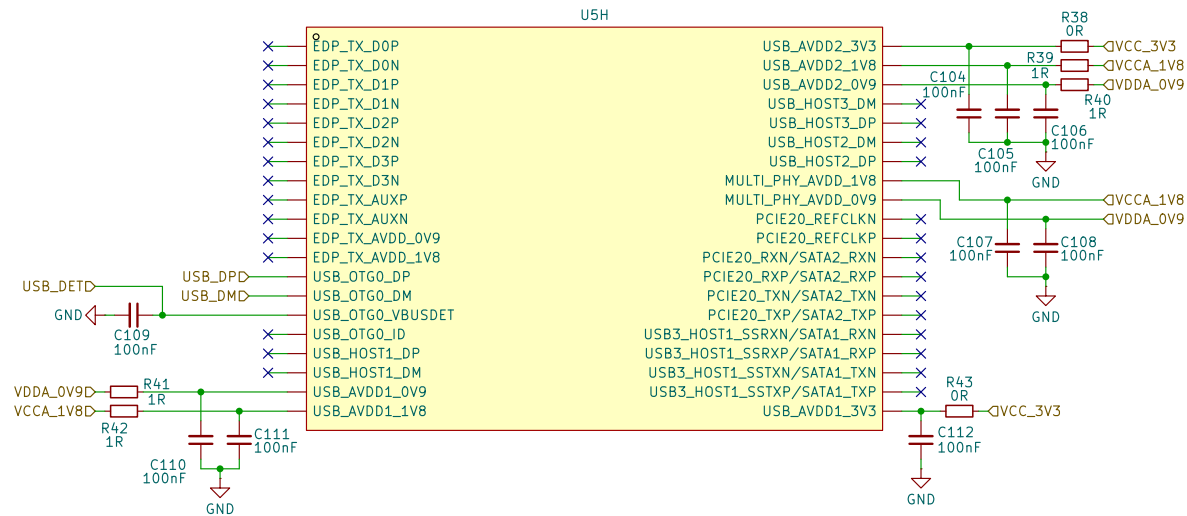
Size: A4

Date: 2025-11-23

Rev: 1.0

KiCad E.D.A. 9.0.5

Id: 10/15



Author: Eric Handley

UVic Rocketry

Sheet: /RK3566 SoC/USB AVDD/

File: usb-avdd.kicad_sch

Title: USB AVDD

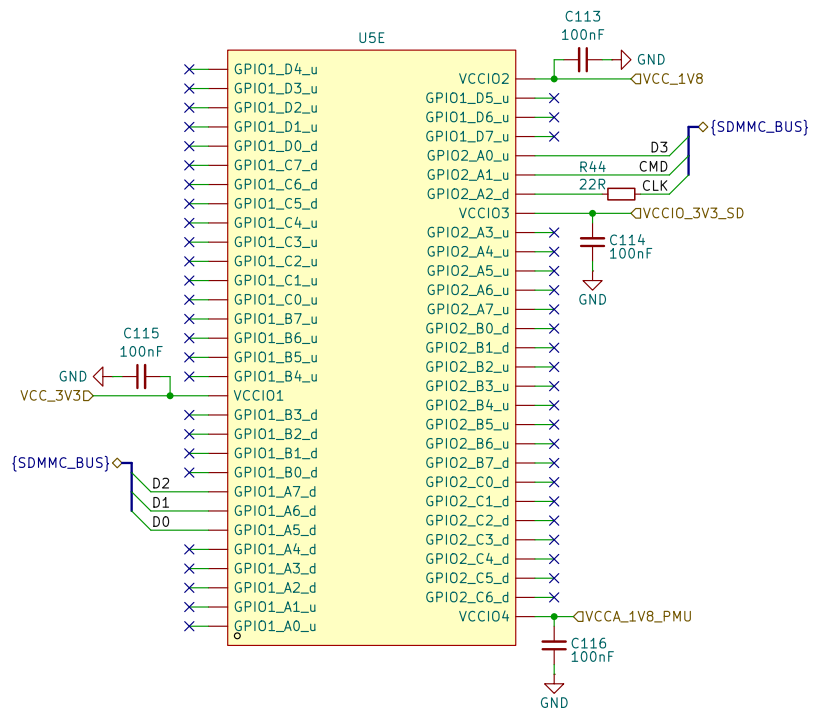
Size: A4

Date: 2025-11-23

Rev: 1.0

KiCad E.D.A. 9.0.5

Id: 11/15



Author: Eric Handley
UVic Rocketry

Sheet: /RK3566 SoC/VCCIO 1-4/
 File: vccio1-4.kicad_sch

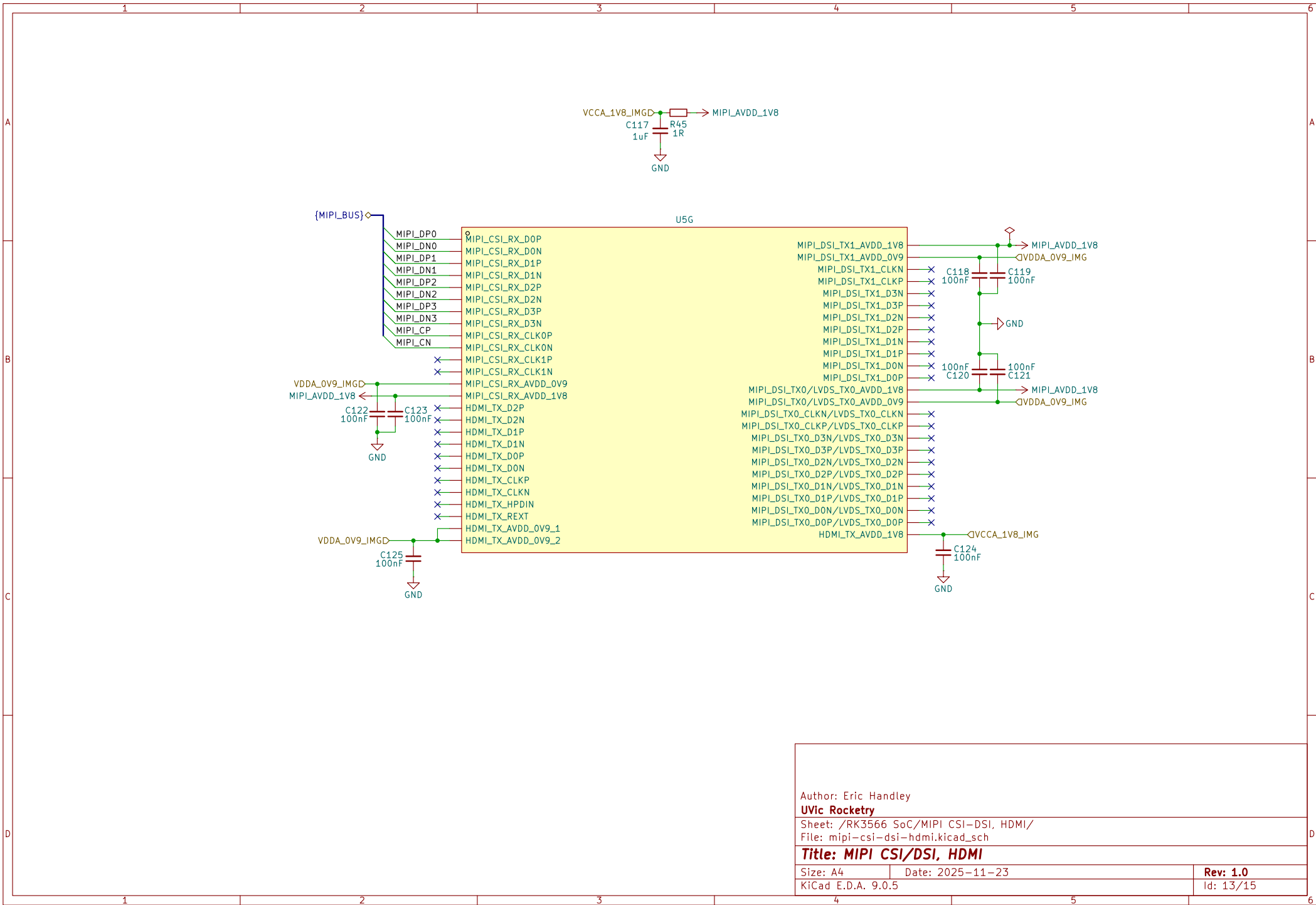
Title: VCCIO 1/2/3/4

Size: A4 Date: 2025-11-23

KiCad E.D.A. 9.0.5

Rev: 1.0

Id: 12/15



Author: Eric Handley

UVic Rocketry

Sheet: /RK3566 SoC/MIPI CSI-DSI, HDMI/

File: mipi-csi-dsi-hdmi.kicad_sch

Title: MIPI CSI/DSI, HDMI

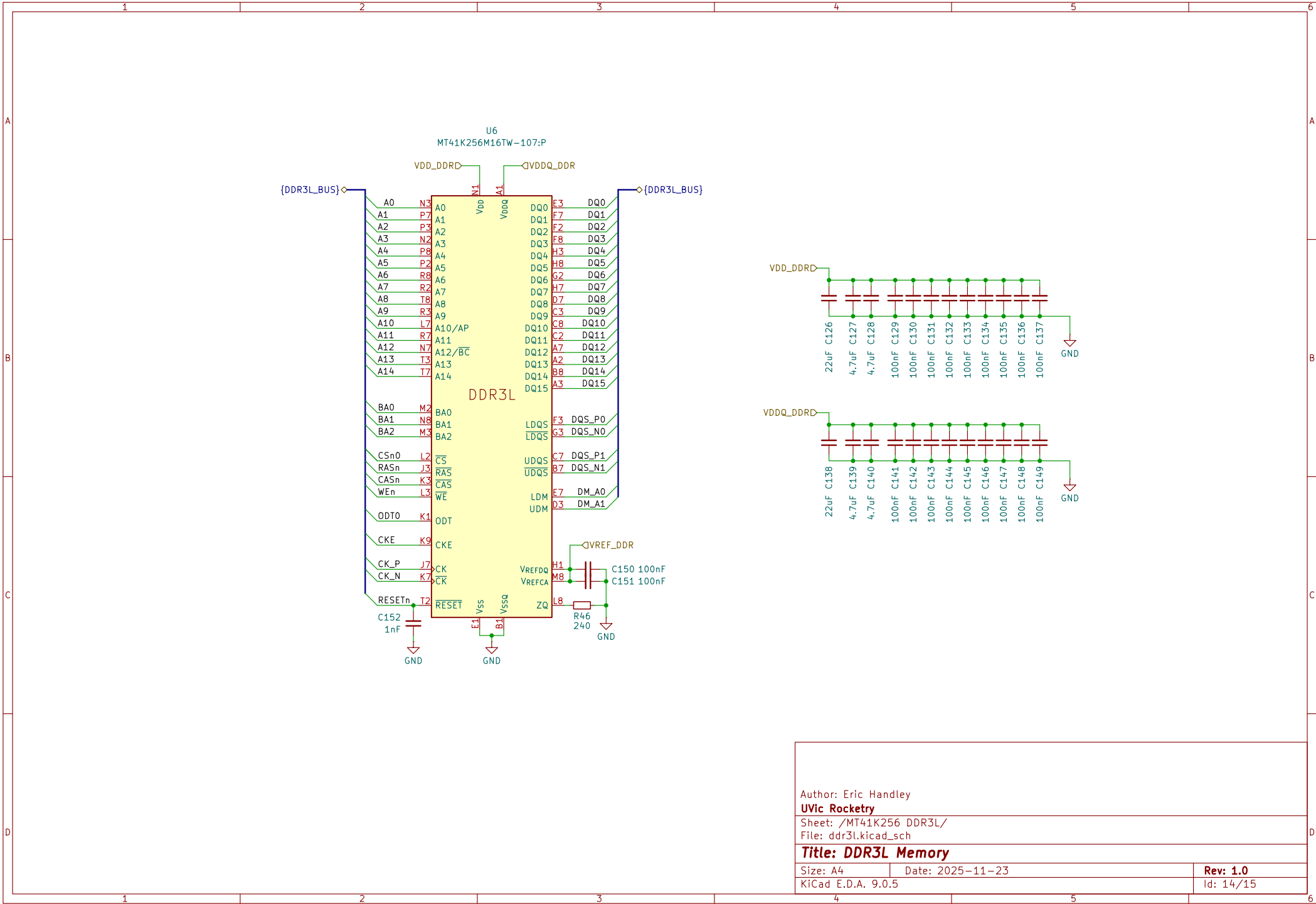
Size: A4

Date: 2025-11-23

Rev: 1.0

KiCad E.D.A. 9.0.5

Id: 13/15



Author: Eric Handley

UVic Rocketry

Sheet: /MT41K256 DDR3L/

File: ddr3l.kicad_sch

Title: DDR3L Memory

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| Size: A4 | Date: 2025-11-23 | Rev: 1.0 |
| KiCad E.D.A. 9.0.5 | | Id: 14/15 |

Id: 15/15