

A

A

DISCLAIMER

Raspberry Pi is a trademark of the Raspberry Pi Foundation

This is not a replacement for the official schematic and it was created for information and educational purposes only.

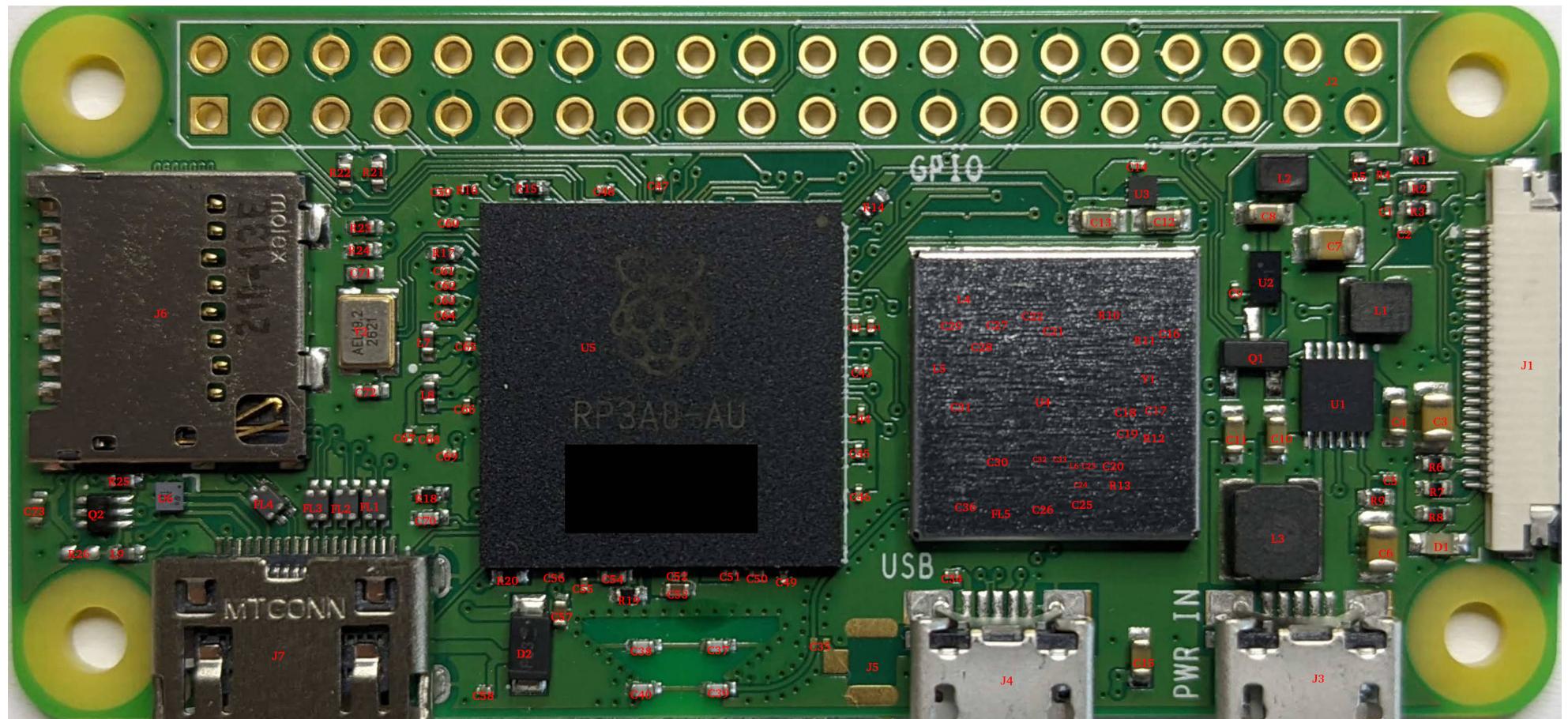
The information inside this schematic was created by using a best effort approach and making many assumptions (which may or may not be correct), therefore, there is no warranty about it's accuracy.

For information only, read DISCLAIMER
Probably contains errors
Minimal or no component values
Created by reverse engineering the PCB
iocapa

Sheet: /DISCLAIMER/
File: DISCLAIMER.sch

Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4	Date: 2022-01-15	Rev: V1.0
KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1	Id: 2/15	



For information only, read DISCLAIMER
Probably contains errors

Minimal or no component values
Created by reverse engineering the PCB

iocapa

Sheet: /TOP_DESIGNATORS/
File: TOP_DESIGNATORS.sch

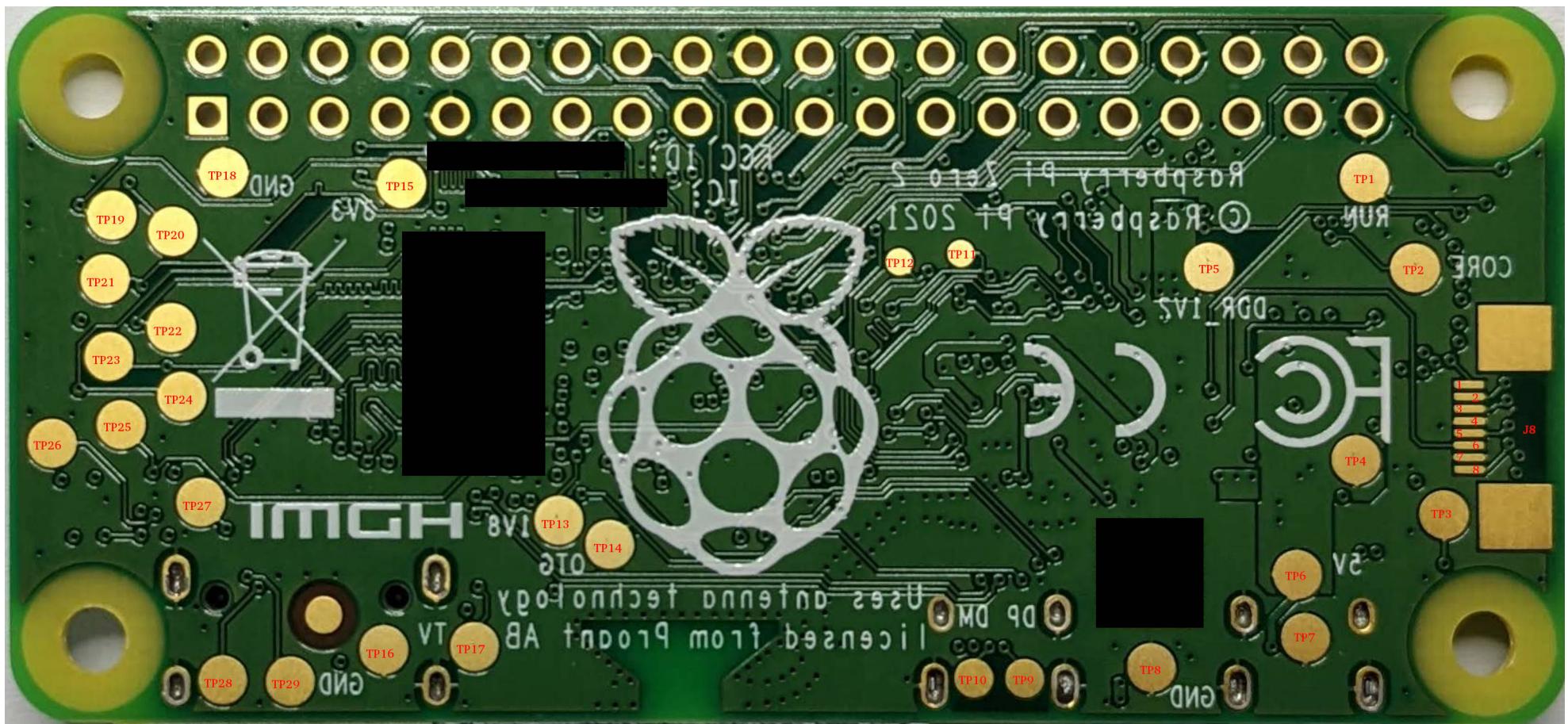
Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 3/15



For information only, read DISCLAIMER

Probably contains errors

Minimal or no component values

Created by reverse engineering the PCB

iocapa

Sheet: /BOTTOM_DESIGNATORS/

File: BOTTOM_DESIGNATORS.sch

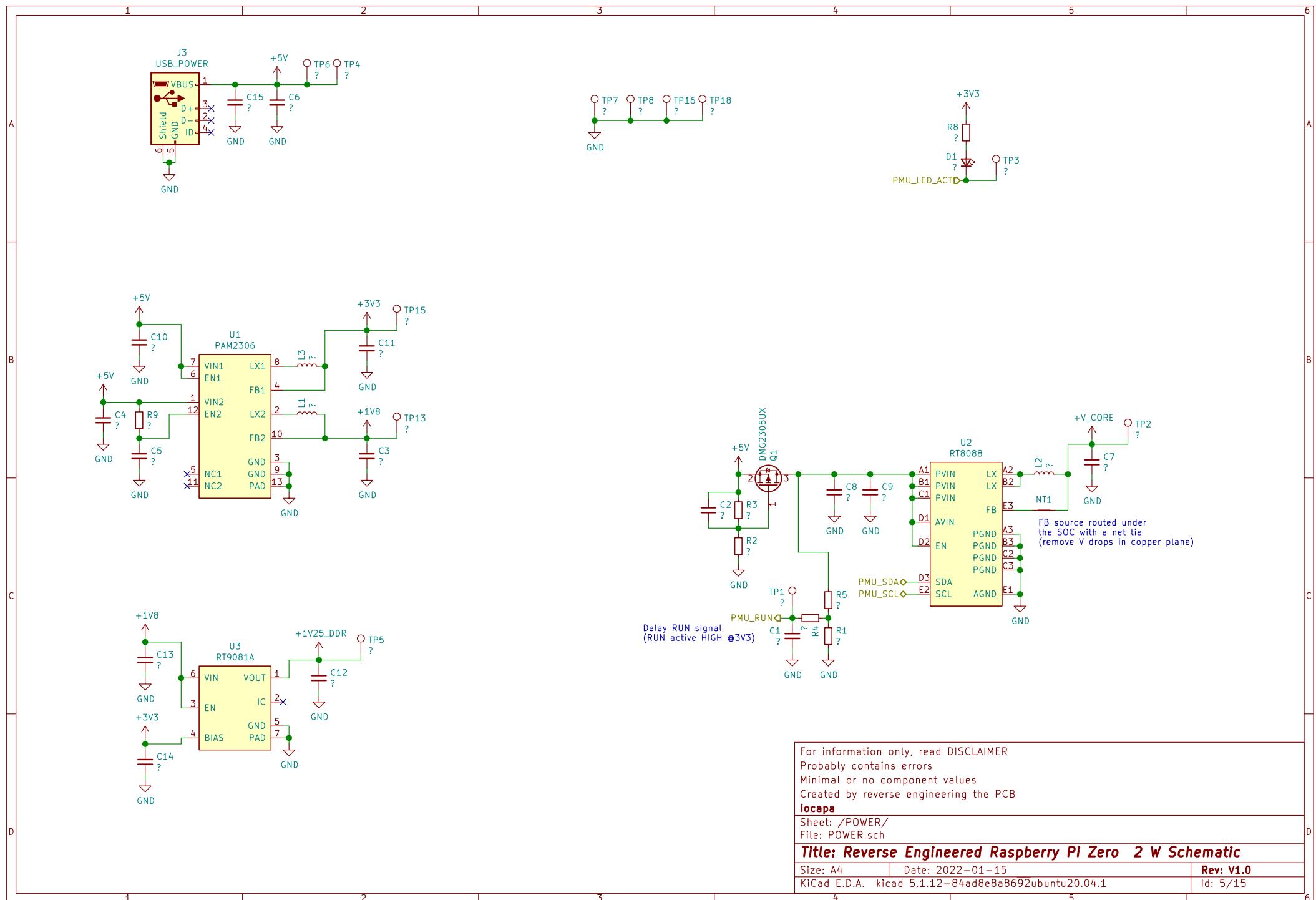
Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

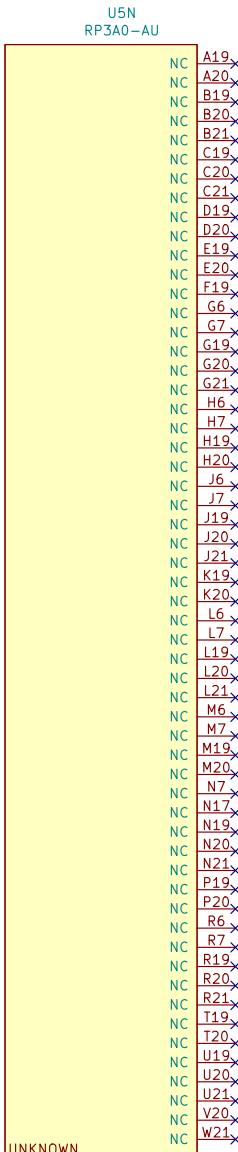
KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

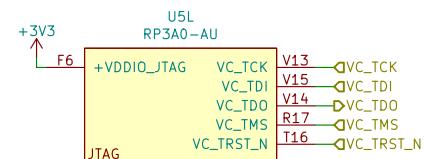
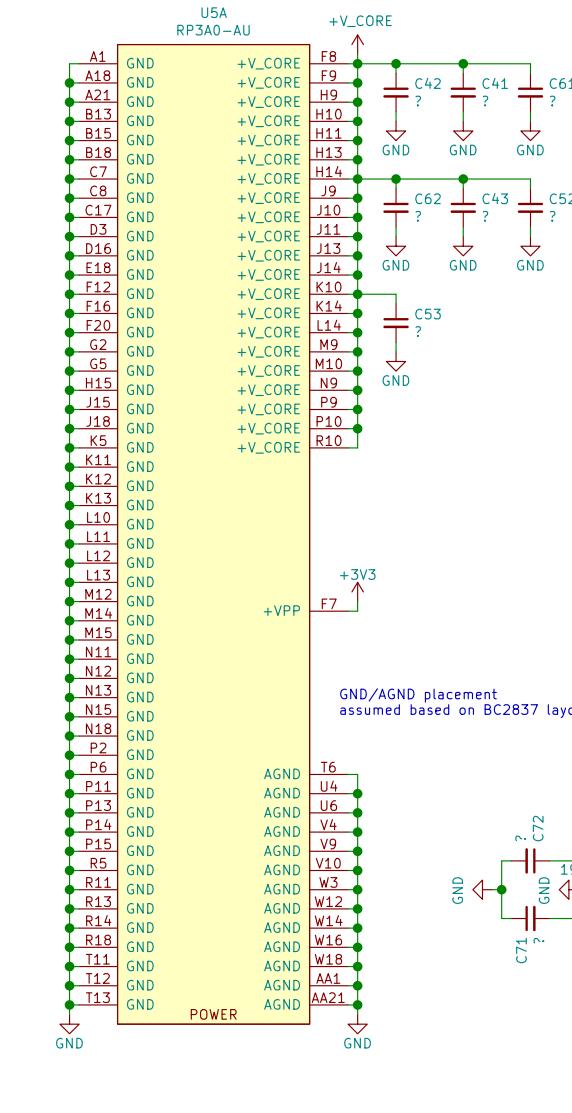
Id: 4/15



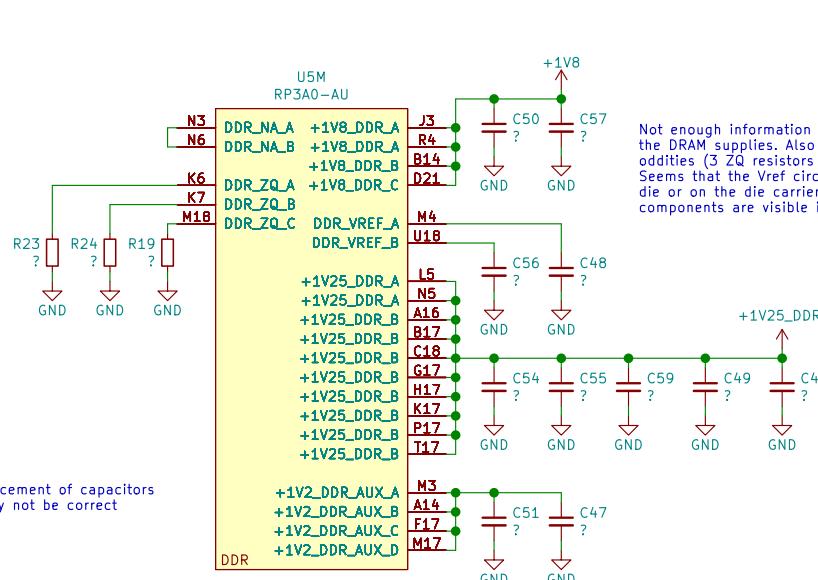
1 2 3 4 5 6



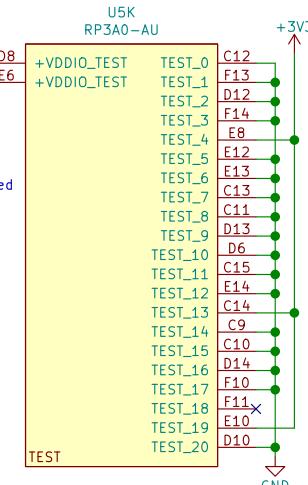
These are probably DRAM related
High probability that the SOC
has provisioning for an extra 512M
DRAM die



Placement of capacitors
may not be correct



Not enough information to decipher
the DRAM supplies. Also there are some
oddities (3 ZQ resistors for example).
Seems that the Vref circuit is either on
die or on the die carrier (some passive
components are visible in existing xrays).



For information only, read DISCLAIMER

Probably contains errors

Minimal or no component values

Created by reverse engineering the PCB

iocapa

Sheet: /SOC_SYS/

File: SOC_SYS.sch

Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

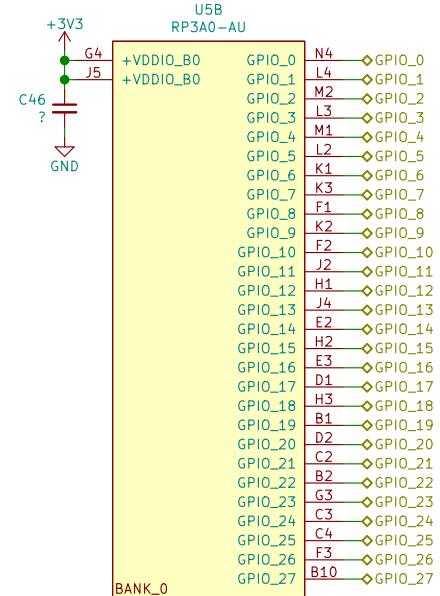
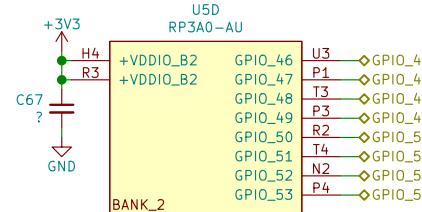
KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 6/15

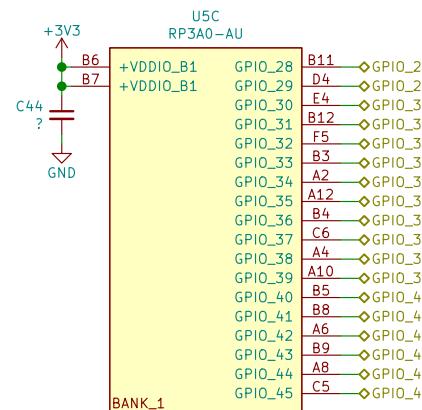
1 2 3 4 5 6

A



Some ball coordinates are assumed from .dts files and pinouts.
Capacitors are also assumed to be 1 per bank

B



For information only, read DISCLAIMER
Probably contains errors
Minimal or no component values
Created by reverse engineering the PCB
iocapa

Sheet: /SOC_GPIO/
File: SOC_GPIO.sch

Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

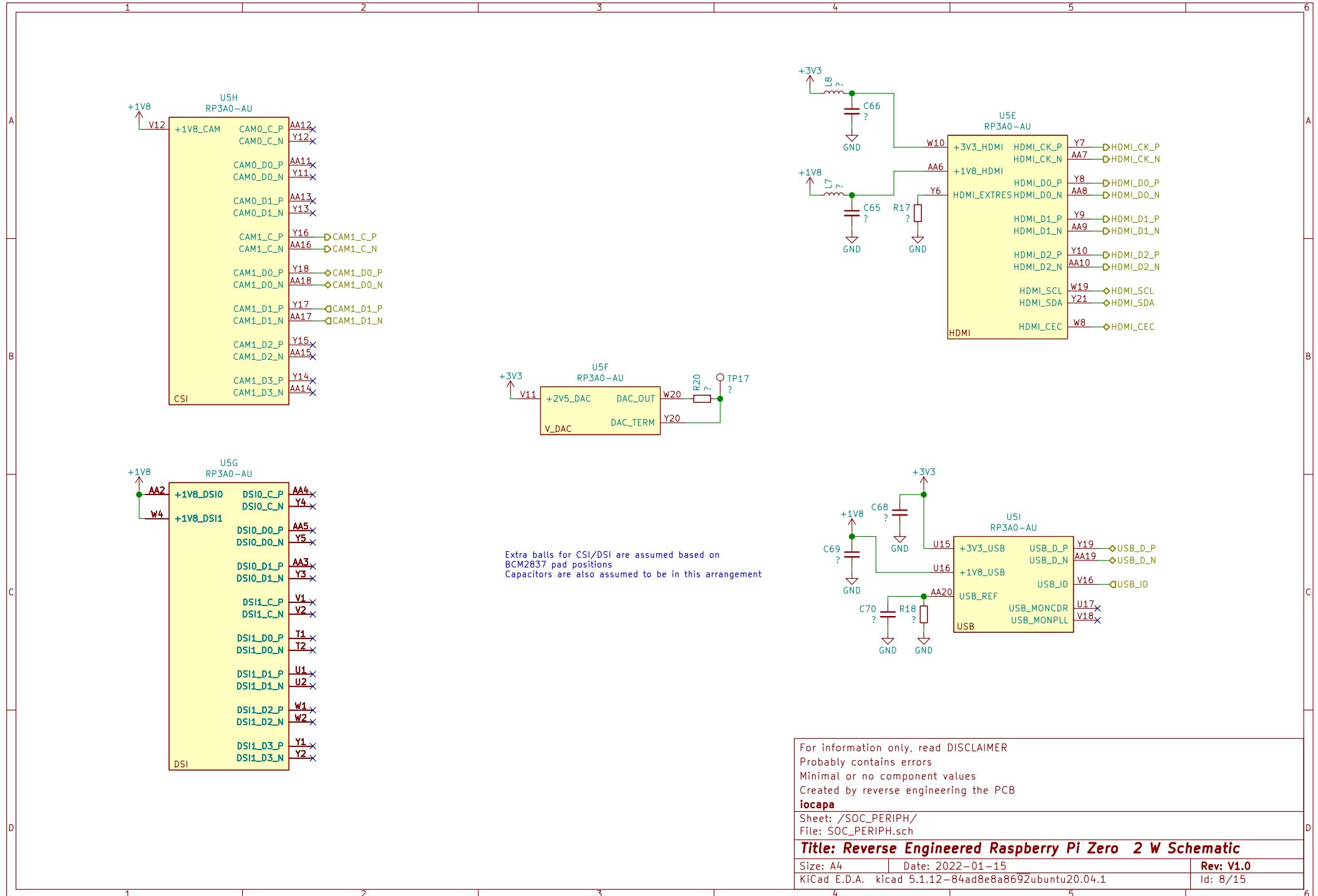
Rev: V1.0

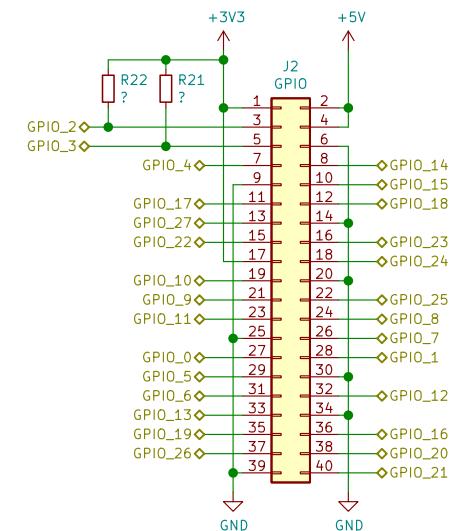
KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Id: 7/15

C

D





For information only, read DISCLAIMER
 Probably contains errors
 Minimal or no component values
 Created by reverse engineering the PCB
iocapa

Sheet: /GPIO/
 File: GPIO.sch

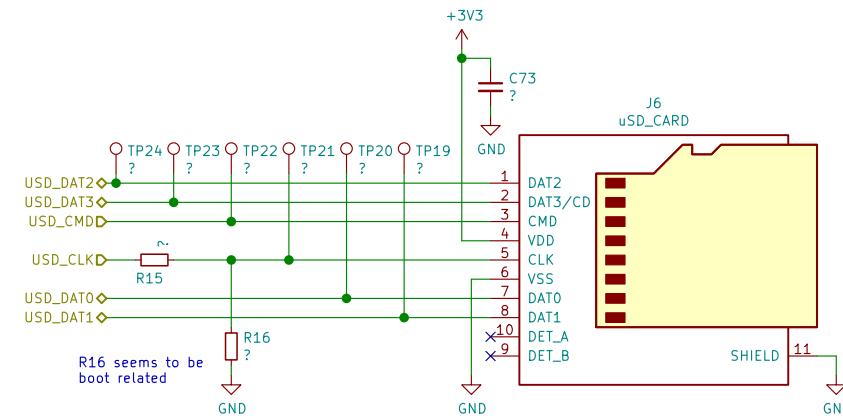
Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 9/15



For information only, read DISCLAIMER
 Probably contains errors
 Minimal or no component values
 Created by reverse engineering the PCB
iocapa

Sheet: /CARD/
 File: CARD.sch

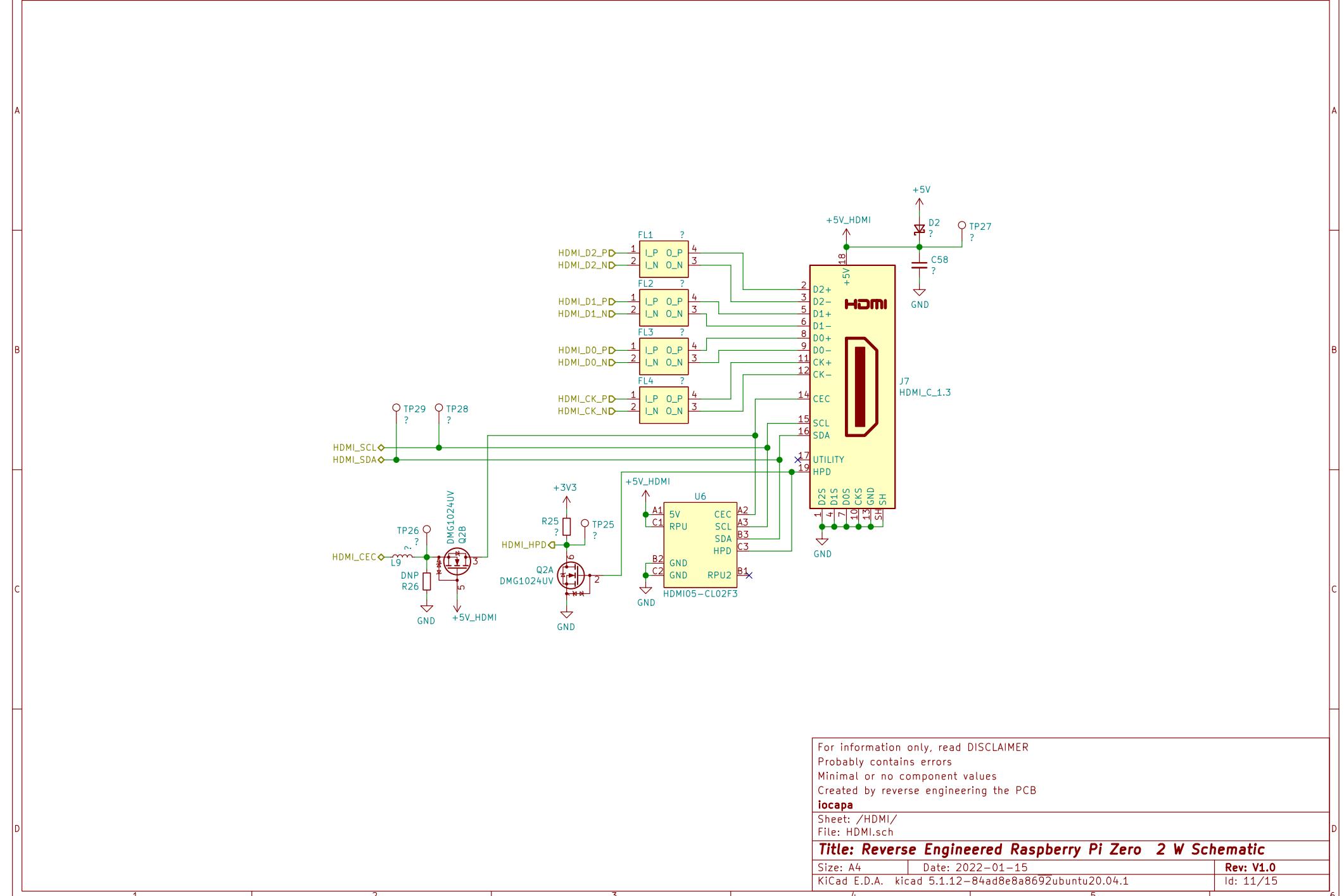
Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 10/15



A

A

B

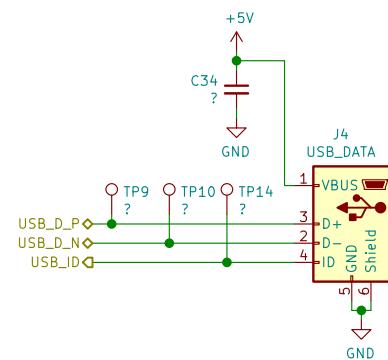
B

C

C

D

D



For information only, read DISCLAIMER
 Probably contains errors
 Minimal or no component values
 Created by reverse engineering the PCB
iocapa

Sheet: /USB/
 File: USB.sch

Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 12/15

A

B

C

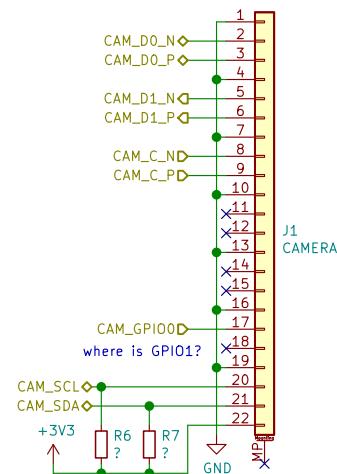
D

A

B

C

D



For information only, read DISCLAIMER
Probably contains errors

Minimal or no component values
Created by reverse engineering the PCB

iocapa

Sheet: /CAMERA/
File: CAMERA.sch

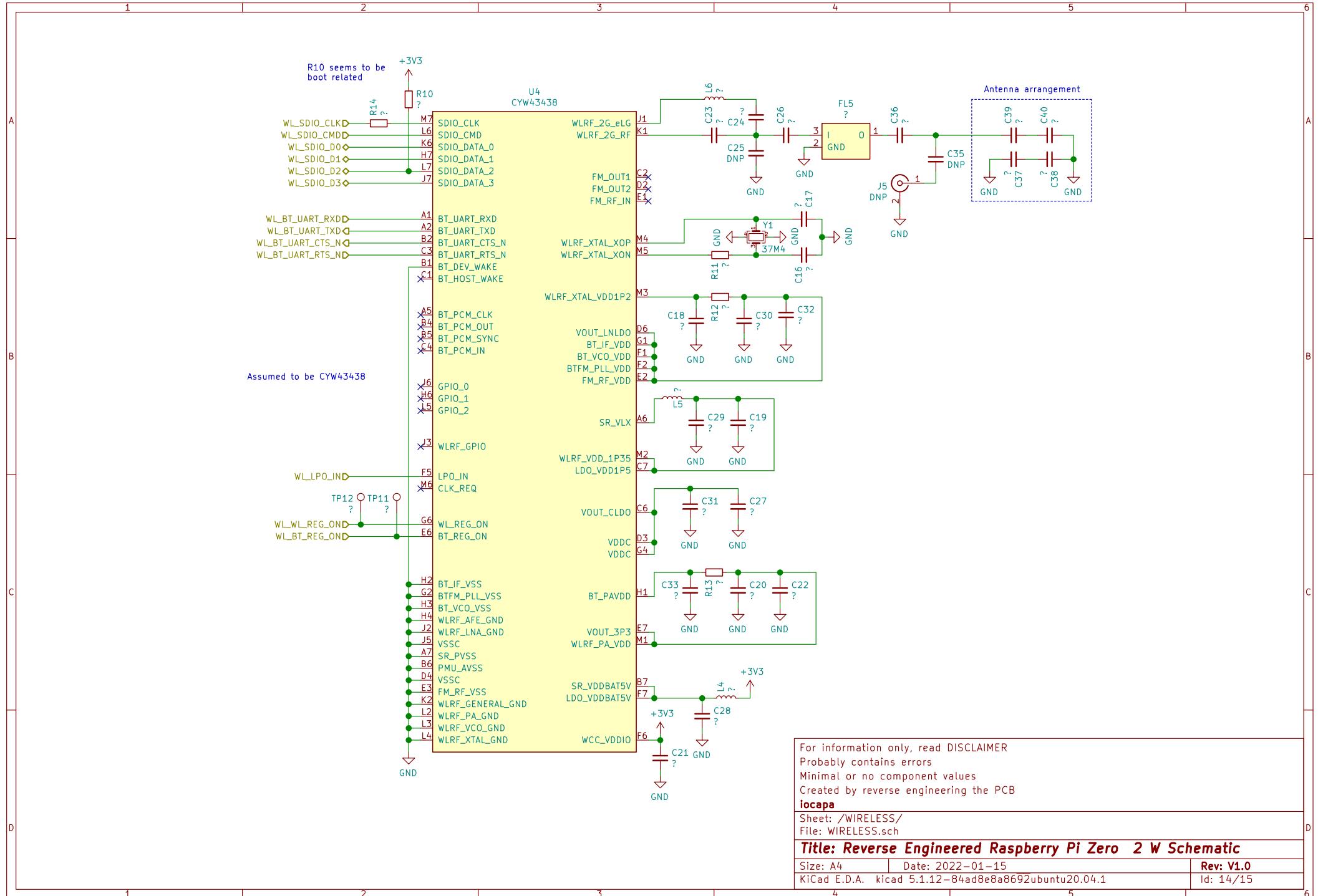
Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 13/15



A

A

B

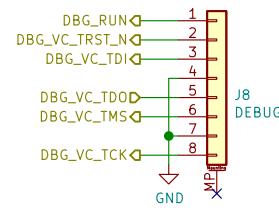
B

C

C

D

D



For information only, read DISCLAIMER
Probably contains errors
Minimal or no component values
Created by reverse engineering the PCB
iocapa

Sheet: /DEBUG/
File: DEBUG.sch

Title: Reverse Engineered Raspberry Pi Zero 2 W Schematic

Size: A4 Date: 2022-01-15

KiCad E.D.A. kicad 5.1.12-84ad8e8a8692ubuntu20.04.1

Rev: V1.0

Id: 15/15