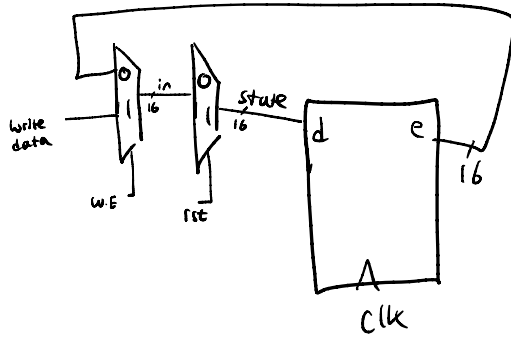


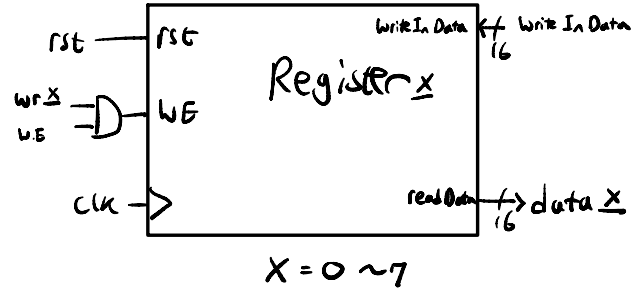
# Schematic 3-1

Yeon Jue Cho

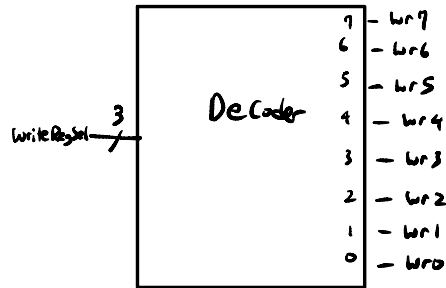
16-bit DFF



Registers by 8 16-bit DFF



3 to 8 Decoder



8 to 1 mux

