

CA HW2 Report

電機三 梁正 B08901169

```
Inferred memory devices in process
in routine ALU line 136 in file
'/home/raid7_2/userb08/b08169/CA_HW2/CA_HW2/HW2.v'.
=====
| Register Name | Type | Width | Bus | MB | AR | AS | SR | SS | ST |
=====
| shreg_reg     | Flip-flop | 64 | Y | N | Y | N | N | N | N |
| alu_in_reg    | Flip-flop | 32 | Y | N | N | N | N | N | N |
| state_reg     | Flip-flop | 3  | Y | N | Y | N | N | N | N |
| counter_reg   | Flip-flop | 5  | Y | N | Y | N | N | N | N |
=====
Presto compilation completed successfully.
Current design is now '/home/raid7_2/userb08/b08169/CA_HW2/CA_HW2/ALU.db:ALU'
Loaded 1 design.
Current design is 'ALU'.
ALU
```