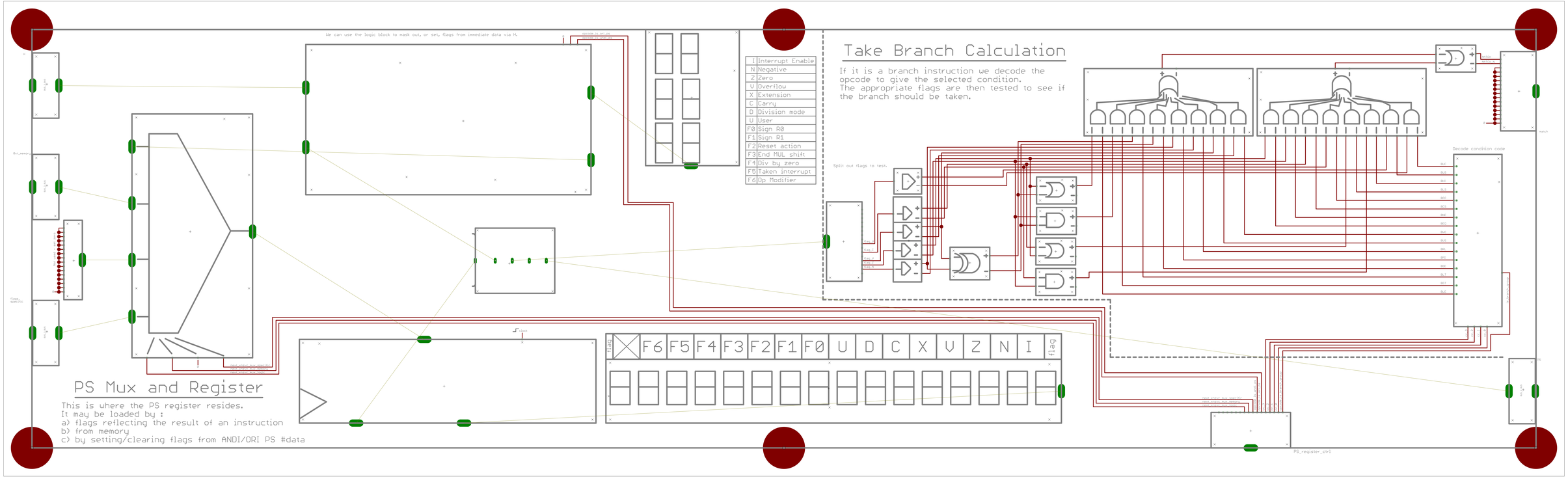
We can use the logic block to mask out, or set, flags from immediate data via M.
I Interrupt Enable
N Negative
Z Zero
V Overflow
X Extension
C Carry
D Division mode
U User
F0 Sign R0
F1 Sign R1
F2 Reset action
F3 End MUL shift
F4 Div by zero
F5 Taken interrupt
F6 Op Modifier
Take Branch Calculation
If it is a branch instruction we decode the opcode to give the selected condition.
The appropriate flags are then tested to see if the branch should be taken.
Split out flags to test.
Decode condition code
PS Mux and Register
This is where the PS register resides.
It may be loaded by :
a) flags reflecting the result of an instruction
b) from memory
c) by setting/clearing flags from ANDI/ORI PS #data
F6 F5 F4 F3 F2 F1 F0 U D C X V Z N I
flag
PS_register_ctrl