

Altera UP Lab 3

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1 Lab 1

1.1 Part 1

The switches 17-0 illuminate the red LEDs 17-0 respectively.

1.2 Part 2

A 2-1 multiplexer, SW[7-0] are one set of inputs while SW[15-8] are the other. SW[17] is the selector bit. The output is displayed on the green LEDs 7-0.

1.3 Part 3

A 3-bit wide 5-to-1 multiplexer is implemented in this lab. SW[14-0] are used for the five 3-bit inputs. SW[17-15] are used as our 3 selector pins.

1.4 Part 4

A three bit decoder module is implemented in this lab controlled by switches 2-0. The output of this result is displayed on HEX0.

1.5 Part 5

Switches 14-0 are used for five 3 bit inputs each of which are passed into a 5-1 multiplexer. The order of the letters displayed is changed by switches 17-15.

1.6 Part 6

2 Lab 2

2.1 Part 1

Switches 15-0 compose four 4-bit groupings that select what is displayed on HEX3-HEX0 respectively.

2.2 Part 2

The switches 3-0 control the four multiplexers that change the BCD displayed on the 7 segment displays.

2.3 Part 3

Switches 7-4 and 3-0 are two 4 bit numbers that are added together with their results being displayed on the green LEDs.

2.4 Part 4

Adds two BCD numbers, switches 7-4 and 3-0 are used as two 4-bit inputs, switch 8 is used for carry-in. The numbers A and B are displayed on HEX6 and HEX4 respectively. The result of the addition is displayed on HEX1 and HEX0.

2.5 Part 5

Addition of two 2-digit BCD numbers. The first 2-digits are represented by switches 15-8, with the second 2 digits are represented by switches 7-0. The results are displayed on HEX2, HEX1, and HEX0.

2.6 Part 6

A similar type of adder as in part 5 is implemented using different methodology.

2.7 Part 7

A 6-bit number is represented by switches 5-0 and displayed on HEX1 and HEX0.

3 Lab 3

3.1 Part 1

3.2 Part 2

3.3 Part 3

3.4 Part 4

3.5 Part 5

When SW[17] is equal to one, hex displays 7-4 are being modified by the switches 15-0 after KEY0 (our clock) is pressed. When SW[17] is zero, the hex displays 3-0

are being modified by the values of the switches 15-0 after a clock pulse simulated by KEY0.

4 Lab 4

5 Lab 5

5.1 Part 1

In the main module the parameters `cntrBits` and `k` define the precision of the counter and the value the counter will reach before resetting respectively. The value of the counter is displayed on the red LEDs. The manual clock is attached to KEY2, and the reset is attached to KEY1.

5.2 Part 2

KEY 1 is used to reset the counter. Hex displays 2-0 display the count in BCD representation. When the counter reaches 999 it rolls over to 000.

5.3 Part 3

Works as a 24 hour clock, begins counting right away. The hour and minutes displays will take on the values of the switches initially.