

# Uber-lazy Symbolic Execution

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**Abstract**—One of the fundamental challenges of using symbolic execution to analyze software has been the treatment of dynamically allocated data. State-of-the-art symbolic execution techniques have addressed this challenge by constructing the heap *lazily*, materializing objects on the concrete heap “as needed” and using non-deterministic choice points to explore each feasible concrete heap configuration. Because analysis of the materialized heap locations relies on concrete program semantics, the lazy initialization approach exacerbates the state space explosion problem that limits the scalability of symbolic execution. In this work we present a novel approach for lazy symbolic execution of heap manipulating software which utilizes a fully symbolic heap constructed on-the-fly during symbolic execution. Our approach is 1) *scalable* – it does not create the additional points of non-determinism introduced by existing lazy initialization techniques and it explores each execution path only once for any given set of isomorphic heaps, 2) *precise* – at any given point during symbolic execution, the symbolic heap represents the exact set of feasible concrete heap structures for the program under analysis, and 3) *expressive* – the symbolic heap can represent recursive data structures and heaps resulting from loops and recursive control structures in the code. We report on a case-study of an implementation of our technique in the Symbolic PathFinder tool to illustrate its scalability, precision and expressiveness. We also discuss how test case generation – a common use for symbolic execution results – can benefit from symbolic execution which uses a fully symbolic heap.

## I. INTRODUCTION

In recent years symbolic execution – a program analysis technique for systematic exploration of program execution paths using symbolic input values – has provided the basis for various software testing and analysis techniques. For each execution path explored during symbolic execution, constraints on the symbolic inputs are collected to create a *path condition*. The set of path conditions computed by symbolic execution characterize the observed program execution behaviours and can be used as an enabling technology for various applications, e.g., regression analysis [1], [2], [3], [4], [5], [6], data structure repair [7], dynamic discovery of invariants [8], [9], and debugging [10].

Initial work on symbolic execution largely focused on checking properties of programs with primitive types, such as integers and booleans [11], [12]. Despite recent advances in constraint solving technologies, improvements in raw computing power, and advances in reduction and abstraction techniques [13], [14] symbolic execution of programs of modest size containing only primitive types, remains challenging because of the large number of execution paths generated during symbolic analysis.

With the advent of object-oriented languages that manipulate dynamically allocated data, .g., Java and C++, recent work has generalized the core ideas of symbolic execution to

enable analysis of programs containing complex data structures with unbounded domains, i.e., data stored on the heap [15], [16], [17]. These techniques construct the heap in a lazy manner, deferring materialization of objects on the concrete heap until they are needed for the analysis to proceed. Treatment of heap allocated data then follows concrete program semantics once a heap location is materialized, resulting in a large number of feasible concrete heap configurations, and as a result, a large number of points of non-determinism to be analyzed, further exacerbating the state space explosion problem.

THIS PARA IS NOT QUITE RIGHT BUT THE IDEA IS STARTING TO COME OUT. Although lazy symbolic execution techniques have been instrumental in enabling analysis of heap manipulating programs, they miss an important opportunity to control the state space explosion problem by treating only inputs with primitive types symbolically and materializing a concrete heap. As we show in this work, the use of a fully *symbolic heap* during lazy symbolic execution, can improve the scalability of the analysis while maintaining precision and efficiency. Moreover, the number of path conditions computed by lazy symbolic execution when a symbolic heap is used produces considerably fewer path conditions – a valuable benefit for client analyses that use the results of symbolic execution, e.g., regression analyses.

The key advantages of our approach to lazy symbolic execution using a fully symbolic heap include:

- *Scalability*. Our approach constructs the symbolic heap on-the-fly during symbolic execution and avoids creating the additional points of non-determinism introduced by existing lazy initialization techniques. Moreover, it explores each execution path only once for any given set of isomorphic heaps.
- *Precision*. At any given point during symbolic execution, the symbolic heap represents the exact set of feasible concrete heap structures for the program under analysis
- *Expressiveness*. The symbolic heap can represent recursive data structures and heap structures resulting from loops and recursive control structures in the analyzed code.

This paper makes the following contributions:

- We present a novel lazy symbolic execution technique for analyzing heap manipulating programs that constructs a fully symbolic representation of the heap on-the-fly during symbolic execution.
- We prove the soundness and completeness of our algorithm...

- We implement our approach in the Symbolic PathFinder tool
- We demonstrate experimentally that our technique improves the scalability of symbolic execution of heap manipulating software over state-of-the-art techniques, while maintaining efficiency and precision.
- We discuss the benefits of using a symbolic heap that can be realized by the client analysis that uses the results of symbolic execution.

## II. BACKGROUND AND MOTIVATION

In this section we present the background on state of the art techniques that have been developed to handle data non-determinism arising from complex data structures. We present an overview of lazy initialization and lazier# initialization. We also present a brief description of the two bounding strategies used in symbolic execution in heap manipulating programs. Next we present a motivating examples where current concrete initialization of the heap structures struggle to scale to medium sized program due to non-determinism introduced in the symbolic execution tree. We use this example to motivate the need for a more truly symbolic and compact representation of the heap in a manner similar to that of primitive types.

Generalized symbolic execution technique generates a concrete representation of connected memory structures using only the implicit information from the program itself. In the original lazy initialization algorithm, symbolic execution explores different heap shapes by concretizing the heap at the first memory access (read) to an un-initialized symbolic object. At this point, a non-deterministic choice point of concrete heap locations is created that includes: (a) null, (b) an access to a new instance of the object, and (c) aliases to other type-compatible symbolic objects that have been concretized along the same execution path [?]. The number of choices explored in lazy initialization greatly increases the non-determinism and often makes the exploration of the program state space intractable.

The Lazier# algorithm is an improvement of the lazy initialization and it pushes the non-deterministic choices further into the execution tree. In the case of a memory access to an uninitialized reference location, by default, no choice point is created. Instead, the read returns a unique symbolic reference representing the contents of the location. The reference may assume any one of three states: uninitialized, non-null, or initialized. The reference is returned in an uninitialized state, and only in a subsequent memory access is the reference concretely initialized.

## III. PSEUDO-CODE

Figure 1 defines the surface syntax for the Javalite language [18]. The Figure 2 is the machine syntax. The semantics of Javalite is syntax based and defined as rewrites on a string. The semantics use a CEKS machine model with a (C)ontrol string representing the expression being evaluated, an (E)nvironment for local variables, a (K)ontinuation for what is to be executed next, and a (S)tore for the heap. This paper only defines salient features of the language and machine relevant to understanding the new algorithm.

```

P ::= (μ (C m) )
μ ::= (CL ...)
T ::= bool | C
CL ::= (class C ([T f] ...) (M ...))
M ::= (T m [T x] e)
e ::= x
    | v
    | (new C)
    | (e $ f)
    | (e @ m e)
    | (e = e)
    | (x := e)
    | (x $ f := e)
    | (if e e else e)
    | (var T x := e in e)
    | (begin e ...)
    | (pop η)
x ::= this | id
f ::= id
m ::= id
C ::= id
v ::= r | null | true | false
r ::= number
id ::= variable-not-otherwise-mentioned

```

Fig. 1. The Javalite surface syntax.

```

e ::= (... | (v @ m v) )
φ ::= constraint
l ::= number
η ::= (mt (η [x → v]))
s ::= (μ L R g η e k)
k ::= end
    | (* $ f → k)
    | (* @ m (e ...) → k)
    | (v @ m v * (e ...) → k)
    | (* = e → k)
    | (v = * → k)
    | (x := * → k)
    | (x $ f := * → k)
    | (if * e else e → k)
    | (var T x := * in e → k)
    | (begin * (e ...) → k)
    | (pop η k)
    | (init f k)

```

Fig. 2. The machine syntax for Javalite.

This paper uses a standard definition of constraints  $\phi \in \Phi$  assuming all the usual relational operators and connectives. The heap is a labeled bipartite graph consisting of references  $R$  and locations  $L$  in the store. The functions  $R$  and  $L$  are defined for convenience in manipulating the labeled bipartite graph.

- $R(l, f)$  maps location-field pairs from the store to a reference in  $R$ ; and
- $L(r)$  maps references to a set of location-constraint pairs in the store.

A reference is a node that gathers the possible store locations for an object during symbolic execution. Each store location

is guarded by a constraint that determines the aliasing in the heap. Intuitively, the reference is a level of indirection between a variable and the store, and the reference is used to group a set of possible store locations each predicated on the possible aliasing in the associated constraint. For a variable (or field) to access any particular store location associated with its reference, the corresponding constraint must be satisfied.

Locations are boxes in the graphical representation and indicated with the letter  $l$  in the math. References are circles in the graphical representation and indicated with the letter  $r$  in the math. Edges from locations are labeled with field names  $f \in F$ . Edges from the references are labeled with constraints  $\phi \in \Phi$  (we assume  $\Phi$  is a power set over individual constraints and  $\phi$  is a set of constraints for the edge).

The function  $\mathbb{VS}(L, R, r, f)$  constructs the value-set given a heap, reference, and desired field such that  $(l' \ \phi' \wedge \phi) \in \mathbb{VS}(L, R, r, f)$  if and only if

$$\begin{aligned} \exists(l \ \phi) \in L(r) ( \\ \exists r' \in R(l, f) ( \\ \exists(l' \ \phi') \in L(r') (\mathbb{S}(\phi \wedge \phi')))) \end{aligned}$$

where  $\mathbb{S}(\phi)$  returns true if  $\phi \wedge g$  is satisfiable.

The strengthen function  $\mathbb{ST}(L, r, \phi')$  strengthens every constraint from the reference  $r$  and keeps only location-constraint pairs that are satisfiable after strengthening. Formally,  $(l \ \phi \wedge \phi') \in \mathbb{ST}(L, r, \phi')$  if and only if  $\exists(l \ \phi) \in L(r) \wedge \mathbb{S}(\phi \wedge \phi')$

The empty-reference function  $\mathbb{ER}(L, \phi') = \{r \mid L(r) \neq \emptyset \wedge \forall(l, \phi) \in L(r) (\neg \mathbb{S}(\phi \wedge \phi'))\}$  searches the heap for references that become disconnected from all their locations after strengthening. These references, if reachable, imply the heap is no longer valid on the current search path. As such, the symbolic execution algorithm should backtrack. This check is similar to a feasibility check in classic symbolic execution with only primitive data types.

The consistency function is critical to the soundness of the algorithm as it detects when a symbolic heap becomes invalid along a path, similar to a feasibility check when doing classical symbolic execution with just primitives. As the constraints in the heap are strengthened with different aliasing requirements, it is possible to reach a point where the heap is no longer connected. Meaning, a valid reference is live, either in the local environment or the continuation, the reaches another reference that is no longer connected to any locations due to strengthening. The function relies on the empty-reference function to identify disconnected references. The function in essence checks every reference in the local environment and every reference found in the continuation, as these are all considered live. This operation similar to garbage collection where the local environment and stack are inspected to find the roots of the heap for the scan.

The consistency function relies on two auxiliary functions which are informally defined. The function  $\text{ref}(\eta, k)$  inspects the local environment and continuation for all live references, and it returns those references in a set. The function  $\text{reach}(L, R, r, r')$  returns true if  $r'$  is reachable from  $r$  in the heap and false otherwise. The consistency function  $\mathbb{C}(L, R, X, \eta, k)$  is now defined as

$$\begin{cases} 0 & \exists r \in \text{ref}(\eta, k) (\exists r' \in X (\text{reach}(L, R, r, r'))) \\ 1 & \text{otherwise} \end{cases}$$

#### IV. UBER-LAZY OPERATIONAL SEMANTICS

The operational semantics for Uber-lazy symbolic execution are specified using the Javalite language [18]. Javalite is an imperative model of Java that includes many features of the Java language. In this work, we present only the salient features of the Javalite language relevant to understanding the Uber-lazy symbolic execution algorithm. A detailed explanation of Javalite is available in [18].

Javalite is an imperative model of the Java language developed to facilitate rapid prototyping of model checking algorithms and proofs about the algorithms. It is specified with a Java-like syntax and a set of reduction rules for syntactically executing Javalite programs. Javalite is based on a variant of the CEKS syntactic machine [19]. The operational semantics are defined using the structure (syntax) of the language and a set of reduction rules for syntactically executing Javalite programs. In a CEKS machine, the (C)ontrol string represents a program, command, or instruction to be evaluated; it is initialized to a string representing the entire program. An (E)nvironment maps (local) variables to their values. The (K)ontinuation specifies what is to be executed next, and the (S)ore is used to store dynamically allocated data, i.e., the heap.

##### A. Symbolic Store

At the core of the Uber-lazy symbolic execution algorithm is a fully symbolic heap, i.e., a heap in which objects are never materialized as concrete objects during symbolic execution, but instead are represented using constraints characterizing feasible heap shapes. To represent the heap store ( $S$ ), our algorithm defines a labeled bi-partite graph where  $S = (R, L, E)$ .  $R$  contains the set of nodes representing program *references*.  $L$  contains the set of nodes representing *locations* in the store. The store is initialized with two special locations: *null* and  $\perp$  representing a null object and an uninitialized location respectively. Each edge in the set of labeled edges,  $E$ , is unidirectional. An edge from a reference  $r \in R$  to a location  $l \in L$  is labeled with a constraint  $\phi$  indicating the conditions under which  $r$  references i.e., points to, that location in the store. This paper uses a standard definition of constraints  $\phi \in \Phi$  assuming all of the usual relation operators and connectives. Reference nodes collect the the feasible points-to relations for a given program execution path during symbolic execution. Each edge from a location  $l \in L$  to a reference  $r \in R$  is labeled with the name of a field  $f \in F$ .

More details here including we assume the input program is typesafe or if there is a type and a mismatch occurs, then the machine halts.

The machine also halts if an exception is thrown.

##### B. Syntax

Javalite programs and expressions are written in a syntax specified by the grammar in Figure 2. The production rules correspond to the various features supported by Javalite such as classes, fields, methods, and expressions.

Figure 2 specifies the machine syntax for Javalite. The expression  $e$  is equivalent to a CEKS machine's control string. The

### C. Reduction Rules

We first present the reduction rules

1) *Basic Reduction Rules*: Most rules presented here - short discussion

2) *Store Update Rules*: Interesting rules presented here; more elaborate discussion

Start off with helper functions for rules that manipulate the store...

The function  $\mathbb{VS}(L, R, r, f)$  constructs the value-set given a heap, reference, and desired field such that  $(l' \phi' \wedge \phi) \in \mathbb{VS}(L, R, r, f)$  if and only if

$$\begin{aligned} \exists (l \phi) \in L(r) ( \\ \exists r' \in R(l, f) ( \\ \exists (l' \phi') \in L(r') (\mathbb{S}(\phi \wedge \phi')))) \end{aligned}$$

where  $\mathbb{S}(\phi)$  returns true if  $\phi$  is satisfiable.

The strengthen function  $\mathbb{ST}(L, r, \phi')$  strengthens every constraint from the reference  $r$  and keeps only location-constraint pairs that are satisfiable after strengthening. Formally,  $(l \phi \wedge \phi') \in \mathbb{ST}(L, r, \phi')$  if and only if  $\exists (l \phi) \in L(r) \wedge \mathbb{S}(\phi \wedge \phi')$

The empty-reference function  $\mathbb{ER}(L, \phi') = \{r \mid L(r) \neq \emptyset \wedge \forall (l, \phi) \in L(r) (\neg \mathbb{S}(\phi \wedge \phi'))\}$  searches the heap for references that become disconnected from all their locations after strengthening. These references, if reachable, imply the heap is no longer valid on the current search path. As such, the symbolic execution algorithm should backtrack. This check is similar to a feasibility check in classic symbolic execution with only primitive data types.

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$$\begin{cases} 0 & \exists r \in \text{ref}(\eta, k) (\exists r' \in X (\text{reach}(L, R, r, r'))) \\ 1 & \text{otherwise} \end{cases}$$

### V. RELATED WORK

The related work goes here.

### ACKNOWLEDGMENT

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VARIABLE LOOKUP

$$(L \ R \ g \ \eta \ x \ k) \rightarrow (L \ R \ g \ \eta \ \eta(x) \ k)$$

FIELD ACCESS(EVAL)

$$(L \ R \ g \ \eta \ (e \ \$ \ f) \ k) \rightarrow (L \ R \ g \ \eta \ e \ (\mathbf{init} \ f \ (* \ \$ \ f \rightarrow k)))$$

LAZY INITIALIZATION

$$\begin{aligned} \Lambda &= \{l \mid \exists \phi \ ((\phi \ l) \in L(r) \wedge R(l, f) = \emptyset)\} \quad \Lambda \neq \emptyset \quad l_x = \min_l(\Lambda) \\ \text{type}(f) &= C_r \quad \text{init}_r() = r_f \quad \text{init}_l(C_r) = l_f \\ \rho &= \{(r' \ \phi' \ l') \mid \text{isInit}(r') \wedge (\phi' \ l') \in L(r') \wedge \text{type}(l') = C_r\} \\ \theta_{alias} &= \{(\phi \ l) \mid (r' \ \phi' \ l) \in \rho \wedge \phi = (\phi' \wedge r_f \neq r_{null} \wedge r_f = r' \wedge (\wedge_{(r'' \ \phi'' \ l'') \in \rho} (r'' \neq r') \ r_f \neq r''))\} \\ \theta_{new} &= \{(\phi \ l_f) \mid \phi = (r_f \neq r_{null} \wedge (\wedge_{(r', \ \phi', \ l') \in \rho} r_f \neq r'))\} \\ \theta_{null} &= \{(\phi \ l_{null}) \mid \phi = (r_f = r_{null})\} \\ \theta &= \theta_{alias} \cup \theta_{new} \cup \theta_{null} \end{aligned}$$

$$(L \ R \ g \ \eta \ r \ (\mathbf{init} \ f \ k)) \rightarrow (L[r_f \mapsto \theta] \ R[(l_x, f) \mapsto r_f] \ g \ \eta \ r \ (\mathbf{init} \ f \ k))$$

NEW

$$\begin{aligned} \text{fresh}_r() &= r \quad \text{fresh}_l(C) = l \\ R' &= R[\forall f \in \text{fields}(C) \ ((l \ f) \mapsto \text{fresh}_r())] \\ \rho &= \{r' \mid \exists f \in \text{fields}(C) \ (r' = R'(l, f))\} \\ L' &= L[r \mapsto \{(\phi_T \ l)\}][\forall r' \in \rho \ (r' \mapsto (\phi_T \ l_{null}))] \end{aligned}$$

LAZY INITIALIZATION (END)

$$\frac{\forall (\phi \ l) \in L(r) \ (R(l, f) \neq \emptyset)}{(L \ R \ g \ \eta \ r \ (\mathbf{init} \ f \ k)) \rightarrow (L \ R \ g \ \eta \ r \ k)}$$

$$(L \ R \ g \ \eta \ (\mathbf{new} \ C) \ k) \rightarrow (L' \ R' \ g \ \eta \ r \ k)$$

FIELD ACCESS

$$\frac{\forall (\phi \ l) \in L(r) \ (l = l_{null} \rightarrow \neg \mathbb{S}(\phi \wedge g)) \quad \text{fresh}_r() = r'}{(L \ R \ g \ \eta \ r \ (* \ \$ \ f \rightarrow k)) \rightarrow (L[r' \mapsto \mathbb{V}\mathbb{S}(L, R, r, f, g)] \ R \ g \ \eta \ r' \ k)}$$

EQUALS (L-OPERAND EVAL)

$$(L \ R \ g \ \eta \ (e_0 = e) \ k) \rightarrow (L \ R \ g \ \eta \ e_0 \ (* = e \rightarrow k))$$

EQUALS (R-OPERAND EVAL)

$$(L \ R \ g \ \eta \ v \ (* = e \rightarrow k)) \rightarrow (L \ R \ g \ \eta \ e \ (v = * \rightarrow k))$$

EQUALS (BOOL)

$$\frac{v_0 \in \{\mathbf{true}, \mathbf{false}\} \quad v_1 \in \{\mathbf{true}, \mathbf{false}\} \quad \text{eq?}(v_0, v_1) = v_r}{(L \ R \ g \ \eta \ v_0 \ (v_1 = * \rightarrow k)) \rightarrow (L \ R \ g \ \eta \ v_r \ k)}$$

EQUALS (REFERENCES-TRUE)

$$\begin{aligned} v_0 &\notin \{\mathbf{true}, \mathbf{false}\} \quad v_1 \notin \{\mathbf{true}, \mathbf{false}\} \\ \theta_\alpha &= \{\phi_0 \wedge \phi_1 \mid \exists l \ ((\phi_0 \ l) \in L(v_0) \wedge (\phi_1 \ l) \in L(v_1))\} \\ \theta_0 &= \{\phi_0 \mid \exists l_0 \ ((\phi_0 \ l_0) \in L(v_0) \wedge \forall (\phi_1 \ l_1) \in L(v_1) \ (l_0 \neq l_1))\} \\ \theta_1 &= \{\phi_1 \mid \exists l_1 \ ((\phi_1 \ l_1) \in L(v_1) \wedge \forall (\phi_0 \ l_0) \in L(v_0) \ (l_0 \neq l_1))\} \\ \phi_g &= (\vee_{\phi_\alpha \in \theta_\alpha} \phi_\alpha) \wedge (\wedge_{\phi_0 \in \theta_0} \neg \phi_0) \wedge (\wedge_{\phi_1 \in \theta_1} \neg \phi_1) \quad g' = g \wedge \phi_g \end{aligned}$$

$$(L \ R \ g \ \eta \ v_0 \ (v_1 = * \rightarrow k)) \rightarrow (L \ R \ g' \ \eta \ \mathbf{true} \ k)$$

EQUALS (REFERENCES-FALSE)

$$\begin{aligned} v_0 &\notin \{\mathbf{true}, \mathbf{false}\} \quad v_1 \notin \{\mathbf{true}, \mathbf{false}\} \\ \theta_\alpha &= \{\phi_0 \rightarrow \neg \phi_1 \mid \exists l \ ((\phi_0 \ l) \in L(v_0) \wedge (\phi_1 \ l) \in L(v_1))\} \\ \theta_0 &= \{\phi_0 \mid \exists l_0 \ ((\phi_0 \ l_0) \in L(v_0) \wedge \forall (\phi_1 \ l_1) \in L(v_1) \ (l_0 \neq l_1))\} \\ \theta_1 &= \{\phi_1 \mid \exists l_1 \ ((\phi_1 \ l_1) \in L(v_1) \wedge \forall (\phi_0 \ l_0) \in L(v_0) \ (l_0 \neq l_1))\} \\ \phi_g &= (\wedge_{\phi_\alpha \in \theta_\alpha} \phi_\alpha) \vee ((\vee_{\phi_0 \in \theta_0} \phi_0) \vee (\vee_{\phi_1 \in \theta_1} \phi_1)) \quad g' = g \wedge \phi_g \end{aligned}$$

$$(L \ R \ g \ \eta \ v_0 \ (v_1 = * \rightarrow k)) \rightarrow (L \ R \ g' \ \eta \ \mathbf{true} \ k)$$

METHOD INVOCATION (OBJECT EVAL)

$$(L \ R \ g \ \eta \ (e_0 \ @ \ m \ e_1) \ k) \rightarrow (L \ R \ g \ \eta \ e_0 \ (* \ @ \ m \ e_1 \rightarrow k))$$

METHOD INVOCATION (ARG EVAL)

$$(L \ R \ g \ \eta \ v_0 \ (* \ @ \ m \ e_1 \rightarrow k)) \rightarrow (L \ R \ g \ \eta \ e_1 \ (v_0 \ @ \ m \ * \rightarrow k))$$

METHOD INVOCATION (RAW)

$$\frac{\text{lookup}(m) = (T \ m \ [T \ x] \ e_m) \quad \eta_m = \eta[\mathbf{this} \mapsto v_0][x \mapsto v_1]}{(L \ R \ g \ \eta \ v_1 \ (v_0 \ @ \ m \ * \rightarrow k)) \rightarrow (L \ R \ g \ \eta \ (\mathbf{raw} \ v_0 \ @ \ m \ v_1 \ k))}$$

METHOD INVOCATION

$$\frac{\text{lookup}(m) = (T \ m \ [T \ x] \ e_m) \quad \eta_m = \eta[\mathbf{this} \mapsto v_0][x \mapsto v_1]}{(L \ R \ g \ \eta \ (\mathbf{raw} \ v_0 \ @ \ m \ v_1 \ k)) \rightarrow (L \ R \ g \ \eta_m \ e_m \ (\mathbf{pop} \ \eta \ k))}$$

Fig. 3. Uber-lazy state reductions

$$\begin{array}{c}
\text{ITE (EXPR-EVAL)} \\
(L R g \eta \text{ (if } * e_0 e_1 \text{ else } e_2) k) \rightarrow (L R g \eta e_0 \text{ (if } * e_1 \text{ else } e_2) \rightarrow k) \\
\\
\text{ITE (TRUE)} \quad \text{ITE (FALSE)} \\
(L R g \eta \text{ true (if } * e_1 \text{ else } e_2) \rightarrow k) \rightarrow (L R g \eta e_1 k) \quad (L R g \eta \text{ false (if } * e_1 \text{ else } e_2) \rightarrow k) \rightarrow (L R g \eta e_2 k) \\
\\
\text{FIELD WRITE (EVAL)} \\
(L R g \eta (x \$ f := e) k) \rightarrow (L R g \eta e \text{ (init } f (x \$ f := * \rightarrow k) )) \\
\\
\text{FIELD WRITE} \\
\begin{array}{l}
r_x = \eta(x) \quad \forall (\phi l) \in L(r_x) \quad (l = l_{null} \rightarrow \neg \mathbb{S}(\phi \wedge g)) \\
\Psi_x = \{ (r_{old} \phi l) \mid (\phi l) \in L(r_x) \wedge r_{old} \in R(l, f) \} \\
X = \{ (r_{old} \theta l) \mid \exists \phi \quad (r_{old} \phi l) \in \Psi_x \wedge \theta = \mathbb{ST}(L, r, \phi) \cup \mathbb{ST}(L, r_{old}, \neg \phi) \} \\
R' = R[\forall (r_{old} \theta l') \in X \quad ((l f) \mapsto \text{fresh}_r())] \\
L' = L[\forall (r_{old} \theta l') \in X \quad (\exists r' = R'(l, f) \quad (r' \mapsto \theta))] \\
\hline
(L R g \eta r (x \$ f := * \rightarrow k)) \rightarrow (L' R' g \eta k)
\end{array} \\
\\
\text{BEGIN (NO ARGS)} \quad \text{BEGIN (ARG0 EVAL)} \\
(L R g \eta \text{ (begin } * k) \rightarrow (L R g \eta k) \quad (L R g \eta \text{ (begin } e_0 e_1 \dots) k) \rightarrow (L R g \eta e_0 \text{ (begin } * (e_1 \dots) \rightarrow k) ) \\
\\
\text{BEGIN (ARG1 EVAL)} \\
(L R g \eta v \text{ (begin } * (e_i e_{i+1} \dots) \rightarrow k) ) \rightarrow (L R g \eta e_i \text{ (begin } * (e_{i+1} \dots) \rightarrow k) ) \\
\\
\text{BEGIN (ARGN EVAL)} \\
(L R g \eta v \text{ (begin } * (e_n) \rightarrow k) ) \rightarrow (L R g \eta e_n \text{ (begin } * () \rightarrow k) ) \\
\\
\text{BEGIN} \\
(L R g \eta v \text{ (begin } * () \rightarrow k) ) \rightarrow (L R g \eta v k) \\
\\
\text{VARIABLE DECLARATION (EVAL)} \\
(L R g \eta \text{ (var } T x := e_0 \text{ in } e_1) k) \rightarrow (L R g \eta e_0 \text{ (var } T x := * \text{ in } e_1 \rightarrow k) ) \\
\\
\text{VARIABLE DECLARATION} \\
(L R g \eta v \text{ (var } T x * := \text{ in } e_1 \rightarrow k) ) \rightarrow (L R g \eta [x \mapsto v] e_1 \text{ (pop } \eta k) ) \\
\\
\text{VARIABLE ASSIGNMENT (EVAL)} \quad \text{VARIABLE ASSIGNMENT} \\
(L R g \eta (x := e) k) \rightarrow (L R g \eta e (x := * \rightarrow k) ) \quad (L R g \eta v (x := * \rightarrow k) ) \rightarrow (L R g \eta [x \mapsto v] v k) \\
\\
\text{POP} \\
(L R g \eta \text{ (pop } \eta_0 k) ) \rightarrow (L R g \eta_0 k)
\end{array}$$

Fig. 4. Uber-lazy state reductions