

Precise Heap Summaries from Symbolic Execution

Anonymous

Abstract

One of the fundamental challenges of using symbolic execution to analyze software has been the treatment of dynamically allocated data. State-of-the-art symbolic execution techniques have addressed this challenge by constructing the heap *lazily*, materializing objects on the concrete heap “as needed” and using non-deterministic choice points to explore each feasible concrete heap configuration. Because analysis of the materialized heap locations relies on concrete program semantics, the lazy initialization approach exacerbates the state space explosion problem that limits the scalability of symbolic execution. In this work we present a novel approach for lazy symbolic execution of heap manipulating software which utilizes a fully symbolic heap constructed on-the-fly during symbolic execution. Our approach is 1) *scalable* – it does not create the additional points of non-determinism introduced by existing lazy initialization techniques and it explores each execution path only once for any given set of isomorphic heaps, 2) *precise* – at any given point during symbolic execution, the symbolic heap represents the exact set of feasible concrete heap structures for the program under analysis, and 3) *expressive* – the symbolic heap can represent recursive data structures and heaps resulting from loops and recursive control structures in the code. We report on a case-study of an implementation of our technique in the Symbolic PathFinder tool to illustrate its scalability, precision and expressiveness. We also discuss how test case generation – a common use for symbolic execution results – can benefit from symbolic execution which uses a fully symbolic heap.

Categories and Subject Descriptors CR-number [subcategory]: third-level

General Terms term1, term2

Keywords keyword1, keyword2

1. Introduction

In recent years symbolic execution – a program analysis technique for systematic exploration of program execution paths using symbolic input values – has provided the basis for various software testing and analysis techniques. For each execution path explored during symbolic execution, constraints on the symbolic inputs are collected to create a *path condition*. The set of path conditions computed by symbolic execution characterize the observed program ex-

ecution behaviours and can be used as an enabling technology for various applications, e.g., regression analysis [2, 8, 13–15, 17], data structure repair [10], dynamic discovery of invariants [4, 18], and debugging [12].

Initial work on symbolic execution largely focused on checking properties of programs with primitive types, such as integers and booleans [3, 11]. Despite recent advances in constraint solving technologies, improvements in raw computing power, and advances in reduction and abstraction techniques [1, 7] symbolic execution of programs of modest size containing only primitive types, remains challenging because of the large number of execution paths generated during symbolic analysis.

With the advent of object-oriented languages that manipulate dynamically allocated data, e.g., Java and C++, recent work has generalized the core ideas of symbolic execution to enable analysis of programs containing complex data structures with unbounded domains, i.e., data stored on the heap [5, 6, 9]. These techniques construct the heap in a lazy manner, deferring materialization of objects on the concrete heap until they are needed for the analysis to proceed. Treatment of heap allocated data then follows concrete program semantics once a heap location is materialized, resulting in a large number of feasible concrete heap configurations, and as a result, a large number of points of non-determinism to be analyzed, further exacerbating the state space explosion problem.

THIS PARA IS NOT QUITE RIGHT BUT THE IDEA IS STARTING TO COME OUT. Although lazy symbolic execution techniques have been instrumental in enabling analysis of heap manipulating programs, they miss an important opportunity to control the state space explosion problem by treating only inputs with primitive types symbolically and materializing a concrete heap. As we show in this work, the use of a fully *symbolic heap* during lazy symbolic execution, can improve the scalability of the analysis while maintaining precision and efficiency. Moreover, the number of path conditions computed by lazy symbolic execution when a symbolic heap is used produces considerably fewer path conditions – a valuable benefit for client analyses that use the results of symbolic execution, e.g., regression analyses.

The key advantages of our approach to lazy symbolic execution using a fully symbolic heap include:

- *Scalability*. Our approach constructs the symbolic heap on-the-fly during symbolic execution and avoids creating the additional points of non-determinism introduced by existing lazy initialization techniques. Moreover, it explores each execution path only once for any given set of isomorphic heaps.
- *Precision*. At any given point during symbolic execution, the symbolic heap represents the exact set of feasible concrete heap structures for the program under analysis
- *Expressiveness*. The symbolic heap can represent recursive data structures and heap structures resulting from loops and recursive control structures in the analyzed code.

This paper makes the following contributions:

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<http://dx.doi.org/10.1145/nnnnnnnn.nnnnnnn>

- We present a novel lazy symbolic execution technique for analyzing heap manipulating programs that constructs a fully symbolic representation of the heap on-the-fly during symbolic execution.
- We prove the soundness and completeness of our algorithm...
- We implement our approach in the Symbolic Pathfinder tool
- We demonstrate experimentally that our technique improves the scalability of symbolic execution of heap manipulating software over state-of-the-art techniques, while maintaining efficiency and precision.
- We discuss the benefits of using a symbolic heap that can be realized by the client analysis that uses the results of symbolic execution.

2. Background and Motivation

In this section we present the background on state of the art techniques that have been developed to handle data non-determinism arising from complex data structures. We present an overview of lazy initialization and lazier# initialization. We also present a brief description of the two bounding strategies used in symbolic execution in heap manipulating programs. Next we present a motivating examples where current concrete initialization of the heap structures struggle to scale to medium sized program due to non-determinism introduced in the symbolic execution tree. We use this example to motivate the need for a more truly symbolic and compact representation of the heap in a manner similar to that of primitive types.

Generalized symbolic execution technique generates a concrete representation of connected memory structures using only the implicit information from the program itself. In the original lazy initialization algorithm, symbolic execution explores different heap shapes by concretizing the heap at the first memory access (read) to an un-initialized symbolic object. At this point, a non-deterministic choice point of concrete heap locations is created that includes: (a) null, (b) an access to a new instance of the object, and (c) aliases to other type-compatible symbolic objects that have been concretized along the same execution path [?]. The number of choices explored in lazy initialization greatly increases the non-determinism and often makes the exploration of the program state space intractable.

The Lazier# algorithm is an improvement of the lazy initialization and it pushes the non-deterministic choices further into the execution tree. In the case of a memory access to an uninitialized reference location, by default, no choice point is created. Instead, the read returns a unique symbolic reference representing the contents of the location. The reference may assume any one of three states: uninitialized, non-null, or initialized. The reference is returned in an uninitialized state, and only in a subsequent memory access is the reference concretely initialized.

3. Pseudo-code

Figure 3 defines the surface syntax for the Javalite language [16]. The Figure 4 is the machine syntax. The semantics of Javalite is syntax based and defined as rewrites on a string. The semantics use a CEKS machine model with a (C)ontrol string representing the expression being evaluated, an (E)nvironment for local variables, a (K)ontinuation for what is to be executed next, and a (S)tore for the heap. This paper only defines salient features of the language and machine relevant to understanding the new algorithm.

This paper uses a standard definition of constraints $\phi \in \Phi$ assuming all the usual relational operators and connectives. The heap is a labeled bipartite graph consisting of references R and locations L in the store. The functions R and L are defined for convenience in manipulating the labeled bipartite graph.

```
public class LinkedList {

    /** assume the linked list is valid with no cycles */
    LLNode head;
    Data data0, data1, data2, data3, data4;

    private class Data { Integer val; }

    private class LLNode {
        protected Data elem;
        protected LLNode next; }

    public static boolean contains(LLNode root, Data val) {
        LLNode node = root;
        while (true) {
            if(node.val == val) return true;
            if(node.next == null) return false;
            node = node.next;
        }
    }

    public void run() {
        if(LinkedList.contains(head, data0) &&
           LinkedList.contains(head, data1) &&
           LinkedList.contains(head, data2) &&
           LinkedList.contains(head, data3) &&
           LinkedList.contains(head, data4)) return;
    }
}
```

Figure 1. Linked list

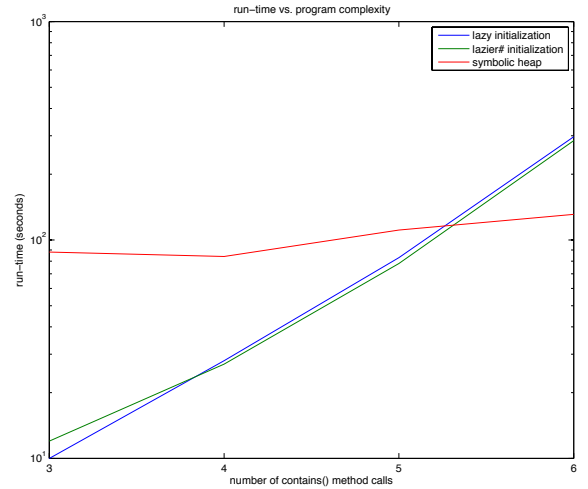


Figure 2. Time versus complexity for the linked list example

- $R(l, f)$ maps location-field pairs from the store to a reference in R ; and
- $L(r)$ maps references to a set of location-constraint pairs in the store.

A reference is a node that gathers the possible store locations for an object during symbolic execution. Each store location is guarded by a constraint that determines the aliasing in the heap. Intuitively, the reference is a level of indirection between a variable and the store, and the reference is used to group a set of possible store locations each predicated on the possible aliasing in the associated constraint. For a variable (or field) to access any particular store location associated with its reference, the corresponding constraint must be satisfied.

```

P ::= (μ (C m))
μ ::= (CL ...)
T ::= bool | C
CL ::= (class C ([T f] ...) (M ...))
M ::= (T m [T x] e)
e ::= x
    | v
    | (new C)
    | (e $f)
    | (e @ m e)
    | (e = e)
    | (x := e)
    | (x $f := e)
    | (if e e else e)
    | (var T x := e in e)
    | (begin e ...)
    | (pop η)
x ::= this | id
f ::= id
m ::= id
C ::= id
v ::= r | null | true | false
r ::= number
id ::= variable-not-otherwise-mentioned

```

Figure 3. The Javalite surface syntax.

```

e ::= (... | (v @ m v))
φ ::= constraint
l ::= number
η ::= (mt (η [x → v]))
s ::= (μ L R φg η e k)
k ::= end
    | (* $f → k)
    | (* @ m e → k)
    | (v @ m * → k)
    | (* = e → k)
    | (v = * → k)
    | (x := * → k)
    | (x $f := * → k)
    | (if * e else e → k)
    | (var T x := * in e → k)
    | (begin * (e ...) → k)
    | (pop η k)
    | (init f k)

```

Figure 4. The machine syntax for Javalite.

Locations are boxes in the graphical representation and indicated with the letter l in the math. References are circles in the graphical representation and indicated with the letter r in the math. The special symbol \perp is an uninitialized reference. Edges from locations are labeled with field names f . Edges from the references are labeled with constraints $\phi \in \Phi$ (it is assumed that Φ is a power set over individual constraints and ϕ is a set of constraints for the edge).

The function $\mathbb{V}\mathbb{S}(L, R, \phi_g, r, f)$ constructs the value-set given a heap, reference, and desired field:

$$\mathbb{V}\mathbb{S}(L, R, \phi_g, r, f) = \{(l' \phi \wedge \phi') \mid \exists l ((l \phi) \in L(r) \wedge \exists r' \in R(l, f) ((l' \phi') \in L(r') \wedge \mathbb{S}(\phi \wedge \phi' \wedge \phi_g)))\}$$

where $\mathbb{S}(\phi)$ returns true if ϕ is satisfiable.

The strengthen function $\mathbb{S}\mathbb{T}(L, r, \phi')$ strengthens every constraint from the reference r with ϕ' and keeps only location-constraint pairs that are satisfiable after this strengthening:

$$\mathbb{S}\mathbb{T}(L, r, \phi') = \{(l \phi \wedge \phi') \mid (l \phi) \in L(r) \wedge \mathbb{S}(\phi \wedge \phi')\}$$

4. Proofs

4.1 Definitions

Definition A symbolic system state represents a set of lazy system states. To denote the representation relationship, we use the expression $\mathcal{L} \sqsubseteq \mathcal{S}$ to say that lazy system state $\mathcal{L} : (\mu L_{\mathcal{L}} R_{\mathcal{L}} \phi_{\mathcal{L}} \eta e k)$ is represented by symbolic state $\mathcal{S} : (\mu L_{\mathcal{S}} R_{\mathcal{S}} \phi_{\mathcal{S}} \eta e k)$. The representation relationship is defined as follows: $\mathcal{L} \sqsubseteq \mathcal{S}$ if and only if there exists functions $g : r \mapsto r$ and $h : l \mapsto l$ such that for any reference $r \in \mathcal{L}$, location $l \in \mathcal{L}$, and field f ,

$$l = L_{\mathcal{L}}(r) \leftrightarrow h(l) \in L_{\mathcal{S}}(g(r))$$

and

$$r = R_{\mathcal{L}}(l, f) \leftrightarrow g(r) = R_{\mathcal{S}}(h(l), f)$$

4.2 Theorems

The following theorem demonstrates the use of the theorem environment.

THEOREM 1. *Let f be continuous on $[a, b]$. If G is an antiderivative for f on $[a, b]$, then*

$$\int_a^b f(t) dt = G(b) - G(a).$$

5. Related Work

The related work goes here.

Acknowledgments

Acknowledgments, if needed.

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$\begin{array}{c} \text{NULL} \\ (L R \phi_g \eta \text{ null } k) \rightarrow \\ (L R \phi_g \eta r_{\text{null}} k) \end{array}$		$\begin{array}{c} \text{VARIABLE LOOKUP} \\ (L R \phi_g \eta x k) \rightarrow \\ (L R \phi_g \eta \eta(x) k) \end{array}$		$\begin{array}{c} \text{NEW} \\ \text{fresh}_r() = r \quad \text{fresh}_l(C) = l \\ R[\forall f \in \text{fields}(C) \ (l f \mapsto \text{fresh}_r())] = R' \\ \{r' \mid \exists f \in \text{fields}(C) \ (r' = R'(l, f))\} = \rho \\ L[r \mapsto \{(\phi_T l)\}][\forall r' \in \rho \ (r' \mapsto (\phi_T l_{\text{null}}))] = L' \\ \hline (L R \phi_g \eta (\text{new } C) k) \rightarrow \\ (L' R' \phi_g \eta r k) \end{array}$
$\begin{array}{c} \text{FIELD ACCESS (EVAL)} \\ (L R \phi_g \eta (e \$ f) k) \rightarrow \\ (L R \phi_g \eta e \ (\text{init } f \ (* \$ f \rightarrow k))) \end{array}$	$\begin{array}{c} \text{EQUALS (L-OPERAND EVAL)} \\ (L R \phi_g \eta (e_0 = e) k) \rightarrow \\ (L R \phi_g \eta e_0 \ (* = e \rightarrow k)) \end{array}$	$\begin{array}{c} \text{EQUALS (R-OPERAND EVAL)} \\ (L R \phi_g \eta v \ (* = e \rightarrow k)) \rightarrow \\ (L R \phi_g \eta e \ (v = * \rightarrow k)) \end{array}$		
$\begin{array}{c} \text{EQUALS (BOOL)} \\ v_0 \in \{\text{true}, \text{false}\} \quad v_1 \in \{\text{true}, \text{false}\} \\ \text{eq?}(v_0, v_1) = v_r \\ \hline (L R \phi_g \eta v_0 \ (v_1 = * \rightarrow k)) \rightarrow \\ (L R \phi_g \eta v_r k) \end{array}$	$\begin{array}{c} \text{METHOD INVOCATION (OBJECT EVAL)} \\ (L R \phi_g \eta (e_0 @ m e_1) k) \rightarrow \\ (L R \phi_g \eta e_0 \ (* @ m e_1 \rightarrow k)) \end{array}$	$\begin{array}{c} \text{METHOD INVOCATION (ARG EVAL)} \\ (L R \phi_g \eta v_0 \ (* @ m e_1 \rightarrow k)) \rightarrow \\ (L R \phi_g \eta e_1 \ (v_0 @ m * \rightarrow k)) \end{array}$		
$\begin{array}{c} \text{METHOD INVOCATION} \\ \text{lookup}(m) = (T m [T x] \ e_m) \\ \eta[\text{this} \mapsto v_0][x \mapsto v_1] = \eta_m \\ \hline (L R \phi_g \eta v_1 \ (v_0 @ m * \rightarrow k)) \rightarrow \\ (L R \phi_g \eta_m e_m \ (\text{pop } \eta k)) \end{array}$	$\begin{array}{c} \text{IF-THEN-ELSE (EVAL)} \\ (L R \phi_g \eta (\text{if } e_0 e_1 \text{ else } e_2) k) \rightarrow \\ (L R \phi_g \eta e_0 \ (\text{if } * e_1 \text{ else } e_2) \rightarrow k) \end{array}$	$\begin{array}{c} \text{IF-THEN-ELSE (TRUE)} \\ (L R \phi_g \eta \text{true} \ (\text{if } * e_1 \text{ else } e_2) \rightarrow k) \rightarrow \\ (L R \phi_g \eta e_1 k) \end{array}$		
$\begin{array}{c} \text{IF-THEN-ELSE (FALSE)} \\ (L R \phi_g \eta \text{false} \ (\text{if } * e_1 \text{ else } e_2) \rightarrow k) \rightarrow \\ (L R \phi_g \eta e_2 k) \end{array}$	$\begin{array}{c} \text{FIELD WRITE (EVAL)} \\ (L R \phi_g \eta (x \$ f := e) k) \rightarrow \\ (L R \phi_g \eta e \ (\text{init } f \ (x \$ f := * \rightarrow k))) \end{array}$	$\begin{array}{c} \text{BEGIN (NO ARGS)} \\ (L R \phi_g \eta (\text{begin}) k) \rightarrow \\ (L R \phi_g \eta k) \end{array}$		
$\begin{array}{c} \text{BEGIN (ARG0 EVAL)} \\ (L R \phi_g \eta (\text{begin } e_0 e_1 \dots) k) \rightarrow \\ (L R \phi_g \eta e_0 \ (\text{begin } * (e_1 \dots) \rightarrow k)) \end{array}$	$\begin{array}{c} \text{BEGIN (ARGI EVAL)} \\ (L R \phi_g \eta v \ (\text{begin } * (e_i e_{i+1} \dots) \rightarrow k)) \rightarrow \\ (L R \phi_g \eta e_i \ (\text{begin } * (e_{i+1} \dots) \rightarrow k)) \end{array}$	$\begin{array}{c} \text{BEGIN (ARGN EVAL)} \\ (L R \phi_g \eta v \ (\text{begin } * (e_n) \rightarrow k)) \rightarrow \\ (L R \phi_g \eta e_n \ (\text{begin } * () \rightarrow k)) \end{array}$		
$\begin{array}{c} \text{BEGIN} \\ (L R \phi_g \eta v \ (\text{begin } * () \rightarrow k)) \rightarrow \\ (L R \phi_g \eta v k) \end{array}$	$\begin{array}{c} \text{VARIABLE DECLARATION (EVAL)} \\ (L R \phi_g \eta (\text{var } T x := e_0 \text{ in } e_1) k) \rightarrow \\ (L R \phi_g \eta e_0 \ (\text{var } T x := * \text{ in } e_1 \rightarrow k)) \end{array}$	$\begin{array}{c} \text{VARIABLE DECLARATION} \\ (L R \phi_g \eta v \ (\text{var } T x := \text{in } e_1 \rightarrow k)) \rightarrow \\ (L R \phi_g \eta [x \mapsto v] e_1 \ (\text{pop } \eta k)) \end{array}$		
$\begin{array}{c} \text{VARIABLE ASSIGNMENT (EVAL)} \\ (L R \phi_g \eta (x := e) k) \rightarrow \\ (L R \phi_g \eta e \ (x := * \rightarrow k)) \end{array}$	$\begin{array}{c} \text{VARIABLE ASSIGNMENT} \\ (L R \phi_g \eta v \ (x := * \rightarrow k)) \rightarrow \\ (L R \phi_g \eta [x \mapsto v] v k) \end{array}$	$\begin{array}{c} \text{POP} \\ (L R \phi_g \eta (\text{pop } \eta_0 k)) \rightarrow \\ (L R \phi_g \eta_0 k) \end{array}$		

Figure 5. Rules common for classic symbolic execution with lazy initialization and symbolic execution with the fully symbolic heap.

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$$\begin{array}{c}
\text{INITIALIZE (NULL)} \\
\Lambda = \{l \mid \exists \phi ((\phi l) \in L(r) \wedge R(l, f) = \perp)\} \quad \Lambda \neq \emptyset \\
r' = \text{fresh}_r() \quad \theta_{\text{null}} = \{(\phi_T l_{\text{null}})\} \quad l_x = \min_l(\Lambda) \\
\phi'_g = (\phi_g \wedge r' = r_{\text{null}}) \\
\hline
(LR \phi_g rf) \rightarrow_I (L[r' \mapsto \theta_{\text{null}}] R[(l_x, f) \mapsto r'] \phi'_g rf)
\end{array}
\qquad
\begin{array}{c}
\text{INITIALIZE (NEW)} \\
\Lambda = \{l \mid \exists \phi ((\phi l) \in L(r) \wedge R(l, f) = \perp)\} \quad \Lambda \neq \emptyset \\
C = \text{type}(f) \quad r_f = \text{init}_r() \quad l_f = \text{init}_l(C) \\
\rho = \{(r_a l_a) \mid \text{isInit}(r_a) \wedge \text{type}(l_a) = C \wedge \exists \phi_a ((\phi_a l_a) \in L(r_a))\} \\
\theta_{\text{new}} = \{(\phi_T l_f)\} \quad l_x = \min_l(\Lambda) \\
\phi'_g = (\phi_g \wedge r_f \neq r_{\text{null}} \wedge (\wedge_{(r_a l_a) \in \rho} r_f \neq r_a)) \\
\hline
(LR \phi_g rf) \rightarrow_I (L[r_f \mapsto \theta_{\text{new}}] R[(l_x, f) \mapsto r_f] \phi'_g rf)
\end{array}$$

$$\begin{array}{c}
\text{INITIALIZE (ALIAS)} \\
\Lambda = \{l \mid \exists \phi ((\phi l) \in L(r) \wedge R(l, f) = \perp)\} \quad \Lambda \neq \emptyset \\
C = \text{type}(f) \quad r' = \text{fresh}_r() \\
\rho = \{(r_a l_a) \mid \text{isInit}(r_a) \wedge \text{type}(l_a) = C \wedge \exists \phi_a ((\phi_a l_a) \in L(r_a))\} \\
(r_a l_a) \in \rho \quad \theta_{\text{alias}} = \{(\phi_T l_a)\} \quad l_x = \min_l(\Lambda) \\
\phi'_g = (\phi_g \wedge r' \neq r_{\text{null}} \wedge r' = r_a \wedge (\wedge_{(r'_a l_a) \in \rho} (r'_a \neq r_a) \rightarrow r' \neq r'_a)) \\
\hline
(LR \phi_g rf) \rightarrow_I (L[r' \mapsto \theta_{\text{alias}}] R[(l_x, f) \mapsto r'] \phi'_g rf)
\end{array}$$

Figure 6. The initialization machine, $s ::= (LR \phi_g rf)$, for generalized symbolic execution with lazy initialization denoted by \rightarrow_I .

$$\begin{array}{c}
\text{FIELD ACCESS} \\
\{(\phi l)\} = L(r) \quad l \neq l_{\text{null}} \\
(LR \phi_g rf) \rightarrow_I^* (L' R' \phi'_g rf) \\
\{(\phi' l')\} = L'(R'(l, f)) \quad \text{fresh}_r() = r' \\
\hline
(LR \phi_g \eta r (*\$f \rightarrow k)) \rightarrow \\
(L'[r' \mapsto (\phi' l')] R' \phi'_g \eta r' k)
\end{array}
\qquad
\begin{array}{c}
\text{FIELD WRITE} \\
r_x = \eta(x) \quad \{(\phi l)\} = L(r_x) \quad l \neq l_{\text{null}} \\
(LR \phi_g r_x f) \rightarrow_I^* (L' R' \phi'_g r_x f) \\
\text{fresh}_r() = r' \quad \theta = L'(r) \\
\hline
(LR \phi_g \eta r (x\$f := * \rightarrow k)) \rightarrow \\
(L'[r' \mapsto \theta] R'[(l, f) \mapsto r'] \phi_g \eta r' k)
\end{array}$$

$$\begin{array}{c}
\text{EQUALS (REFERENCE-TRUE)} \\
L(r_0) = L(r_1) \quad \phi'_g = (\phi_g \wedge r_0 = r_1) \\
\hline
(LR \phi_g \eta r_0 (r_1 = * \rightarrow k)) \rightarrow \\
(LR \phi'_g \eta \text{true } k)
\end{array}
\qquad
\begin{array}{c}
\text{EQUALS (REFERENCE-FALSE)} \\
L(r_0) \neq L(r_1) \quad \phi'_g = (\phi_g \wedge r_0 \neq r_1) \\
\hline
(LR \phi_g \eta r_0 (r_1 = * \rightarrow k)) \rightarrow \\
(LR \phi'_g \eta \text{false } k)
\end{array}$$

Figure 7. Generalized symbolic execution with lazy initialization.

$$\begin{array}{c}
\text{SUMMARIZE} \\
\Lambda = \{l \mid \exists \phi ((\phi l) \in L(r) \wedge l \neq l_{\text{null}} \wedge R(l, f) = \perp)\} \quad \Lambda \neq \emptyset \\
C = \text{type}(f) \quad r_f = \text{init}_r() \quad l_f = \text{init}_l(C) \\
\rho = \{(r_a \phi_a l_a) \mid \text{isInit}(r_a) \wedge (\phi_a l_a) \in L(r_a) \wedge \text{type}(l_a) = C\} \\
\theta_{\text{alias}} = \{(\phi l_a) \mid \exists r_a (\exists \phi_a ((r_a \phi_a l_a) \in \rho \wedge \phi = (\phi_a \wedge r_f \neq r_{\text{null}} \wedge r_f = r_a \wedge (\wedge_{(r'_a \phi'_a l'_a) \in \rho} (r'_a \neq r_a) \rightarrow r_f \neq r'_a))))\} \\
\theta_{\text{new}} = \{(\phi l_f) \mid \phi = (r_f \neq r_{\text{null}} \wedge (\wedge_{(r'_a \phi'_a l'_a) \in \rho} r_f \neq r'_a))\} \\
\theta_{\text{null}} = \{(\phi l_{\text{null}}) \mid \phi = (r_f = r_{\text{null}})\} \\
\theta = \theta_{\text{alias}} \cup \theta_{\text{new}} \cup \theta_{\text{null}} \quad l_x = \min_l(\Lambda) \\
\hline
(LR rf) \rightarrow_S (L[r_f \mapsto \theta] R[(l_x, f) \mapsto r_f] rf)
\end{array}$$

Figure 8. The summary machine, $s ::= (LR rf)$, for precise heap summaries from symbolic execution denoted by \rightarrow_S .

FIELD ACCESS

$$\frac{\begin{array}{c} \forall(\phi \ l) \in L(r) \ (l = l_{null} \rightarrow \neg \mathbb{S}(\phi \wedge \phi_g)) \\ (L \ R \ r \ f) \rightarrow_S^* (L' \ R' \ r \ f) \quad r' = \text{fresh}_r() \end{array}}{(L \ R \ \phi_g \ \eta \ r \ (* \ \$f \rightarrow k)) \rightarrow (L'[r' \mapsto \mathbb{V}\mathbb{S}(L', R', r, f, \phi_g)] \ R' \ \phi_g \ \eta \ r' \ k)}$$

FIELD WRITE

$$\frac{\begin{array}{c} r_x = \eta(x) \quad \forall(\phi \ l) \in L(r_x) \ (l = l_{null} \rightarrow \neg \mathbb{S}(\phi \wedge \phi_g)) \\ (L \ R \ r_x \ f) \rightarrow_S^* (L' \ R' \ r_x \ f) \\ \Psi_x = \{(r_{old} \ \phi \ l) \mid (\phi \ l) \in L'(r_x) \wedge r_{old} \in R(l, f)\} \\ X = \{(r_{old} \ \theta \ l) \mid \exists \phi \ ((r_{old} \ \phi \ l) \in \Psi_x \wedge \theta = \mathbb{ST}(L', r, \phi) \cup \mathbb{ST}(L', r_{old}, \neg \phi))\} \\ R'' = R'[\forall(r_{old} \ \theta \ l) \in X \ ((l, f) \mapsto \text{fresh}_r())] \\ L'' = L'[\forall(r_{old} \ \theta \ l) \in X \ (\exists r' \ (r' = R'(l, f) \wedge (r' \mapsto \theta)))] \end{array}}{(L \ R \ \phi_g \ \eta \ r \ (x \ \$f := * \rightarrow k)) \rightarrow (L'' R'' \ \phi_g \ \eta \ k)}$$

EQUALS (REFERENCES-TRUE)

$$\frac{\begin{array}{c} \theta_\alpha = \{\phi_0 \wedge \phi_1 \mid \exists l \ ((\phi_0 \ l) \in L(r_0) \wedge (\phi_1 \ l) \in L(r_1))\} \\ \theta_0 = \{\phi_0 \mid \exists l_0 \ ((\phi_0 \ l_0) \in L(r_0) \wedge \forall(\phi_1 \ l_1) \in L(r_1) \ (l_0 \neq l_1))\} \\ \theta_1 = \{\phi_1 \mid \exists l_1 \ ((\phi_1 \ l_1) \in L(r_1) \wedge \forall(\phi_0 \ l_0) \in L(r_0) \ (l_0 \neq l_1))\} \\ \phi'_g = \phi_g \wedge (\vee_{\phi_\alpha \in \theta_\alpha} \phi_\alpha) \wedge (\wedge_{\phi_0 \in \theta_0} \neg \phi_0) \wedge (\wedge_{\phi_1 \in \theta_1} \neg \phi_1) \\ \mathbb{S}(\phi'_g) \end{array}}{(L \ R \ \phi_g \ \eta \ r_0 \ (r_1 = * \rightarrow k)) \rightarrow (L \ R \ \phi'_g \ \eta \ \text{true} \ k)}$$

EQUALS (REFERENCES-FALSE)

$$\frac{\begin{array}{c} \theta_\alpha = \{\phi_0 \rightarrow \neg \phi_1 \mid \exists l \ ((\phi_0 \ l) \in L(r_0) \wedge (\phi_1 \ l) \in L(r_1))\} \\ \theta_0 = \{\phi_0 \mid \exists l_0 \ ((\phi_0 \ l_0) \in L(r_0) \wedge \forall(\phi_1 \ l_1) \in L(r_1) \ (l_0 \neq l_1))\} \\ \theta_1 = \{\phi_1 \mid \exists l_1 \ ((\phi_1 \ l_1) \in L(r_1) \wedge \forall(\phi_0 \ l_0) \in L(r_0) \ (l_0 \neq l_1))\} \\ \phi'_g = \phi_g \wedge (\wedge_{\phi_\alpha \in \theta_\alpha} \phi_\alpha) \vee ((\vee_{\phi_0 \in \theta_0} \phi_0) \vee (\vee_{\phi_1 \in \theta_1} \phi_1)) \\ \mathbb{S}(\phi'_g) \end{array}}{(L \ R \ \phi_g \ \eta \ r_0 \ (r_1 = * \rightarrow k)) \rightarrow (L \ R \ \phi'_g \ \eta \ \text{false} \ k)}$$

Figure 9. Precise symbolic heap summaries from symbolic execution.