1

ECE385 Experiment #5

Eric Meyers, Ryan Helsdingen
Section ABG; TAs: Ben Delay, Shuo Liu
February 17th, 2016
emeyer7, helsdin2

I. INTRODUCTION

II. 8-BIT MULTIPLICATION EXAMPLE

III. DESCRIPTION OF CIRCUIT

IV. SCHEMATIC/BLOCK DIAGRAM

V. PRE-LAB SIMULATION WAVEFORMS

VI. POST LAB

VII. CONCLUSION

VIII. FIGURES