

# ECE385 Experiment #8

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## I. INTRODUCTION

## II. DESCRIPTION OF CIRCUIT

## III. PURPOSE OF MODULES

## IV. DESCRIPTION OF CIRCUIT

## V. SCHEMATIC/BLOCK DIAGRAM

## VI. POST LAB

Resource	Value
LUT	
DSP	
Memory (BRAM)	
Flip-Flop	
Frequency	MHz
Static Power	mW
Dynamic Power	mW
Total Power	mW

TABLE I: Design Statistics

1. What is the difference between VGA\_clk and Clk?

2. In the file io\_handler.h, why is it that the otg\_hpi\_address is defined as an integer pointer while the otg\_hpi\_r is defined as a char pointer?

## VII. CONCLUSION

## VIII. FIGURES