

ECE385 Experiment #9

Eric Meyers, Ryan Helsdingen

Section ABG; TAs: Ben Delay, Shuo Liu

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emeyer7, helsdin2

- I. INTRODUCTION
- II. DESCRIPTION OF CIRCUIT
- III. PURPOSE OF MODULES
- IV. SOFTWARE/HARDWARE STATE DIAGRAM
- V. DECRYPTION STATE DIAGRAM
- VI. SCHEMATIC/BLOCK DIAGRAM
- VII. ANNOTATED PRE-LAB WAVEFORMS
- VIII. POST LAB

Resource	Value
LUT	
DSP	
Memory (BRAM)	
Flip-Flop	
Frequency	MHz
Static Power	mW
Dynamic Power	mW
Total Power	mW

TABLE I: Design Statistics

- IX. CONCLUSION

X. FIGURES

APPENDIX