

ECE385 Experiment #7

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I. INTRODUCTION

II. DESCRIPTION OF CIRCUIT

III. PURPOSE OF MODULES

IV. SCHEMATIC/BLOCK DIAGRAM

V. ANSWERS TO INQ QUESTIONS

VI. POST LAB

Resource	Value
LUT	___
DSP	___
Memory (BRAM)	___
Flip-Flop	___
Frequency	___ MHz
Static Power	___ mW
Dynamic Power	___ mW
Total Power	___ mW

TABLE I: Design Statistics

VII. CONCLUSION

VIII. FIGURES