

Chapter 2: Architecture

Eric Pereira
CSE3120: Section 02

September 5th, 2018

2.8 Review Questions

1: In 32-bit mode, aside from the stack pointer (ESP), what other register points to variables on the stack?

Answer:

The base pointer register.

3: Which flag is set when the result of an unsigned arithmetic operation is too large to fit into the destination?

Answer:

The carry flag

4: Which flag is set when the result of a signed arithmetic operation is either too large or too small to fit into the destination?

Answer:

The overflow flag

6: Which flag is set when an arithmetic or logical operation generates a negative result?

Answer:

The sign flag

8: On a 32-bit processor, how many bits are contained in each floating-point data register?

Answer:

80 bits