

A

A

B

B

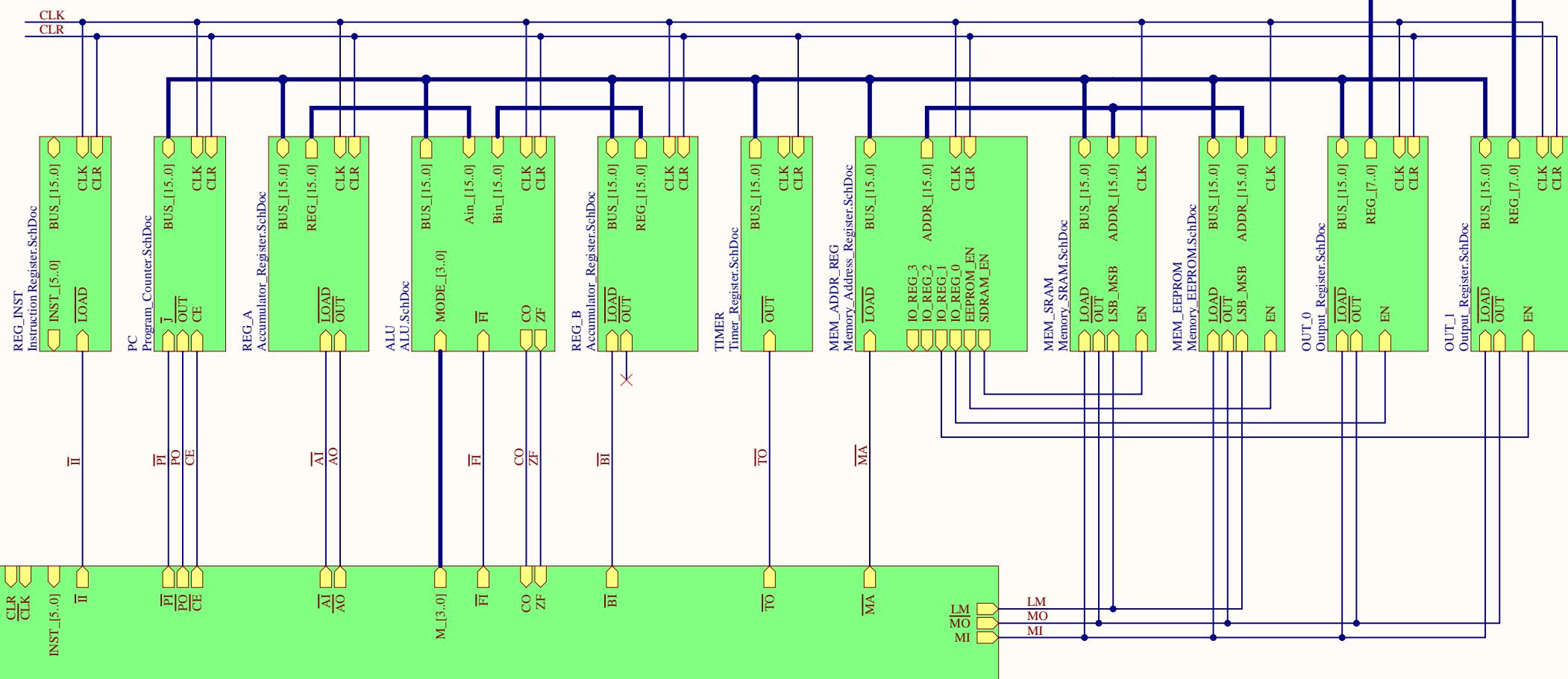
C

C

D

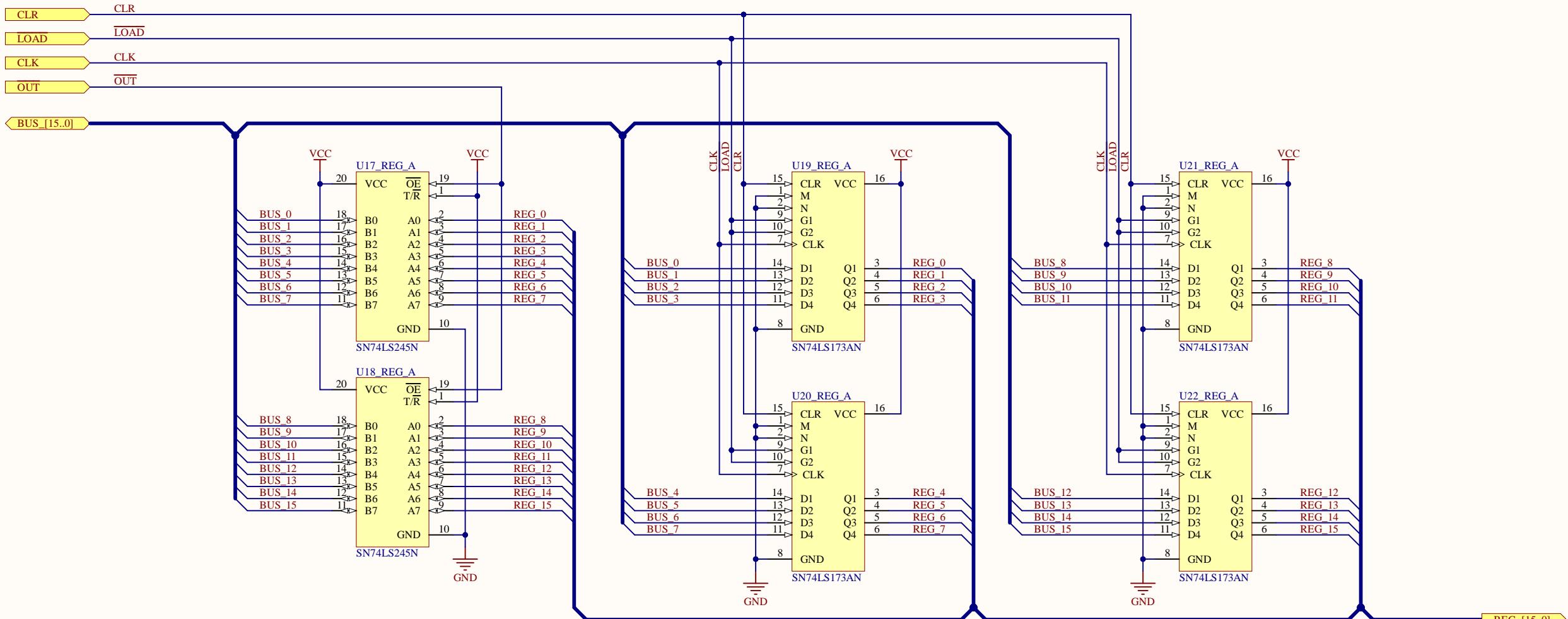
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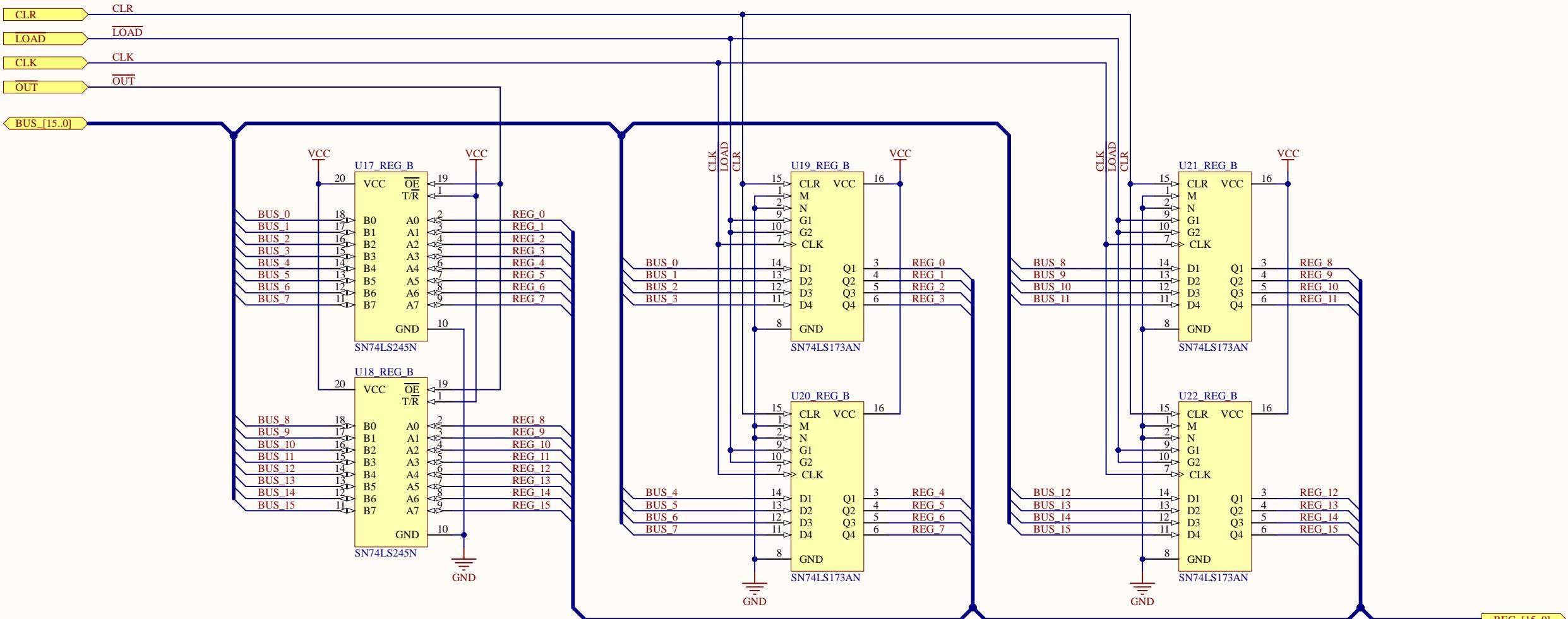
CTRL

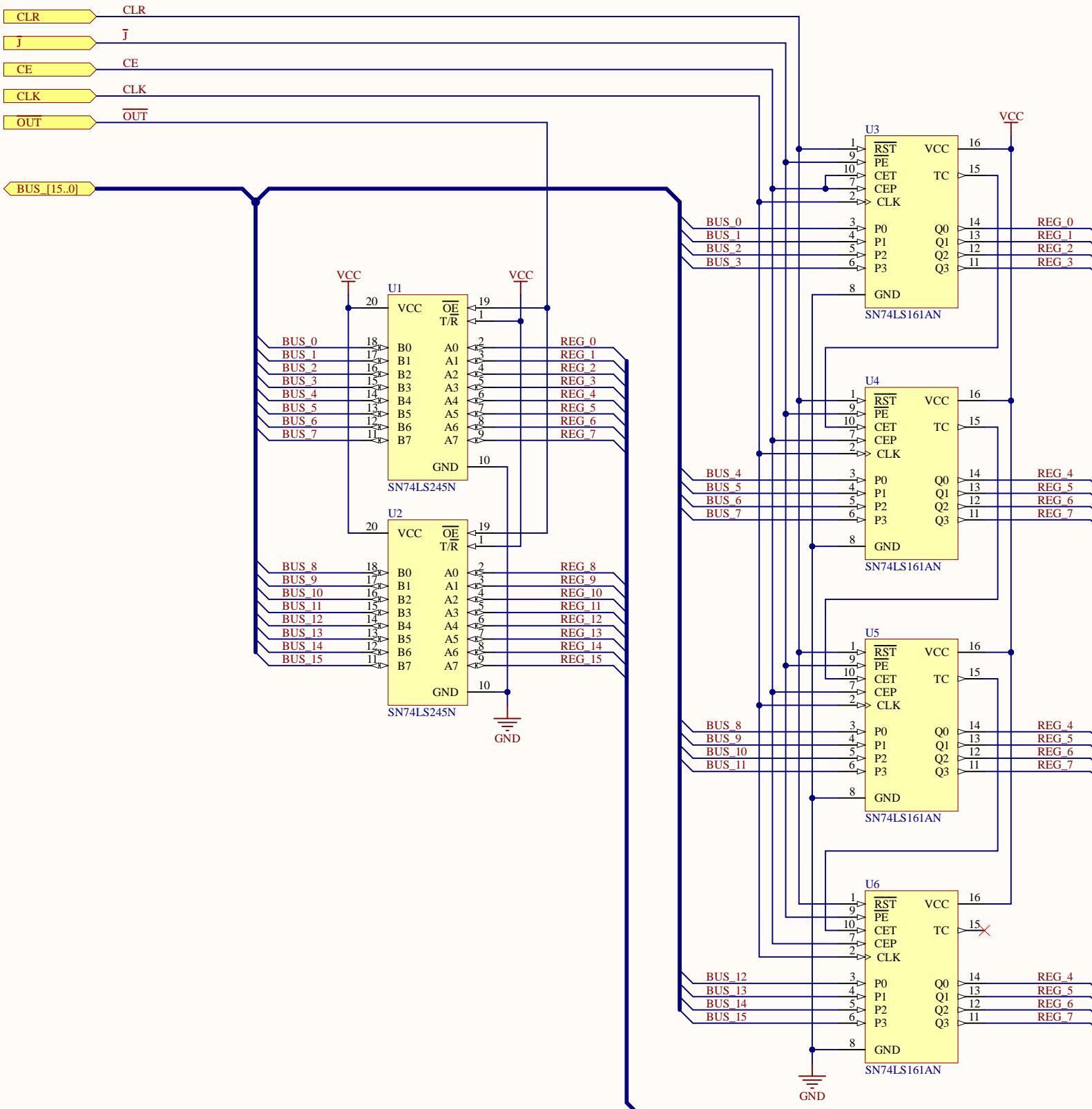


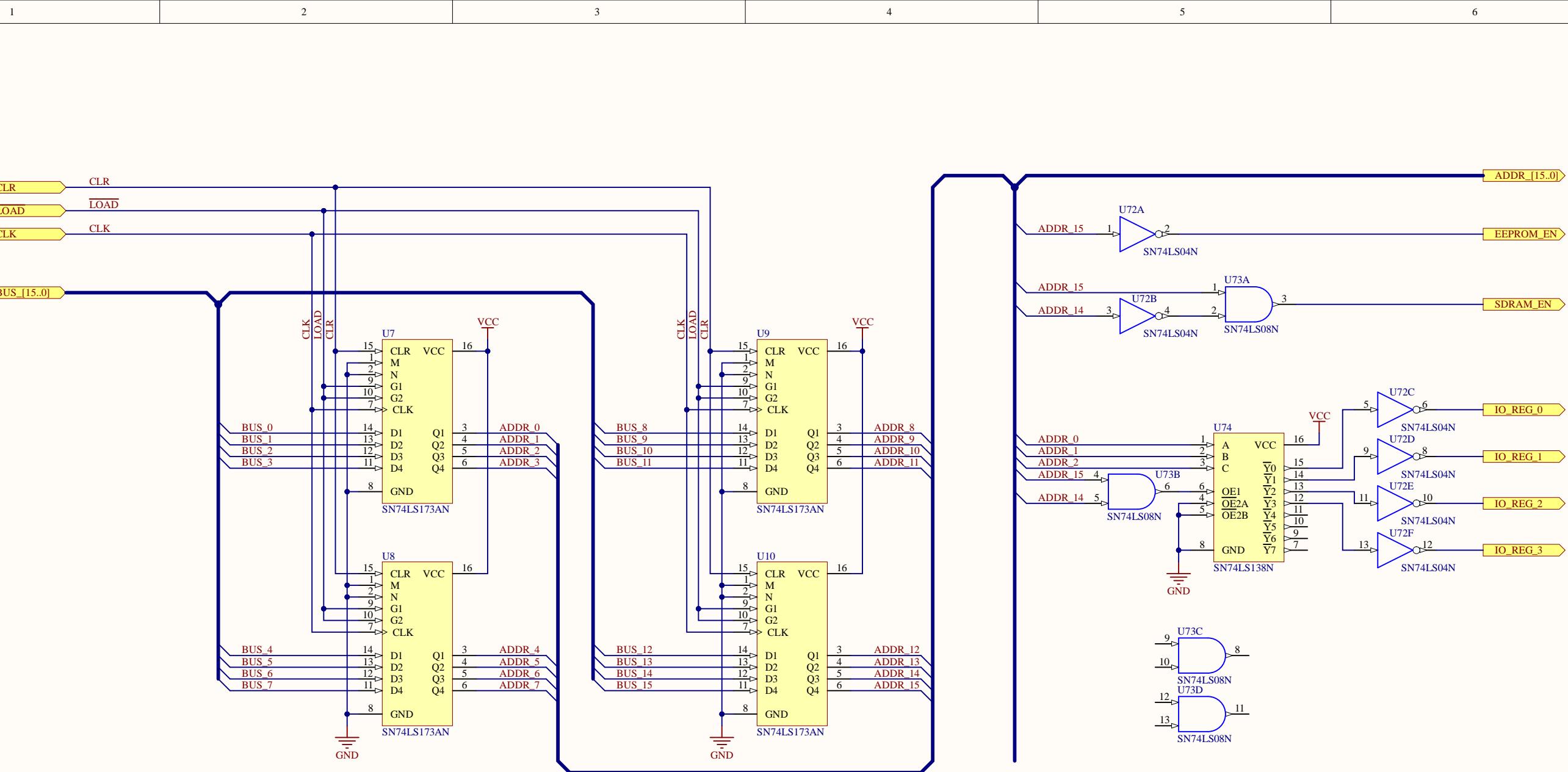
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Assembly Variant: [No Variations]				
Size: B		Number:		
Date: 9/12/2021		Revision:		



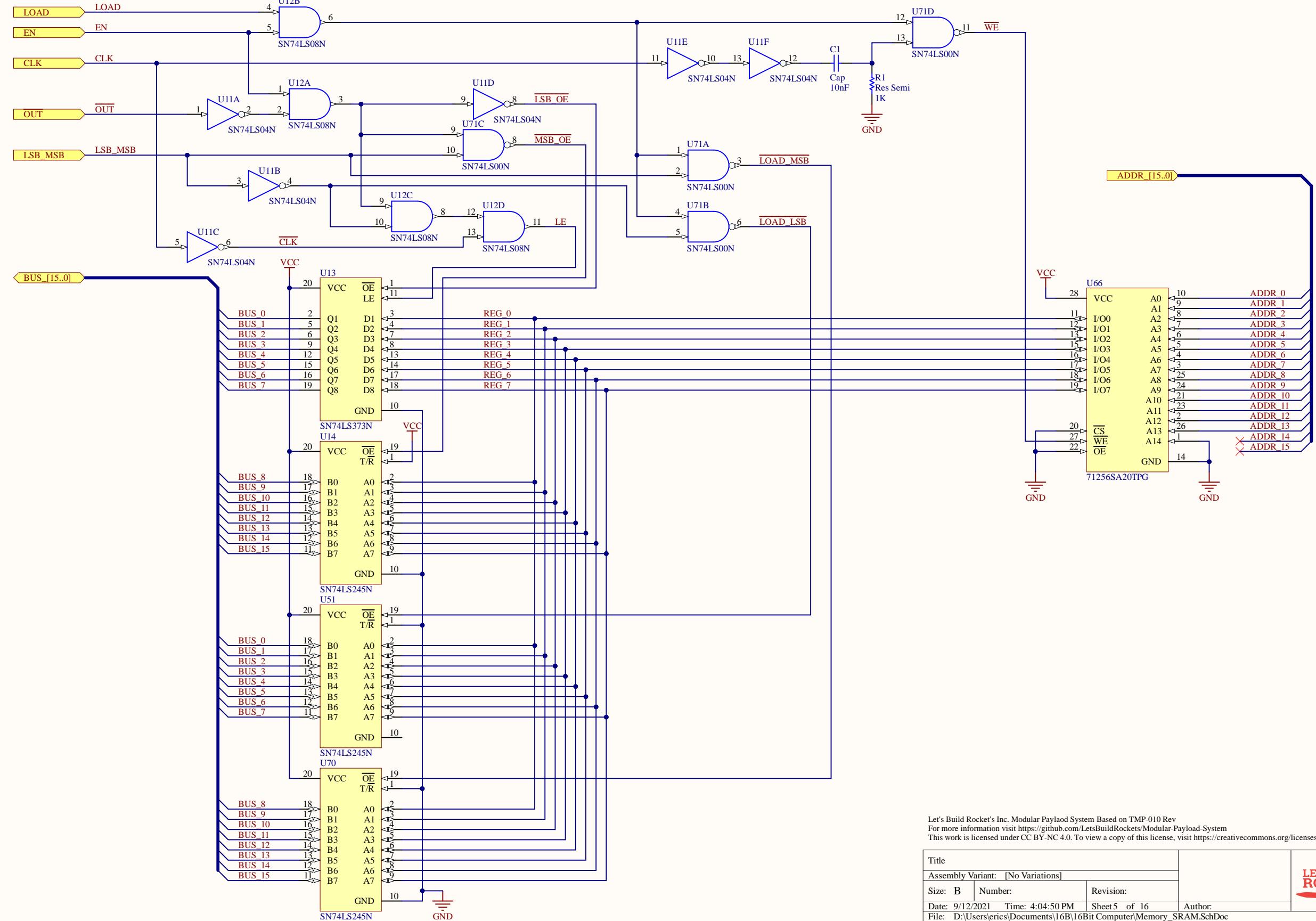


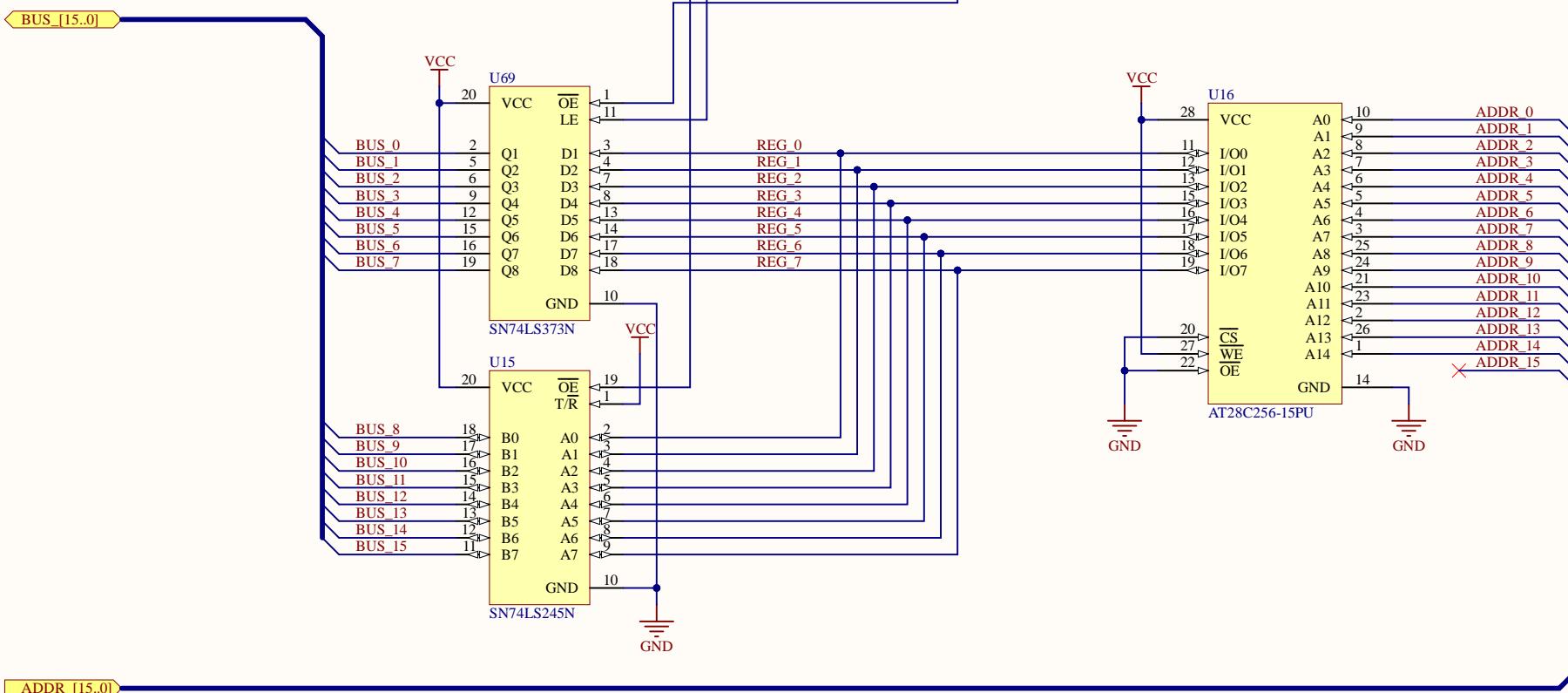
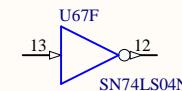
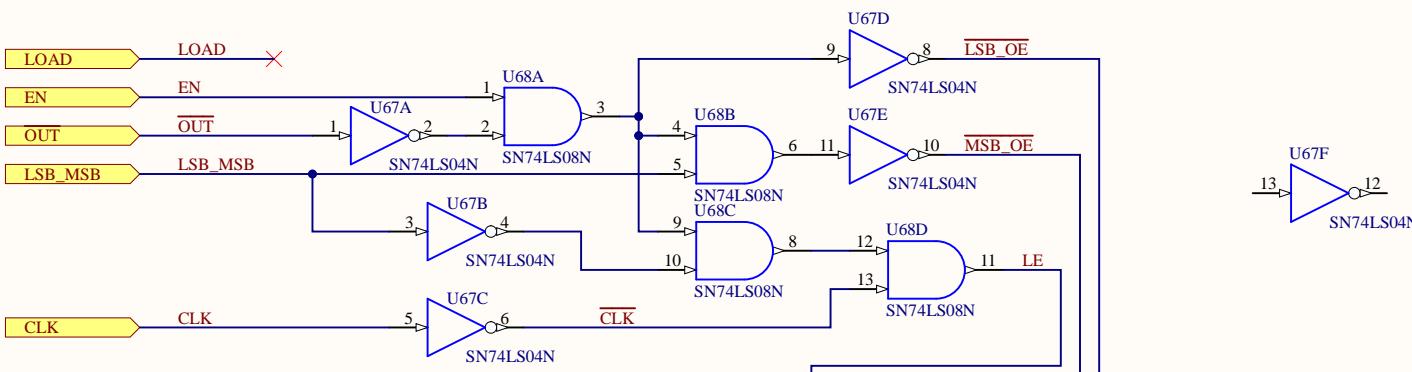




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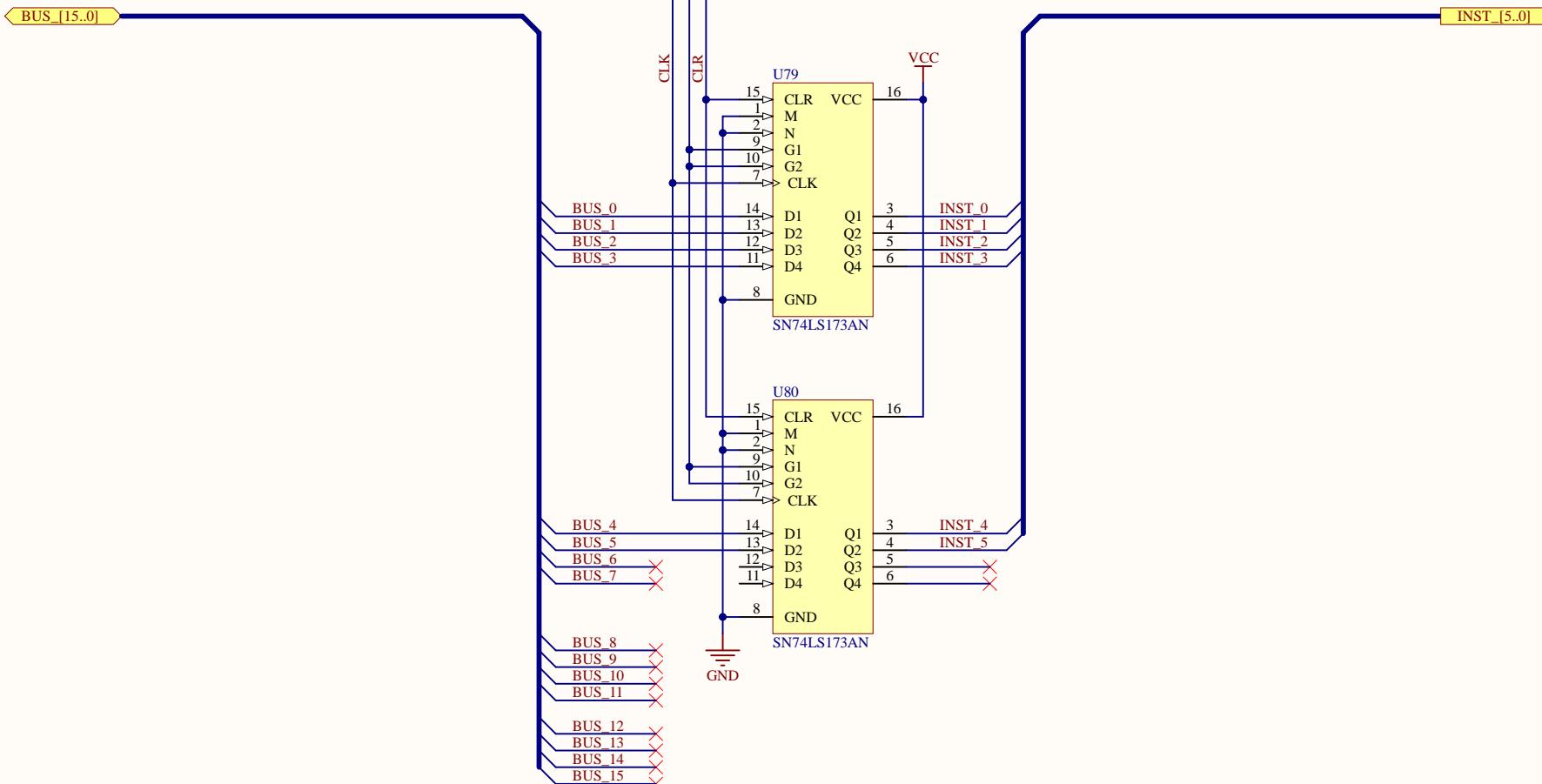
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Size:	B	Number:
Date:	9/12/2021	Time: 4:04:50 PM
File:	D:\Users\erics\Documents\16B\16Bit Computer\Memory_Address_Register.SchDoc	Sheet 4 of 16
Author:		LET'S BUILD ROCKETS





A

CLR
LOAD
CLK



A

B

B

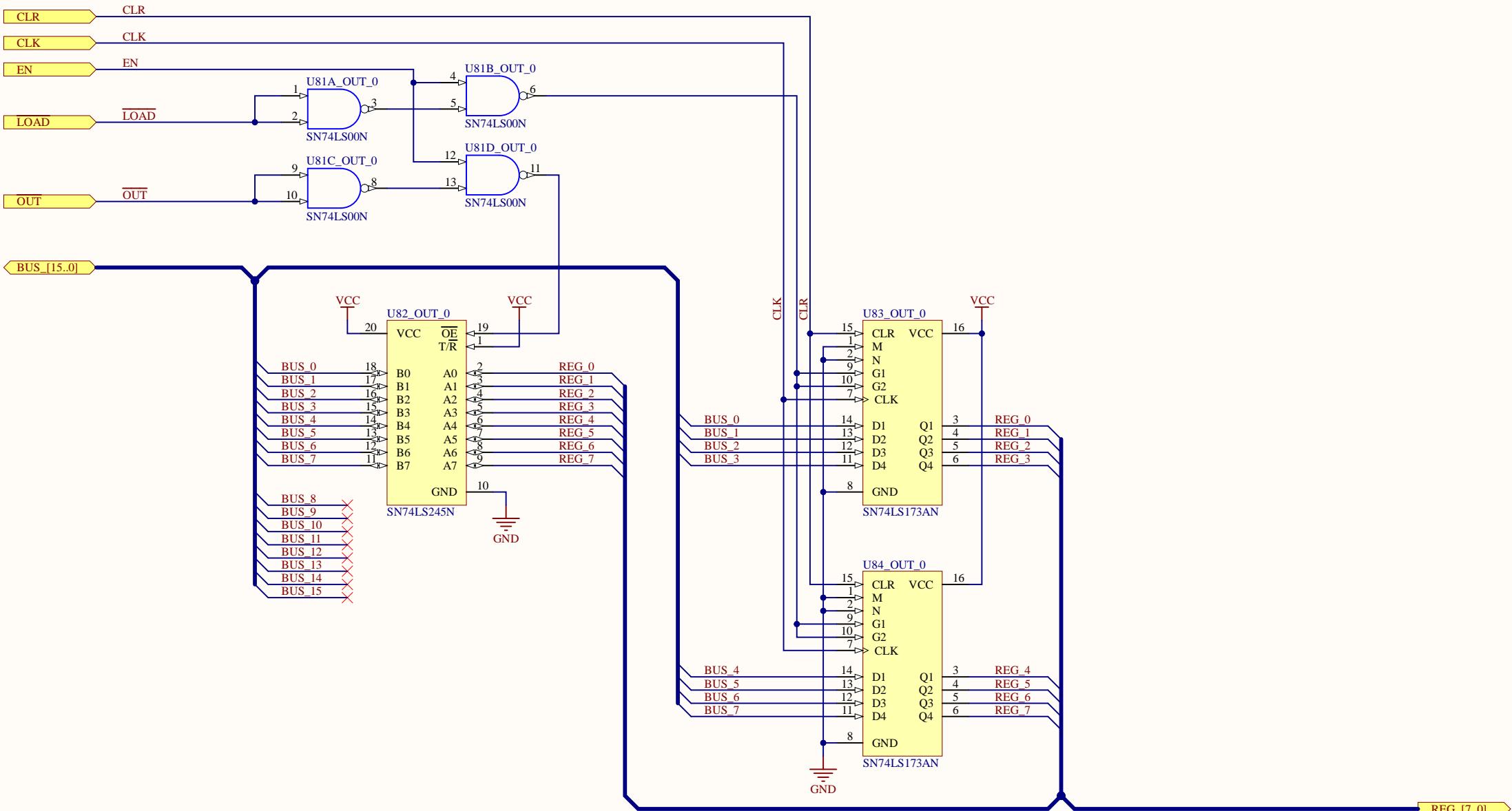
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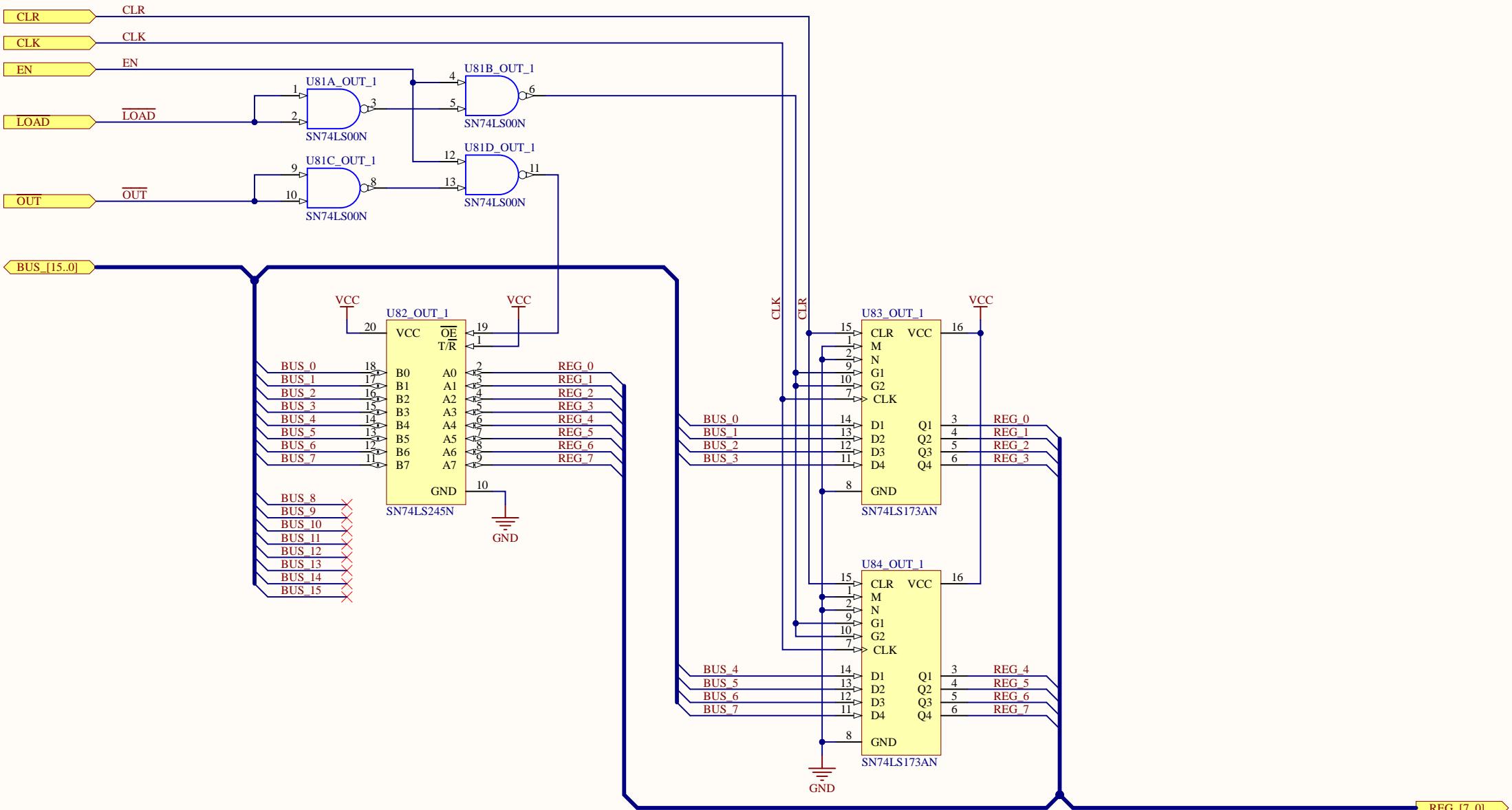
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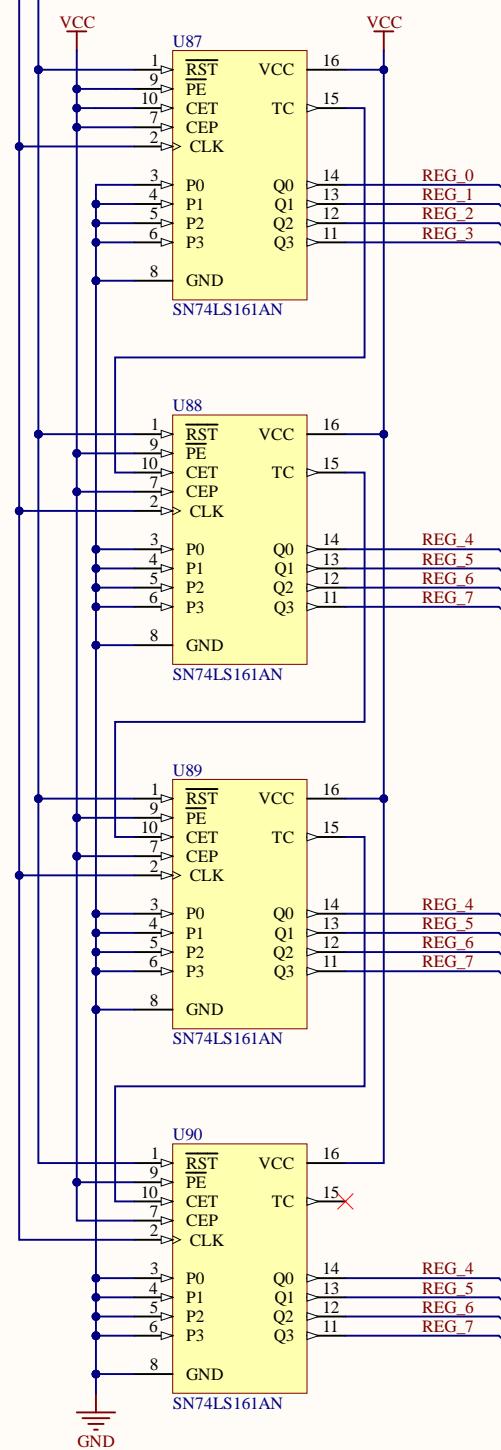
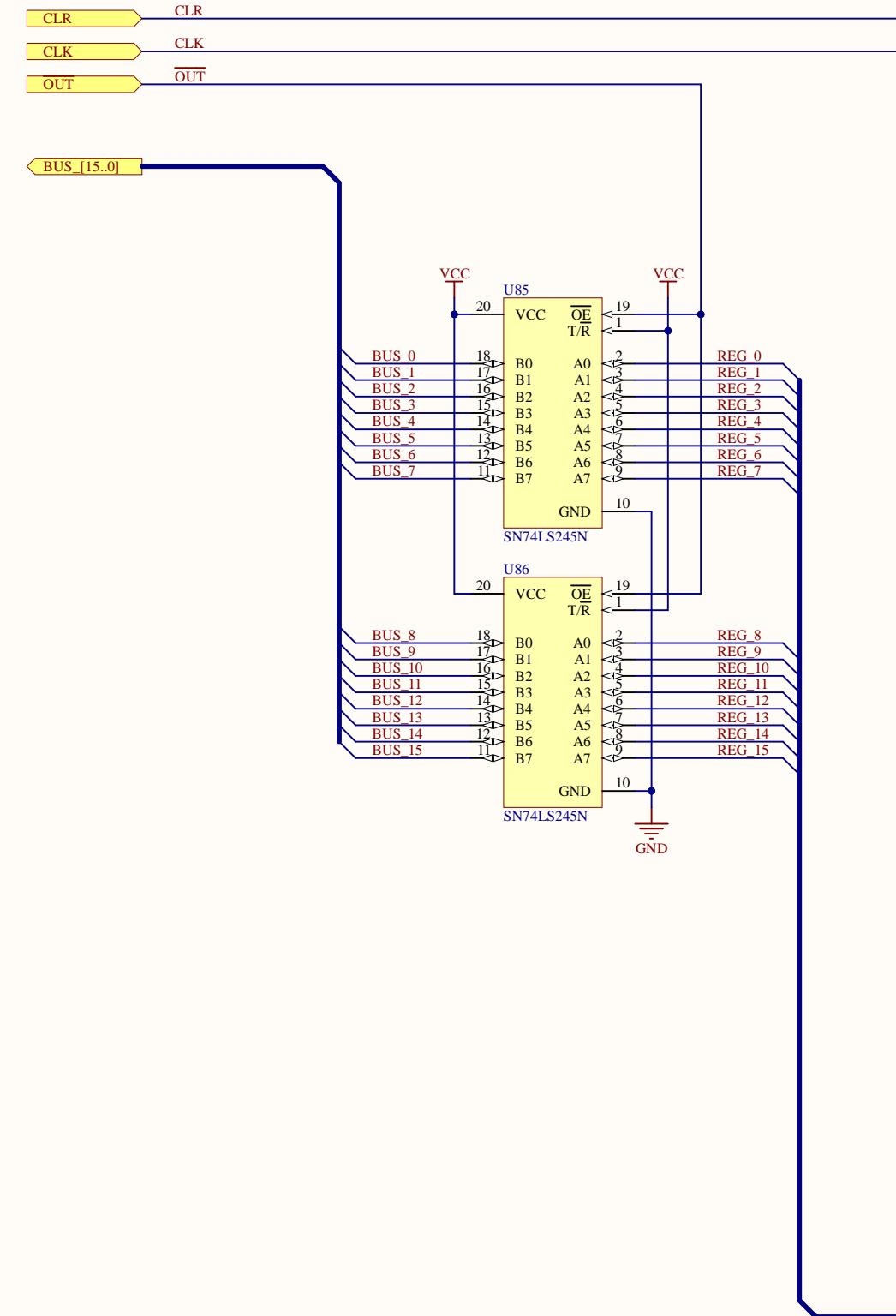
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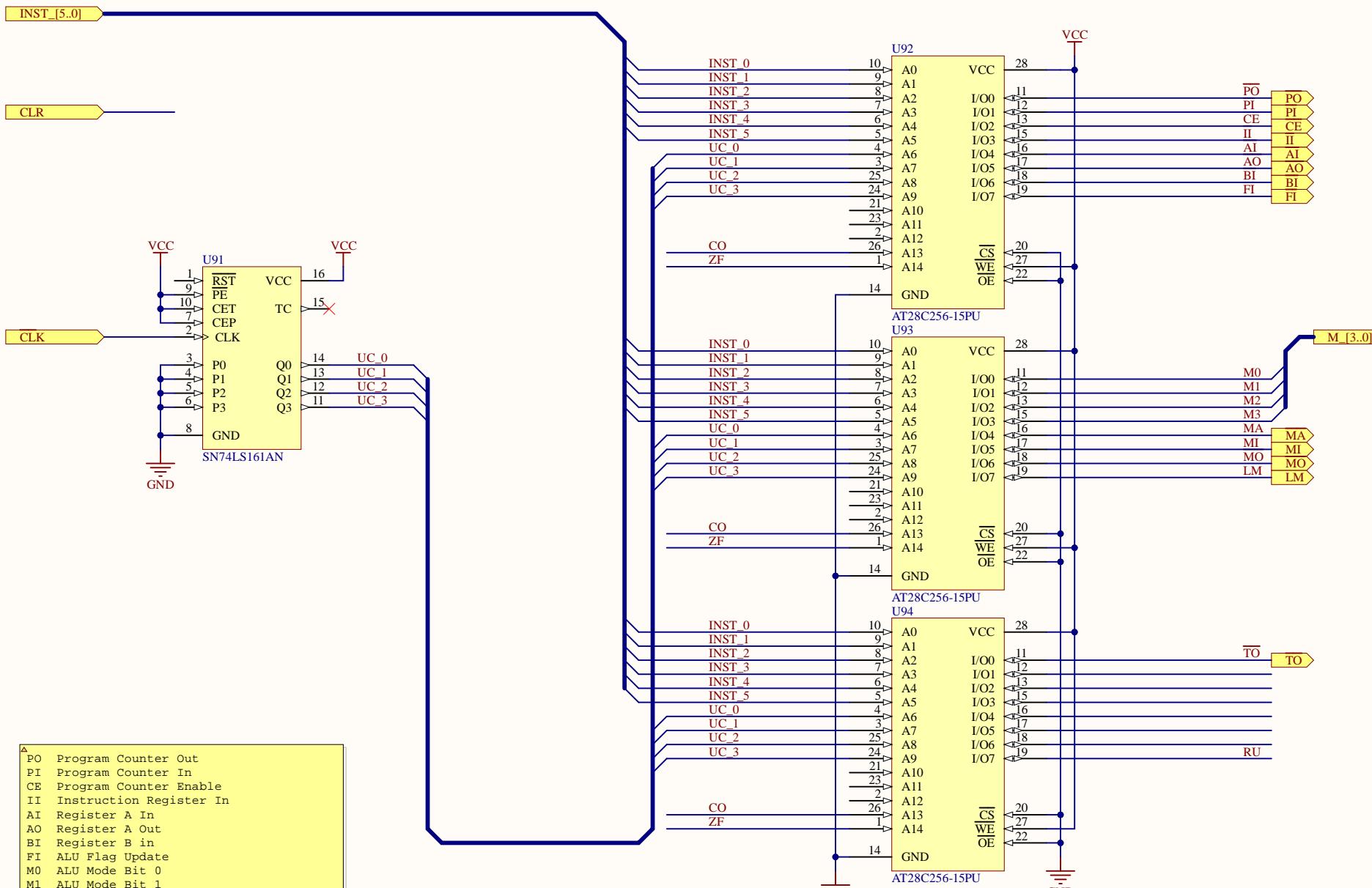
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Title				
Assembly Variant: [No Variations]				
Size: B		Number:		
Date: 9/12/2021	Time: 4:04:51 PM	Sheet 7 of 16	Author:	
File: D:\Users\erics\Documents\16B\16Bit Computer\Instruction Register.SchDoc			LET'S BUILD ROCKETS	







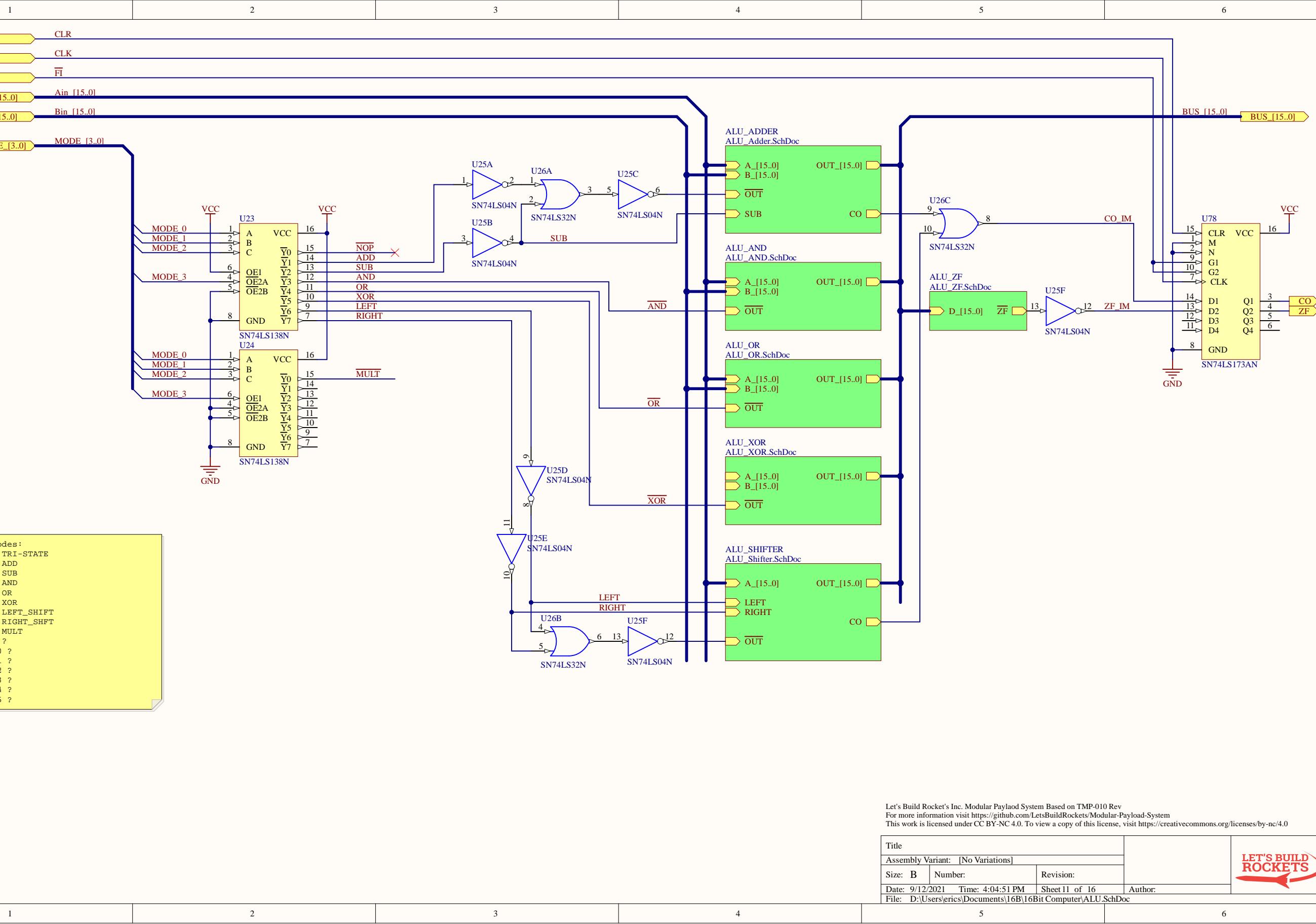
CO
ZF

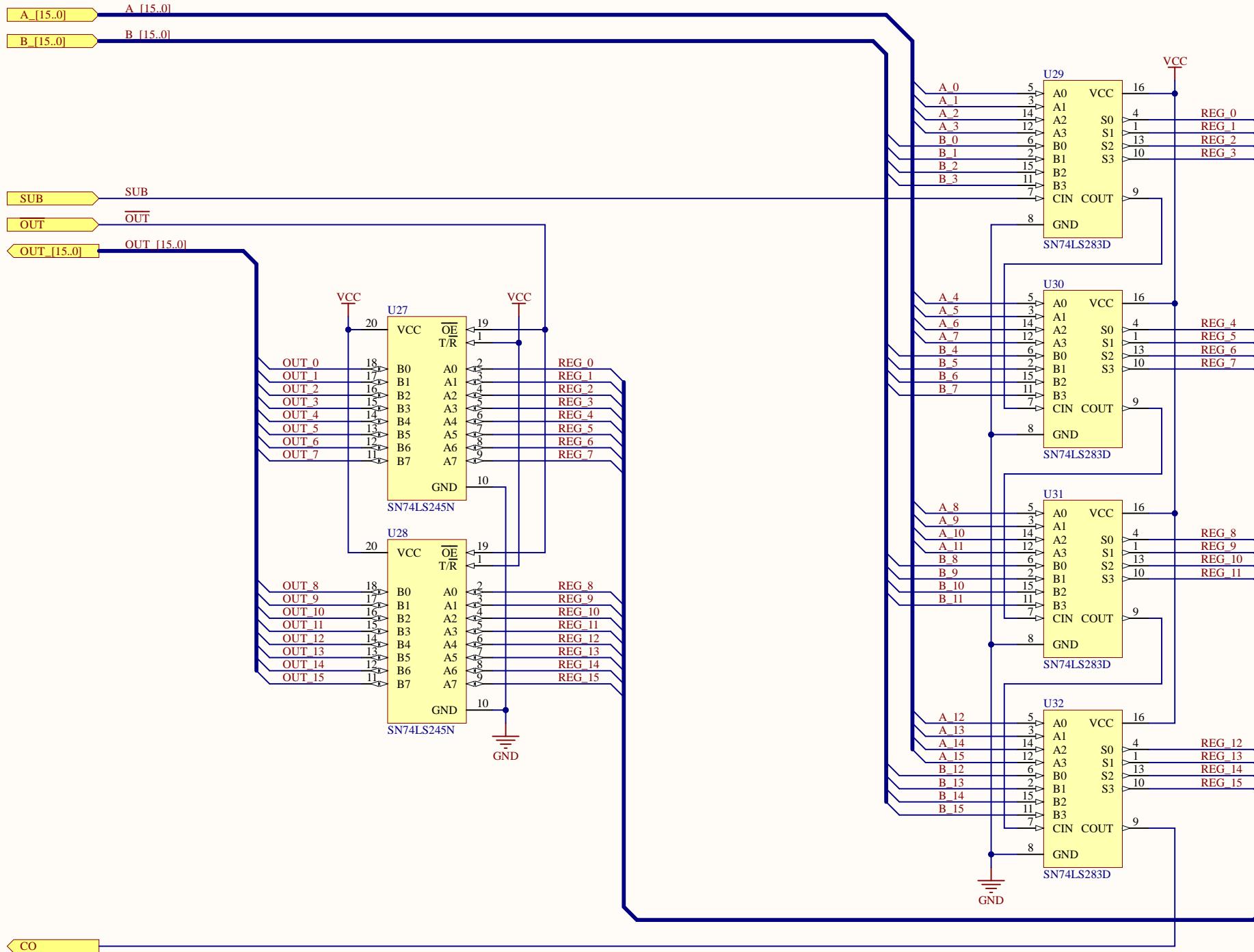
▲
 PO Program Counter Out
 PI Program Counter In
 CE Program Counter Enable
 II Instruction Register In
 AI Register A In
 AO Register A Out
 BI Register B in
 FI ALU Flag Update
 M0 ALU Mode Bit 0
 M1 ALU Mode Bit 1
 M2 ALU Mode Bit 2
 M3 ALU Mode Bit 0
 MA Memmory Address Register In
 MI Memeory In
 MO Memory Out
 LM LSB/MSB select (LSB=0)
 TO Timer Register Out
 RU Reset Microcode Counter

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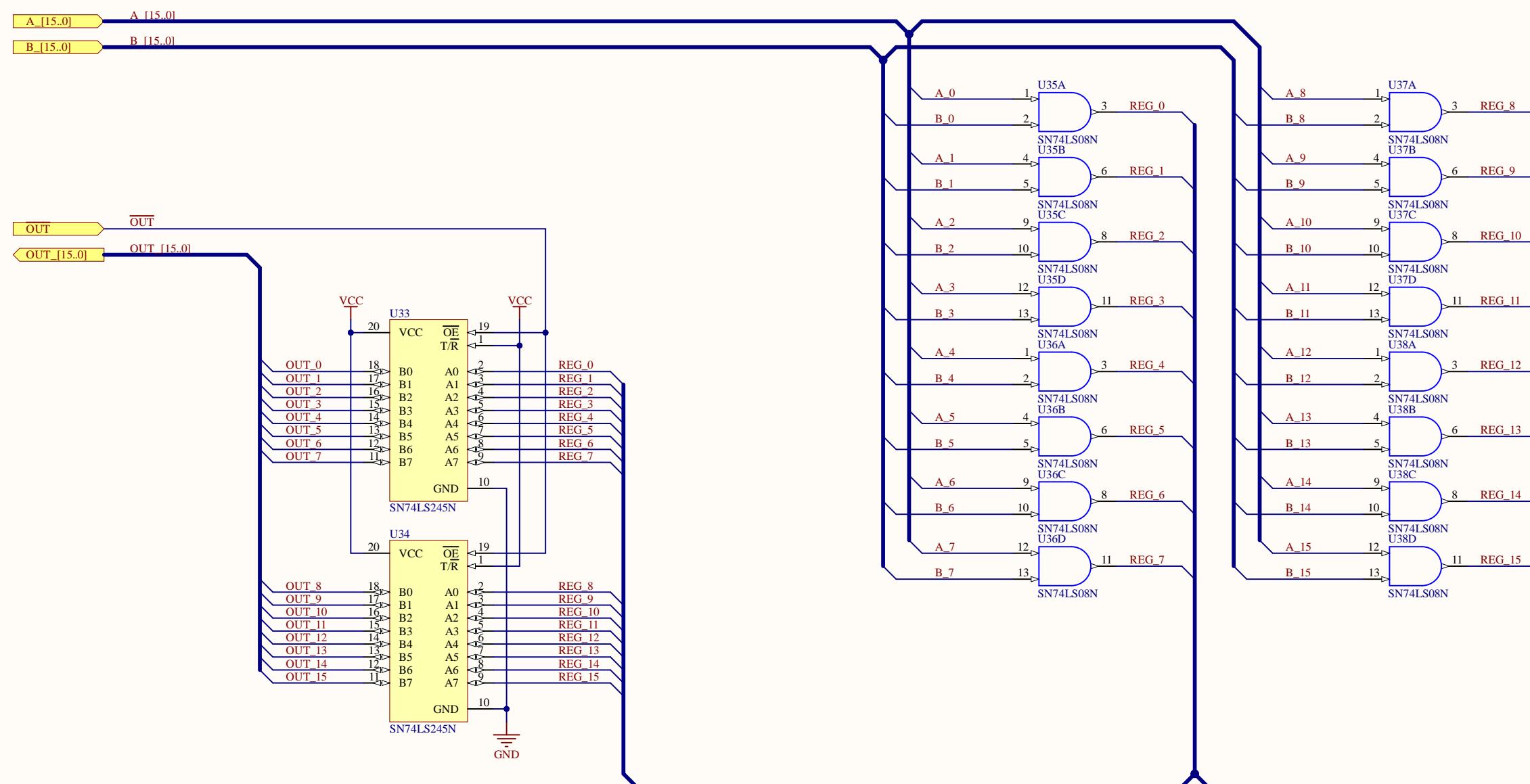
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Size:	Number:	Revision:	
Date: 9/12/2021	Time: 4:04:51 PM	Sheet 10 of 16	Author:
File: D:\Users\erics\Documents\16B\16Bit Computer\Microcode.SchDoc			

**LET'S BUILD
ROCKETS**



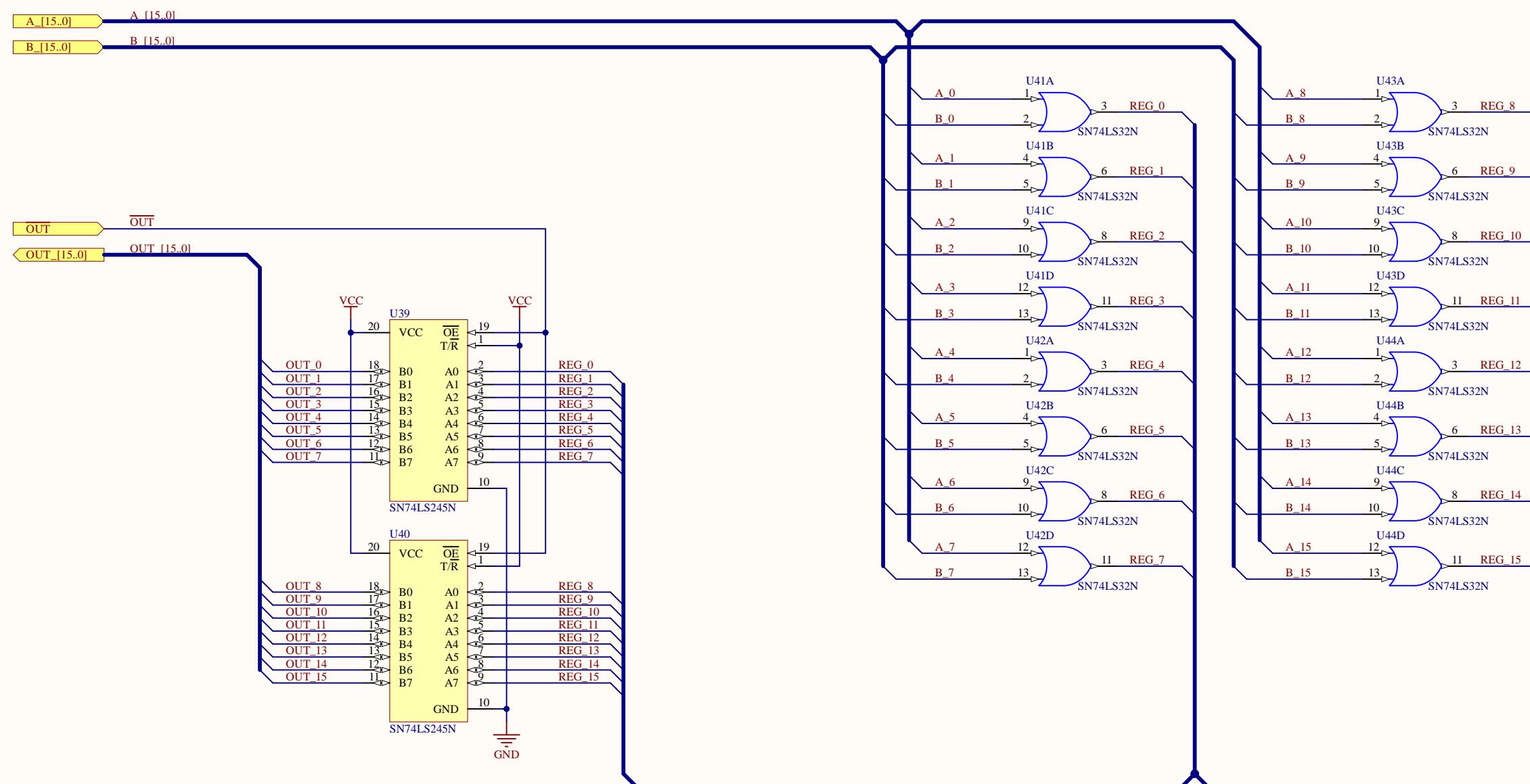


A

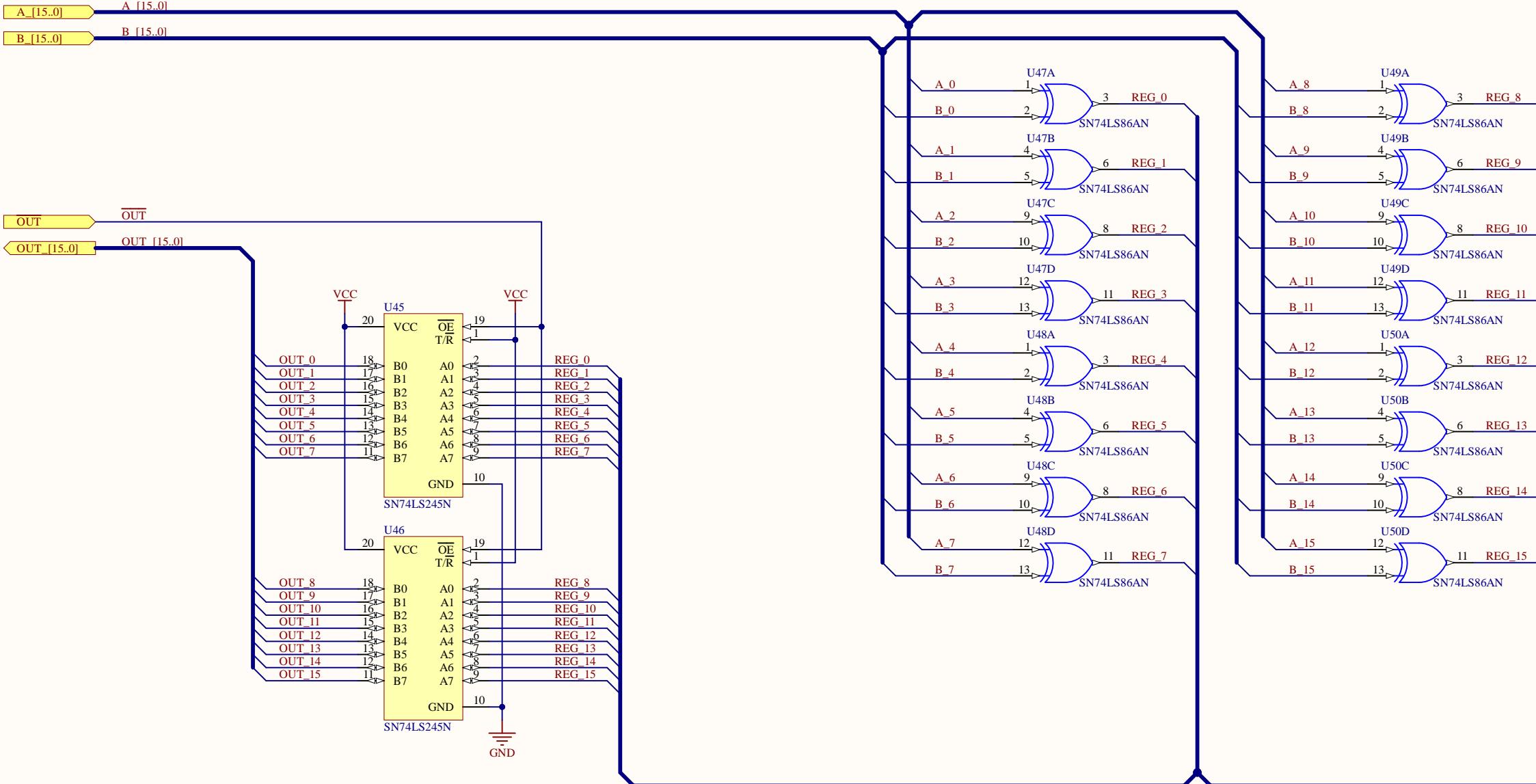


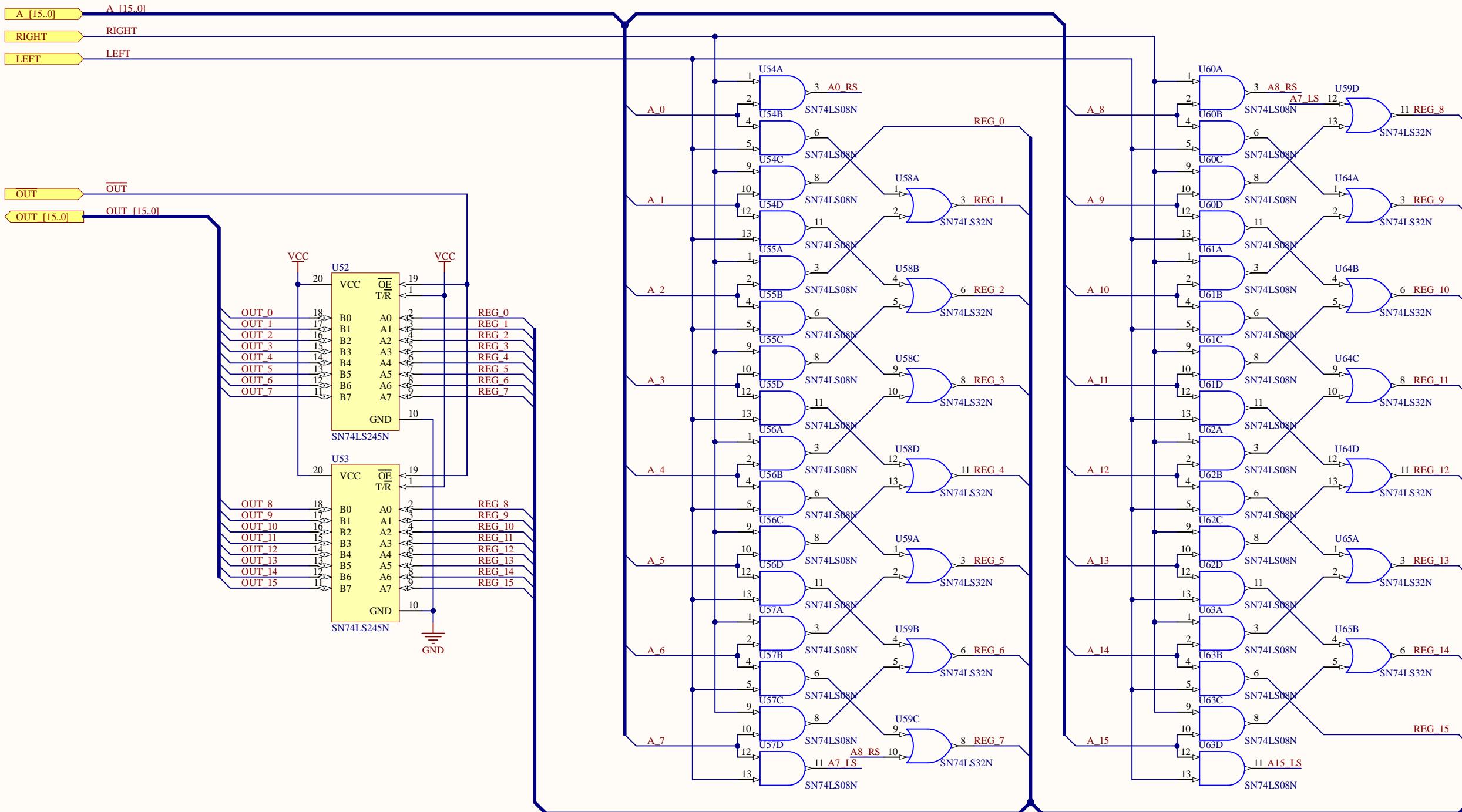
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Size: B		Number:		
Revision:				
Date: 9/12/2021		Time: 4:04:52 PM	Sheet 13 of 16	
File: D:\Users\erics\Documents\16B\16Bit Computer\ALU_AND.SchDoc		Author:		

A



Title Assembly Variant: [No Variations]			LET'S BUILD ROCKETS
Size: B	Number:	Revision:	
Date: 9/12/2021	Time: 4:04:52 PM	Sheet 14 of 16	
File: D:\Users\erics\Documents\16B\16Bit Computer\ALU_OR.SchDoc			



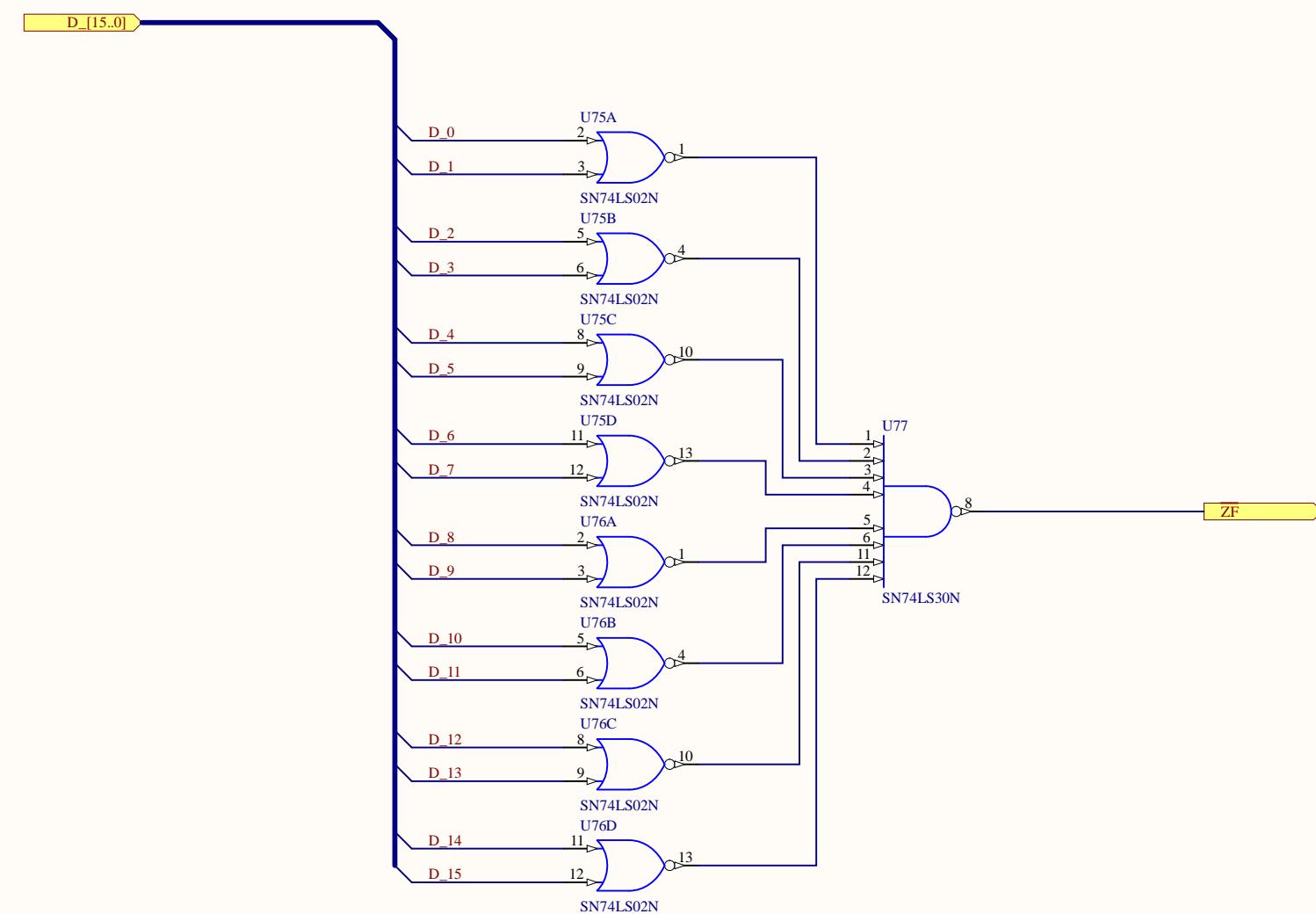


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Title			Assembly Variant:	Number:	Revision:
[No Variations]					
Size:	B				
Date:	9/12/2021	Time:	4:04:52 PM	Sheet 16 of 16	Author:
File:	D:\Users\erics\Documents\16B\16Bit Computer\ALU_Shifter.SchDoc				

LET'S BUILD
ROCKETS

A



Title			LET'S BUILD ROCKETS 	
Assembly Variant: [No Variations]				
Size: B		Number:		
Revision:				
Date: 9/12/2021		Time: 4:04:52 PM	Sheet 17 of 16	
File: D:\Users\erics\Documents\16B\16Bit Computer\ALU_ZFSchDoc		Author:		