

# Eric Tang

erictang25.github.io

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<b>Education</b>	<b>Carnegie Mellon University</b>	Pittsburgh, PA
	Ph.D. Electrical and Computer Engineering	Expected May 2026
	<b>Cornell University</b>	Ithaca, NY
	Bachelor of Science, Electrical and Computer Engineering, Computer Science minor	
	GPA: 3.88/4.30, Dean's List (all semesters), Magna Cum Laude	May 2020
	<i>Relevant Courses:</i> Complex Digital ASIC Design • Distributed Computing • Computer Architecture • Large Scale Machine Learning • Digital Communication	
<b>Awards and Honors</b>	Dean's Fellow, Carnegie Institute of Technology	Feb. 2020
	Eta Kappa Nu (IEEE-HKN)	Nov. 2018
	Tau Beta Pi	Nov. 2018
<b>Skills</b>	<b>Programming Languages:</b> Verilog, C, C++, Matlab, Python, Java, PyMTL3	
	<b>Tools:</b> Git, Altium, Verilator, VCS, Primetime PX	
<b>Research Experience</b>	<b>Cornell University, Batten Research Group</b>	
	Advisor: Christopher Batten	Aug. 2018 – May 2020
	<ul style="list-style-type: none"><li>• Created 3 stage pipelined blocking cache generator parametrized by size of cache lines and total size.</li><li>• Designed a custom energy and power characterization flow that utilizes Synopsys EDA tools (Primetime PX) to find performance metrics for custom ASIC designs.</li><li>• Ran preliminary tests and created breakout board for computer architecture test chip (BRGTC1)</li></ul>	
	<b>Cornell University, Computer Systems Laboratory</b>	
	Advisor: Zhiru Zhang	Jun. 2017 – Aug. 2017
	<ul style="list-style-type: none"><li>• Experimented with various forms of gradient descent on a GPU to filter spam emails more quickly.</li><li>• Implemented stochastic gradient descent using multiple threads with asynchronous updates in C</li></ul>	
<b>Professional Experience</b>	<b>MITRE, Bedford MA</b>	May 2019 – Aug. 2019
	<i>Position Navigation and Timing Intern</i>	
	<ul style="list-style-type: none"><li>• Identified spoofing in GPS signals from data collected during field tests.</li><li>• Created plots and maps to visualize various aspects of GPS signals</li></ul>	

	<b>Draper Laboratory, Cambridge MA</b> <i>Undergraduate Engineering Intern</i>	May 2018 – Aug. 2018
	<ul style="list-style-type: none"> <li>Designed new test procedures and soldered custom test circuits to verify proper sensor functionality.</li> <li>Automated tests utilizing oscilloscope, function generator, power sources and ammeters</li> </ul>	
<b>Teaching</b>	<b>Cornell University, College of Engineering</b> Computer Architecture, ECE 4750 <i>Teaching Assistant</i> Graded labs, problem sets and quizzes and held weekly office hours	Aug. 2019 – Dec. 2019
	Digital Logic and Computer Organization, ECE 2300 Led exam review session for over 20 students	Apr. 2019
	Multivariable Calculus, MATH 1920 <i>Academic Excellence Workshop Facilitator</i> Taught and created problem sets for a class of 15 students	Aug. 2017 – Dec. 2017
<b>Activities</b>	<b>Resistance Racing, Cornell University</b> <i>Electrical Subteam Lead</i>	Sep. 2017 – May 2020
	<ul style="list-style-type: none"> <li>Designed and optimized an energy efficient BLDC motor controller using field-oriented control.</li> <li>Designed, populated, and tested a PCB for measuring power consumption (joulemeter).</li> <li>Tested and integrated battery management system, power converters, data acquisition, motor controller and automation systems onto the vehicle</li> </ul>	
	<b>Club Swimming, Cornell University</b>	Sep. 2016 – May 2017