

AI Term Project Report

Eric Wilson
Advanced Radar Research Center
The University of Oklahoma
Norman, Oklahoma, United States
Eric.J.Wilson-1@ou.edu

—, —,
—
—
—
—

Abstract—This class project aims to implement a machine learning based method of designing electrical analog filters using an unbalanced-ladder topology. The AI method developed is compared to standard filter prototypes using G-coefficients and element scaling.

Keywords—AI in Analog Design, Filter Synthesis, G-coefficients, Filter prototypes, Element Scaling

I. DESCRIPTION OF PROBLEM

Analog filters are used in many electronic devices to block unwanted power at specific frequencies from entering the system. Cell phones, radios and radar systems all use analog filters in the front end to prevent out of band frequencies from compressing sensitive receive electronics (leading to degraded performance). There are some closed form methods for computing filter component values, BUT they are limited to very basic filter shapes (like low-pass, high-pass, symmetric bandpass) AND almost always require a skilled human designer to tune the values afterward. Additionally, the closed form filter synthesis methods cannot easily account for higher-order real-world effects which have a big impact on component value selection. These higher order effects include unintended coupling between circuit elements, component parasitics, and propagation delays for high-frequency distributed circuits.

The problem: Existing closed form filter synthesis methods are limited to very basic filter shapes, do not account for impactful higher order effects, and require significant time for human designers to tweak values by trial and error.

Overall Goal of Paper: Make progress towards a machine learning (ML) model that can:

- 1) generate filters with much more arbitrary frequency responses (not just simple low-pass, symmetric bandpass, etc.),
- 2) accurately account for the higher-order effects which closed-form solutions ignore,
- 3) significantly decrease the amount of human tuning required by generating more accurate component values.

Specific Project Scope: This project specifically tackles the design of 5th-order unbalanced ladder filters with series element first. The user gives model the desired filter frequency

response (can be arbitrary shape), and the model designs a working filter.

Figure 3 shows a 3rd order filter topology. For this design, each element of the ladder is made up of a capacitor and inductor. The series elements are made up of a series LC and the shunt elements are composed of a parallel LC. This means there are 10 different electrical component values to select for the filter design.

II. DESCRIPTION OF APPROACH

To achieve the overall project goal the following approach was taken. Each entry has its own sub-section:

- 1) [Research existing attempts at ML based circuit designers](#)
- 2) [Learn and implement the standard closed form filter synthesis methods *](#)
- 3) [Develop method to quickly evaluate circuit performance AND verify against commercial circuit solvers **](#)
- 4) [Generate Training Data](#)
- 5) [Model Training Details](#)

Importance of criterion selection

Multi-Step Training Program to significantly increase training speed

* This is critical for publishing a paper in the future. Many of the machine learning papers on circuit design DO NOT compare against standard circuit design methods and lack sufficient electrical engineering expertise. The lack of standard EE knowledge is a large hole in the current literature as of 2025.

** generating sufficient data can be expensive in microwave design

A. [Research existing attempts at ML based circuit designers](#)

Existing work on ML based circuit designers can be broken into parameter design vs. topological design. Parameter designs have the AI only choose component values for a pre-chosen circuit, whereas in ML topological design the AI generates the entire circuit with very little human guidance.

My class presentation and survey essay go into depth on the recent ML microwave design literature published in 2023 2025. In those submissions the basics of the RF design process are explained, and the potential for limited AI assistance at each step is briefly discussed [1]. Next, the distinction

between parameter optimization and topological design is made. [2] was used to demonstrate AI's ability to balance the many parameters required in an RF design whose topology is fixed. Then, the topological AI design algorithms described in [3], [4], [5] are described and methods compared. Particular attention is paid to the wavelet transform used in [3] and how it aids in inverse mapping. Additionally, the two-step learning method and fully connected layer dropout operations in [4] was discussed in depth.

Some key items to note are:

- 1) Collecting the required amount of training data is very difficult for microwave designs because the most accurate electromagnetic simulators are extremely compute intensive.
- 2) Many of the AI topological design papers involve training a model which quickly evaluates circuit performance with very little compute, this model is then used to guide an evolutionary algorithm which explores the design space.
- 3) This project takes the reverse approach of generating a model which directly generates the filter design, instead of using an evolutionary algorithm to search through many different designs every time the user asks for a new filter synthesis.

B. Learn and implement the standard closed form filter synthesis methods

One of the standard methods used for filter synthesis involves calculating a table of values called G-coefficients which define a prototype filter. It is called a prototype filter because its cutoff frequency is 1 rad/sec. The G-coefficients must be specifically scaled and altered to achieve a different cutoff frequency and/or to switch filter response shape (e.g. low pass, high pass, bandpass). Different families of filters can be synthesized by using a wide range of complicated recursive formulas. The Butterworth and Chebyshev filter styles are common place in the filter synthesis world.

Why this matters: Many of the existing ML microwave design papers discuss performance of circuits the AI generates, but the authors do not analyze the results against standard methods. For example, in [4] they discuss how amazing it is the AI designed an electrically small patch antenna, but they don't mention antenna efficiency or that electrically small antennas is an entire field of study which has defined upper bound limits of antenna performance for a specific size. [4] is one of the best ML microwave papers as of 2025, but it severely lacks sufficient EE background.

Below the method of computing Butterworth and Chebyshev filter coefficients is described.

For standard filter synthesis there are several commonly used designs which dictate where the poles and zeros of the filter lie. Butterworth and Chebyshev filters are common and are synthesized as follows.

— Butterworth Filter Synthesis

$$g_r = 2 \sin \left(\frac{(2r-1)\pi}{2n} \right), \quad r = 1, \dots, n \quad (1)$$

— Chebyshev Filter Synthesis

— Insertion Loss Ripple

$$L_{A_{dB}} = -10 \cdot \log \left(1 - \left(10^{-\frac{L_{r,dB}}{10}} \right) \right) \quad (2)$$

— Ripple Factor

$$\epsilon = \sqrt{10^{\frac{L_{A_{dB}}}{10}} - 1} \quad (3)$$

— coefficient for input port is always 1

$$g_0 = 1 \quad (4)$$

— First Circuit Element

$$g_1 = \frac{2}{\eta} \sin \left(\frac{\pi}{2n} \right) \quad (5)$$

$$\eta = \sinh \left[\frac{1}{n} \sinh^{-1} \left(\frac{1}{\epsilon} \right) \right] \quad (6)$$

— Subsequent Elements

$$g_r g_{r+1} = \frac{4 \sin \left(\frac{(2r-1)\pi}{2n} \right) \sin \left(\frac{(2r+1)\pi}{2n} \right)}{\eta^2 + \sin^2 \left(\frac{r\pi}{n} \right)}, \quad r = 1, 2, \dots, n-1 \quad (7)$$

— Load for Odd Order Filters

$$g_{\text{load}} = 1 \quad (8)$$

— Load for Even Order Filters From:

$$|S_{21}(0)|^2 = \frac{4g_{n+1}}{(g_n + 1)^2} = \frac{1}{1 + \epsilon^2} \quad (9)$$

Solve for g_{n+1} :

- For $S_{11}(0) \geq 0$:

$$g_{n+1} = \frac{(\epsilon + \sqrt{1 + \epsilon^2})^2}{1} \quad (10)$$

- For $S_{11}(0) \leq 0$:

$$g_{n+1} = \frac{1}{(\epsilon + \sqrt{1 + \epsilon^2})^2} \quad (11)$$

C. Develop method to quickly evaluate circuit performance AND verify against commercial circuit solvers

A significant amount of time went into writing the code to compute the G-coefficients and to scale the values to get a standard filter design.

I wrote a program that calculates the G-coefficients for Butterworth and Chebyshev filters using the recursive equations above. I also wrote the scripts that scale those G-coefficients to get electrical component values for inductors and capacitors. Finally, I wrote a circuit simulator that has been checked against actual circuit simulator to verify accuracy. Figure 4 shows the G-coefficients calculated for Chebyshev filter prototypes of different orders.

I also wrote scripts which simulates the filter circuit to measure its frequency response. The results of my circuit

Chebyshev Lowpass Prototype Coef. (0.05dB ripple)												g_{load} shunt first
N	g_1	g_2	g_3	g_4	g_5	g_6	g_7	g_8	g_9	g_{10}	g_{11}	
1	0.2152	1.0000										
2	0.6923	0.5585	1.2396									0.8067
3	0.8794	1.1132	0.8794	1.0000								
4	0.9588	1.2970	1.6078	0.7734	1.2396							0.8067
5	0.9984	1.3745	1.8283	1.3745	0.9984	1.0000						
6	1.0208	1.4141	1.9183	1.5475	1.7529	0.8235	1.2396					0.8067
7	1.0346	1.4369	1.9637	1.6162	1.9637	1.4369	1.0346	1.0000				
8	1.0436	1.4514	1.9899	1.6503	2.0457	1.6053	1.7992	0.8419	1.2396			0.8067
9	1.0499	1.4611	2.0065	1.6698	2.0858	1.6698	2.0065	1.4611	1.0499	1.0000		
10	1.0544	1.4679	2.0177	1.6820	2.1085	1.7009	2.0851	1.6277	1.8197	0.8506	1.2396	0.8067

Chebyshev Lowpass Prototype Coef. (10 dB ripple)												g_{load} shunt first
N	g_1	g_2	g_3	g_4	g_5	g_6	g_7	g_8	g_9	g_{10}	g_{11}	
1	0.6667	1.0000										
2	1.3601	0.7066	1.9250									0.5195
3	1.5529	1.1058	1.5529	1.0000								
4	1.6269	1.2051	2.3198	0.8452	1.9250							0.5195
5	1.6625	1.2436	2.4956	1.2436	1.6625	1.0000						
6	1.6821	1.2626	2.5617	1.3308	2.4304	0.8738	1.9250					0.5195
7	1.6940	1.2733	2.5940	1.3627	2.5940	1.2733	1.6940	1.0000				
8	1.7019	1.2801	2.6123	1.3781	2.6527	1.3571	2.4641	0.8841	1.9250			0.5195
9	1.7072	1.2846	2.6238	1.3867	2.6805	1.3867	2.6238	1.2846	1.7072	1.0000		
10	1.7111	1.2877	2.6315	1.3921	2.6960	1.4006	2.6797	1.3671	2.4789	0.8889	1.9250	0.5195

Fig. 1. Computed Chebyshev G-coefficients. These values have been verified against other sources.

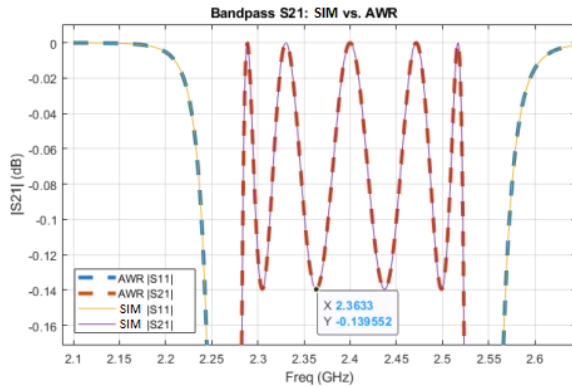


Fig. 2. S21 of a bandpass filter simulated with AWR and my own script. Notice the very zoomed in vertical axis to accurately measure the filter passband ripple. My circuit simulator lines up exactly with the commercial solver.

simulator were compared against a commercial microwave circuit simulator (Microwave Office AWR). Figure 5 shows that my simulated filter performance exactly aligns with the commercial solver. Notice the precise alignment between the commercial solver (AWR) and my own circuit simulator script. It was critical to write the circuit solver so that it could run on the GPU for fast simulation time and so that an ML model training on the GPU could easily differentiate through the weights of the circuit solver. This allows the training ML model to have its loss calculated off of the error of circuit element values OR the difference in the input frequency response versus the generated circuit's simulated frequency response. The difference between these methods is further discussed below.

D. Generate Training Data

I wrote a full set of functions which allowed me to generate filters with different parameters. I used Latin Hyper Cube

sampling to generate a random set of training data, validation data, and testing data. I also added the ability to introduce varying levels of component variation in the generated filter designs.

I used this to generate three categories of training data that is used in my training (more about training steps in next section.)

- 1) Perfect Chebyshev Filter Designs (no random component variation)
- 2) Chebyshev filters with varying level of random component variation
- 3) Completely randomly generated component values

For the Chebyshev family of filters there are three main parameters: Center Frequency, Passband Ripple (dB), and Fractional Bandwidth. The Hyper Cube Sampling ensures that a random sample is taken in each section of this 3-dimensional space. LHCS has the advantage of better coverage for fewer samples than using the Monte-Carlo sampling approach.

TABLE I
DESCRIPTION OF TRAINING DATA STYLES

Function	Description
<code>gen_cheby_data(...)</code>	Ideal Chebyshev filters with no component variation; serves as baseline.
<code>gen_cheby_data(..., comp_var=0.1)</code>	Chebyshev filters with $\pm 10\%$ component variation for moderate fabrication noise.
<code>gen_cheby_data(..., comp_var=0.2)</code>	Chebyshev filters with $\pm 20\%$ variation to represent higher manufacturing variation.
<code>gen_cheby_data(..., comp_var=0.5)</code>	Chebyshev filters with $\pm 50\%$ variation to simulate extreme uncertainty.
<code>gen_rand_data(...)</code>	Filters with fully random component values (10^{-14} – 10^{-6}), unconstrained by a prototype.

This data was saved to files using the pickle python package for quick loading into a workspace for training.

III. EVALUATION OF METHOD

This paper demonstrates that MLP based models can perform extremely well at circuit design of analog filters with arbitrarily defined frequency responses.

IV. PROBLEM STATEMENT

My class presentation covered the recent developments in RF and microwave circuit design methods using AI. The basics of the RF design process were explained, and the potential for limited AI assistance at each step was briefly discussed [1]. Next, the distinction between parameter optimization and topological design was made. [2] was used to demonstrate AI's ability to balance the many parameters required in an RF design whose topology is fixed. Then, the topological AI design algorithms described in [3], [4], [5] were described and methods compared. Particular attention was paid to the wavelet transform used in [3] and how it aids in inverse mapping.

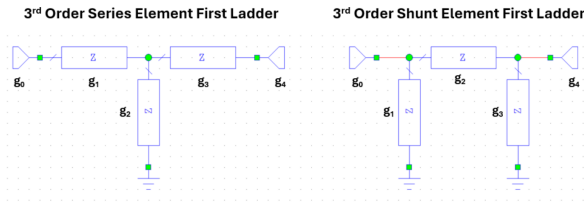


Fig. 3. 3rd order ladder representation: comes in series element first or shunt element first.

Additionally, the two-step learning method and fully connected layer dropout operations in [4] was discussed in depth.

My class project on the other hand involves training my own AI model which takes in circuit performance as an input (i.e. frequency vs. magnitude/phase values) and generates the circuit element values in a ladder topology to reach the desired performance. With a ladder topology circuit design (of high enough order) almost any circuit response can be generated, but choosing each circuit element's type and location in the network is complex. This is a great application of AI because it is easy to calculate the performance of a design, but difficult to determine which values are needed to achieve the circuit characteristics that are desired. The values that are determined by the AI can be compared with standard human design methods such as filter prototypes and scaling using G-coefficients.

V. BACKGROUND

Understanding the standard methods designers use for filter synthesis is critical to evaluating whether using AI for filter design is actually an advantage. The basics of filter synthesis is described below.

In analog filter design the unbalanced-ladder topology for circuit design is often used. Figure 3 shows two versions of a 3rd order ladder circuit.

VI. PROJECT PROGRESS

I wrote a program that calculates the G-coefficients for Butterworth and Chebyshev filters using the recursive equations above. I also wrote the scripts that scale those G-coefficients to get electrical component values for inductors and capacitors. Finally, I wrote a circuit simulator that has been checked against actual circuit simulator to verify accuracy. Figure 4 shows the G-coefficients calculated for Chebyshev filter prototypes of different orders.

I also wrote scripts which simulates the filter circuit to measure its frequency response. The results of my circuit simulator were compared against a commercial microwave circuit simulator (Microwave Office AWR). Figure 5 shows that my simulated filter performance exactly aligns with the commercial solver.

These scripts will be used to generate the training data and the circuit simulator will also be used to evaluate model performance.

N	g_1	g_2	g_3	g_4	g_5	g_6	g_7	g_8	g_9	g_10	g_11	g_load
1	0.2152	1.0000										shunt first
2	0.6923	0.5585	1.2396									0.8067
3	0.8794	1.1132	0.8794	1.0000								0.8067
4	0.9588	1.2970	1.6078	0.7734	1.2396							0.8067
5	0.9984	1.3745	1.8283	1.3745	0.9984	1.0000						0.8067
6	1.0208	1.4141	1.9183	1.5475	1.7529	0.8235	1.2396					0.8067
7	1.0346	1.4369	1.9637	1.6162	1.9637	1.4369	1.0346	1.0000				0.8067
8	1.0436	1.4514	1.9899	1.6503	2.0457	1.6053	1.7992	0.8419	1.2396			0.8067
9	1.0499	1.4611	2.0065	1.6698	2.0858	1.6698	2.0065	1.4611	1.0499	1.0000		0.8067
10	1.0544	1.4679	2.0177	1.6820	2.1085	1.7009	2.0851	1.6277	1.8197	0.8506	1.2396	0.8067

N	g_1	g_2	g_3	g_4	g_5	g_6	g_7	g_8	g_9	g_10	g_11	g_load
1	0.6667	1.0000										shunt first
2	1.3601	0.7066	1.9250									0.5195
3	1.5529	1.1058	1.5529	1.0000								0.5195
4	1.6269	1.2051	2.3198	0.8452	1.9250							0.5195
5	1.6625	1.2436	2.4956	1.2436	1.6625	1.0000						0.5195
6	1.6821	1.2626	2.5617	1.3308	2.4304	0.8738	1.9250					0.5195
7	1.6940	1.2733	2.5940	1.3627	2.5940	1.2733	1.6940	1.0000				0.5195
8	1.7019	1.2801	2.6123	1.3781	2.6527	1.3571	2.4641	0.8841	1.9250			0.5195
9	1.7072	1.2846	2.6238	1.3867	2.6805	1.3867	2.6238	1.2846	1.7072	1.0000		0.5195
10	1.7111	1.2877	2.6315	1.3921	2.6960	1.4006	2.6797	1.3671	2.4789	0.8889	1.9250	0.5195

Fig. 4. Computed Chebyshev G-coefficients. These values have been verified against other sources.

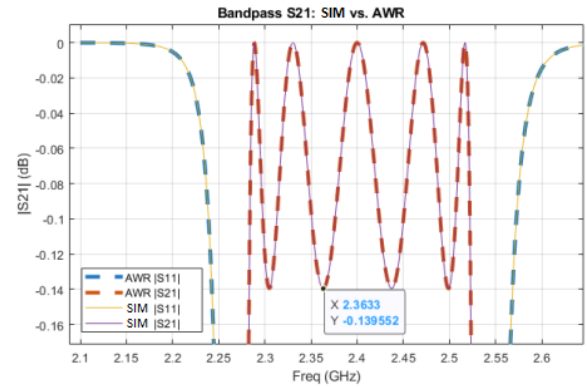


Fig. 5. S21 of a bandpass filter simulated with AWR and my own script. Notice the very zoomed in vertical axis to accurately measure the filter passband ripple. My circuit simulator lines up exactly with the commercial solver.

VII. PROJECT SCOPE

For this project a ladder topology of 6 circuit elements is chosen over a 100x frequency bandwidth. Each circuit element has the following options: open, short, resistor, capacitor, inductor, series LC, or shunt LC. Many randomly generated circuit designs will be generated and performance calculated over the frequency range. This data will be used to train the model. The performance of the model will be based on how well it can design a circuit to match desired performance.

VIII. CONCLUSION

A program has been written that can calculate the G-coefficients for Butterworth and Chebyshev filters. More scripts have been written that can scale those G-coefficients to get electrical component values for inductors and capacitors. Finally, I wrote a circuit simulator that has been checked against actual circuit simulator to verify accuracy. These

scripts will be used to generate the training data and the circuit simulator will also be used to evaluate model performance.

REFERENCES

- [1] P.-Y. Lee and T.-C. Chen, "Ai-driven innovations in ic designs: From planning to implementation," in *2024 International VLSI Symposium on Technology, Systems and Applications (VLSI TSA)*, April 2024, pp. 1–2.
- [2] L. Xue, H. Fan, Y. Ding, and B. Liu, "A design methodology of mmic power amplifiers using ai-driven design techniques," in *2023 19th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD)*, July 2023, pp. 1–4.
- [3] K. Xu *et al.*, "Flexible inverse design of microwave filter customized on demand with wavelet transform deep learning," *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, pp. 1–1, 2024.
- [4] E. A. Karahan, J. Zhou, Z. Liu, Z. Shao, S. Fisher, and K. Sengupta, "Deep learning enabled design of rf/mmwave ic and antennas," in *2024 IEEE 67th International Midwest Symposium on Circuits and Systems (MWSCAS)*, August 2024, pp. 769–772.
- [5] H. Taşkıran, E. Sağlıcan, and E. Afacan, "Ann-based analog/rf ic synthesis featuring reinforcement learning-based fine-tuning," in *2024 20th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD)*, July 2024, pp. 1–4.