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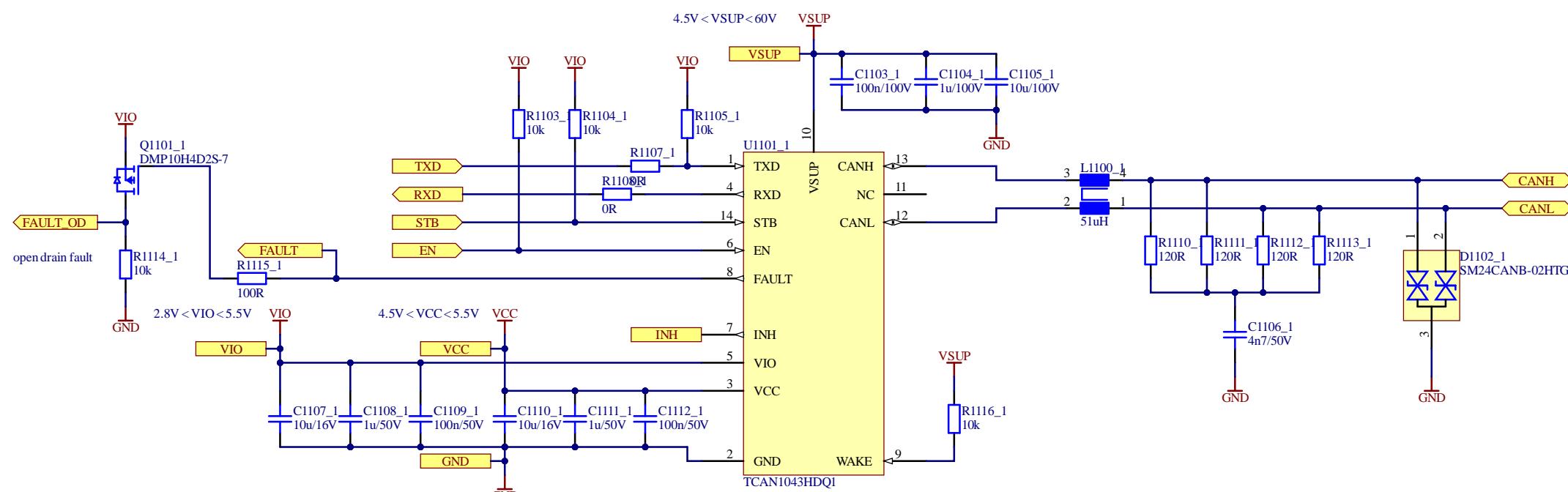
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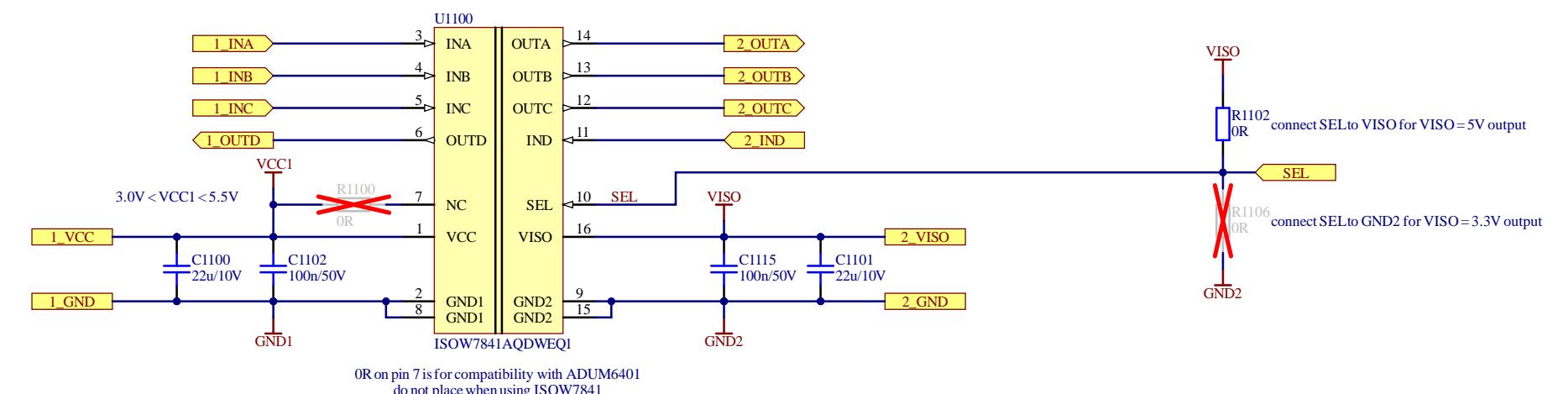
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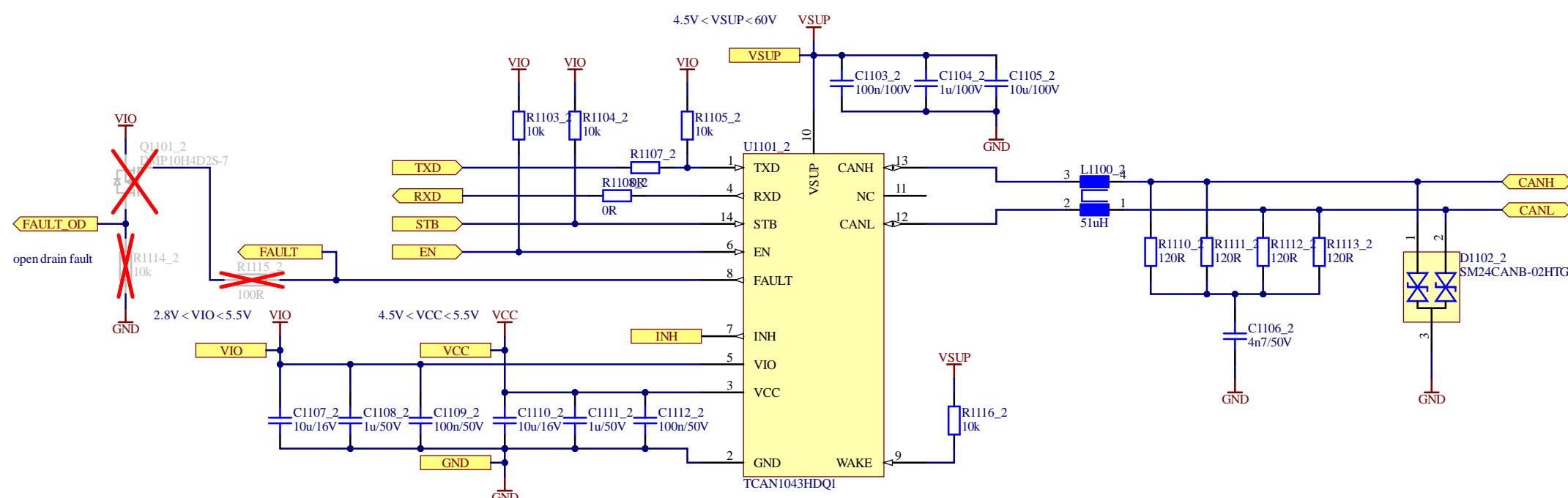
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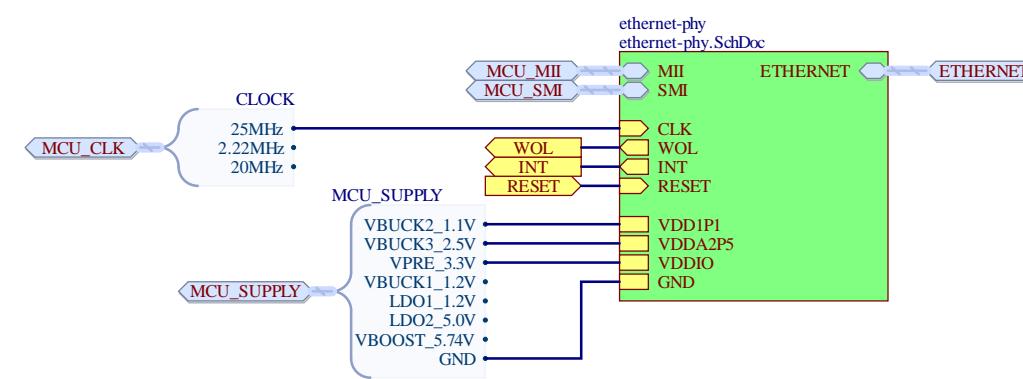
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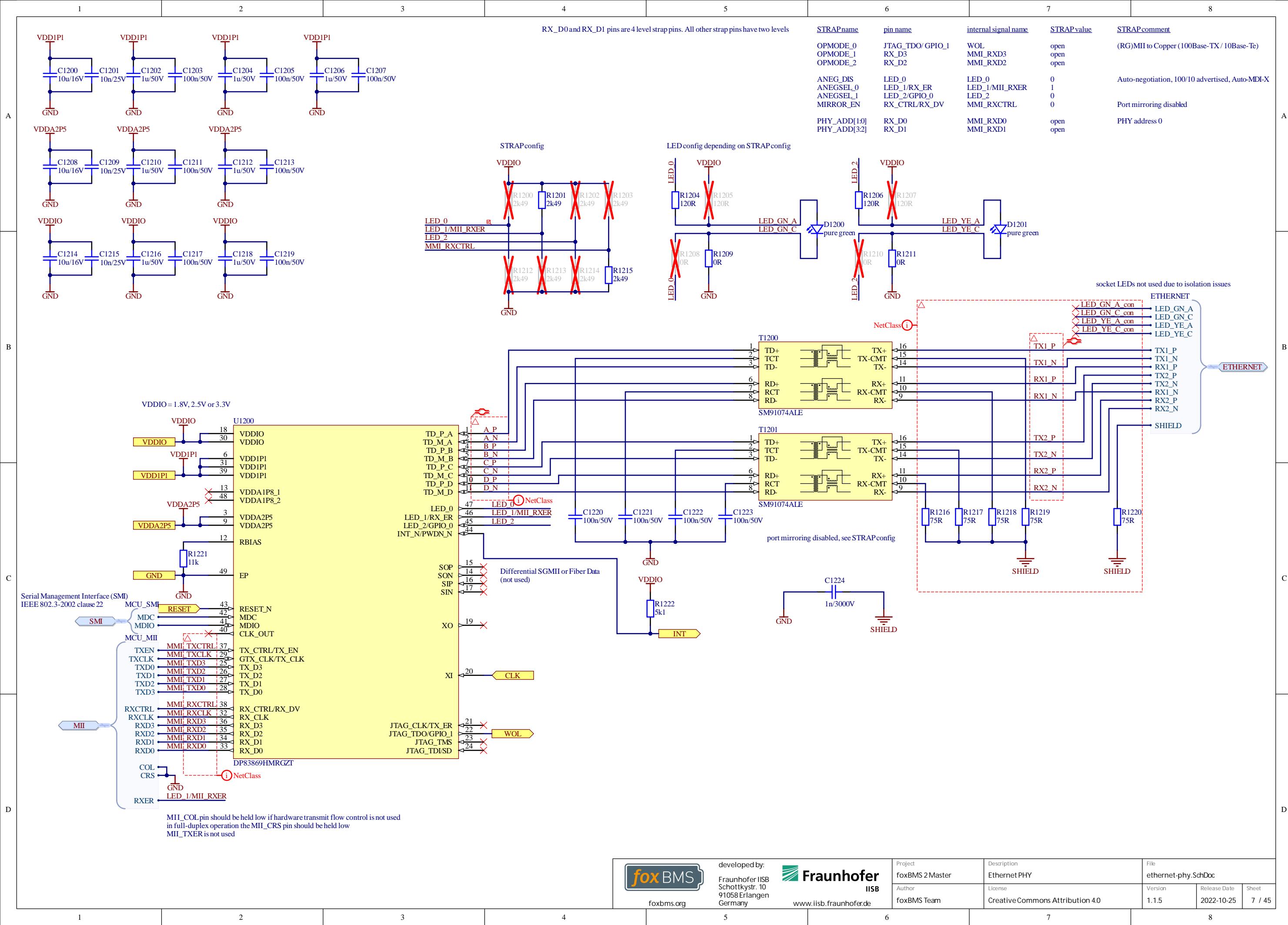
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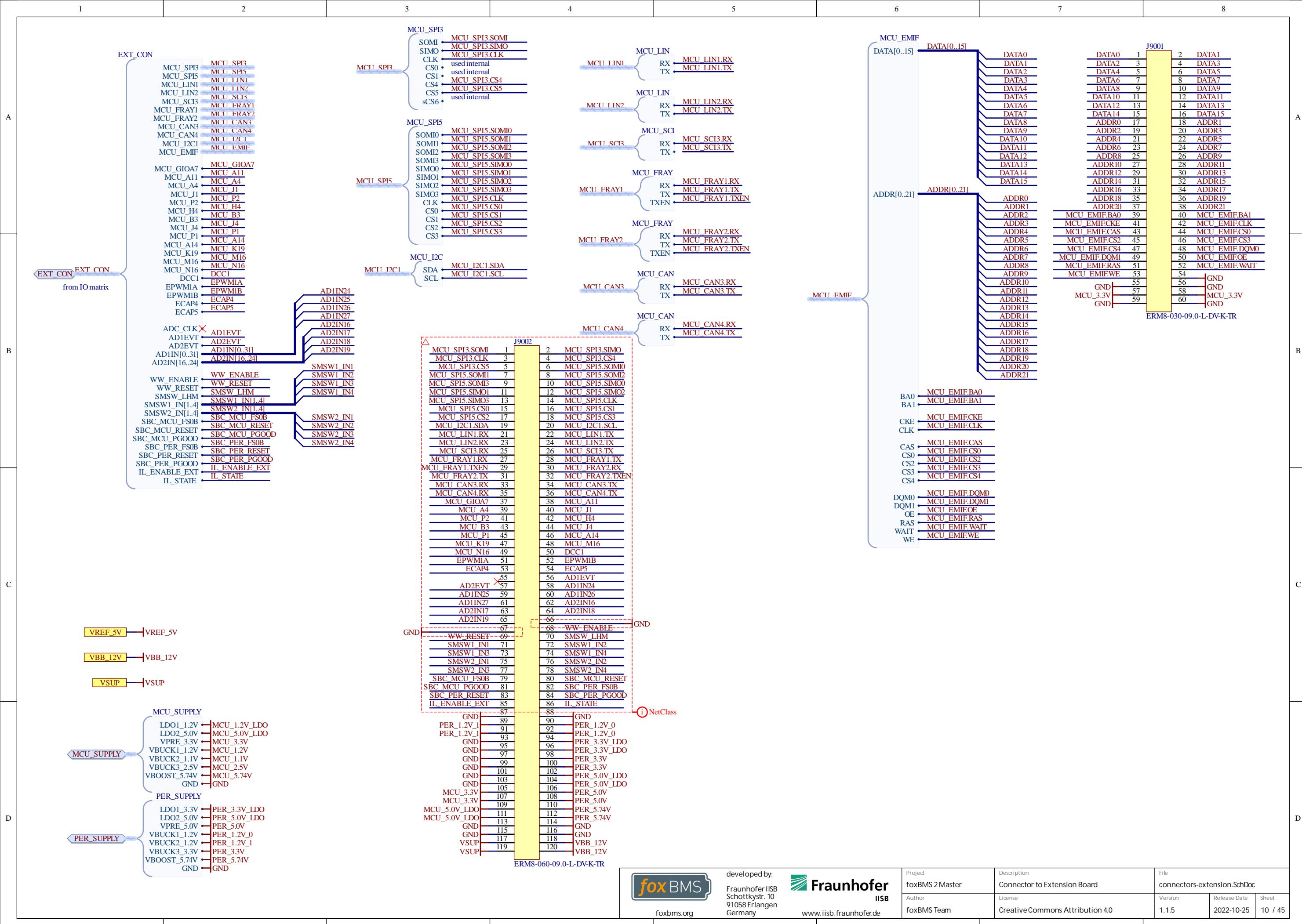
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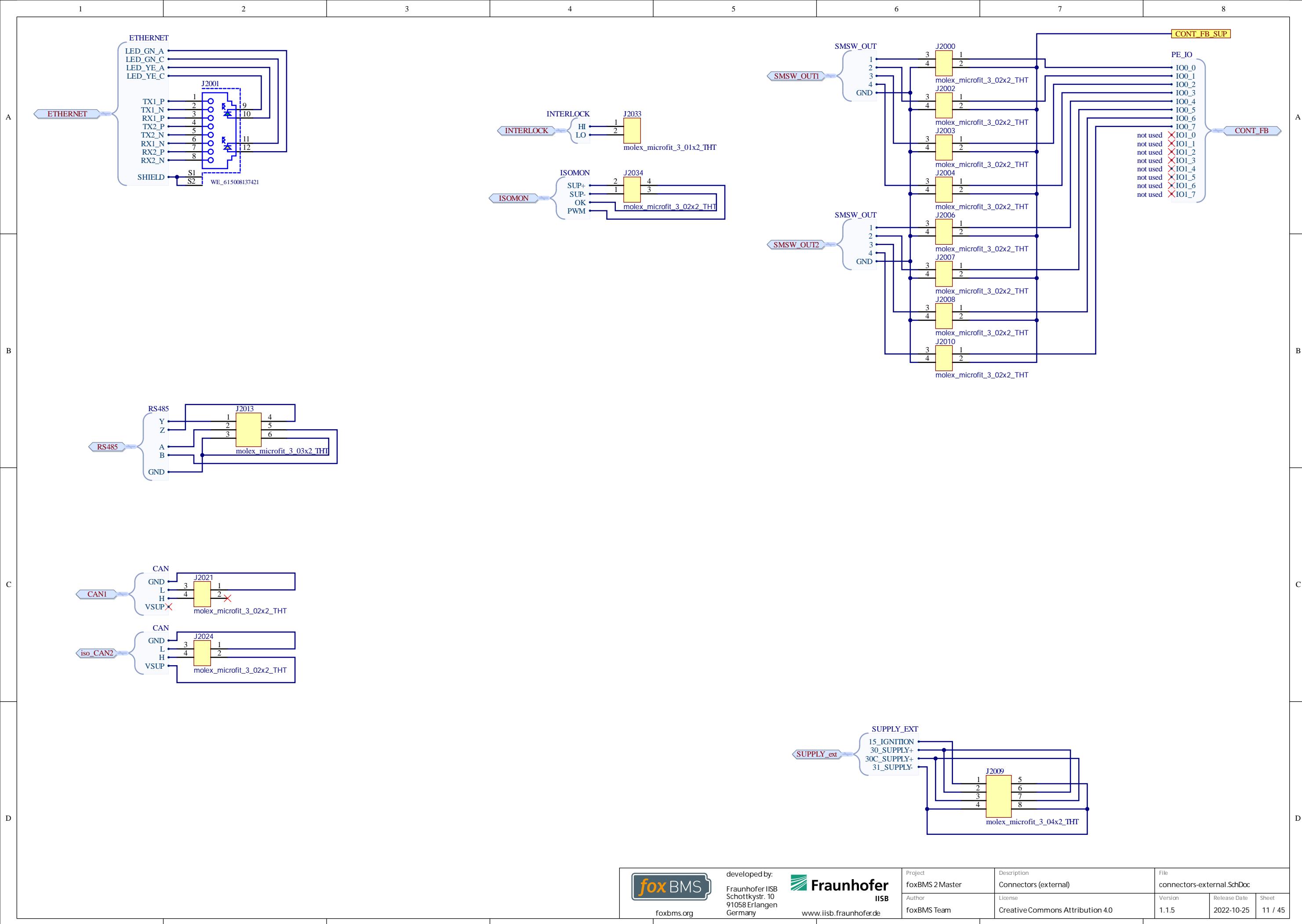
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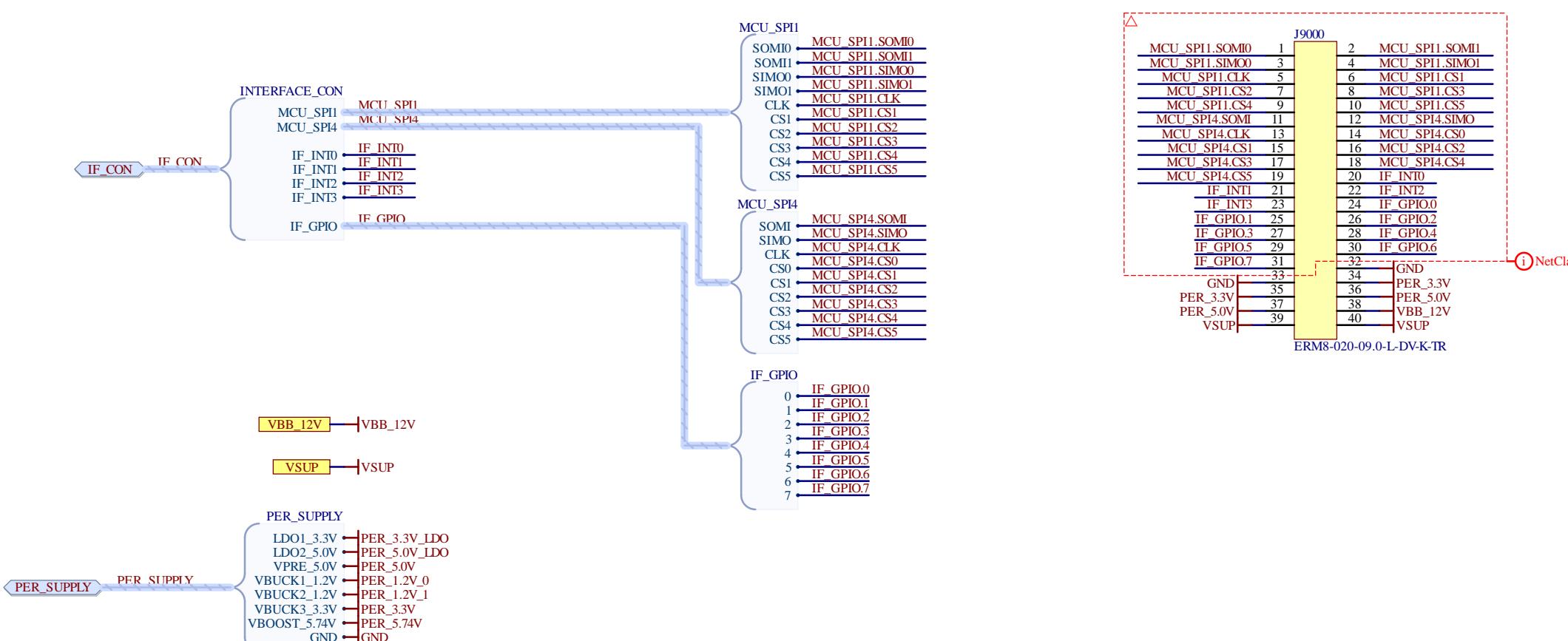
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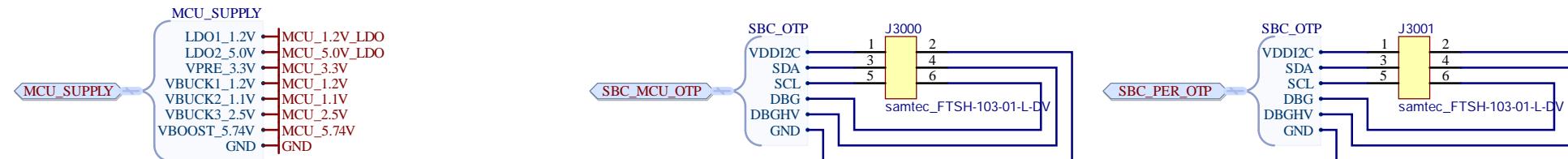
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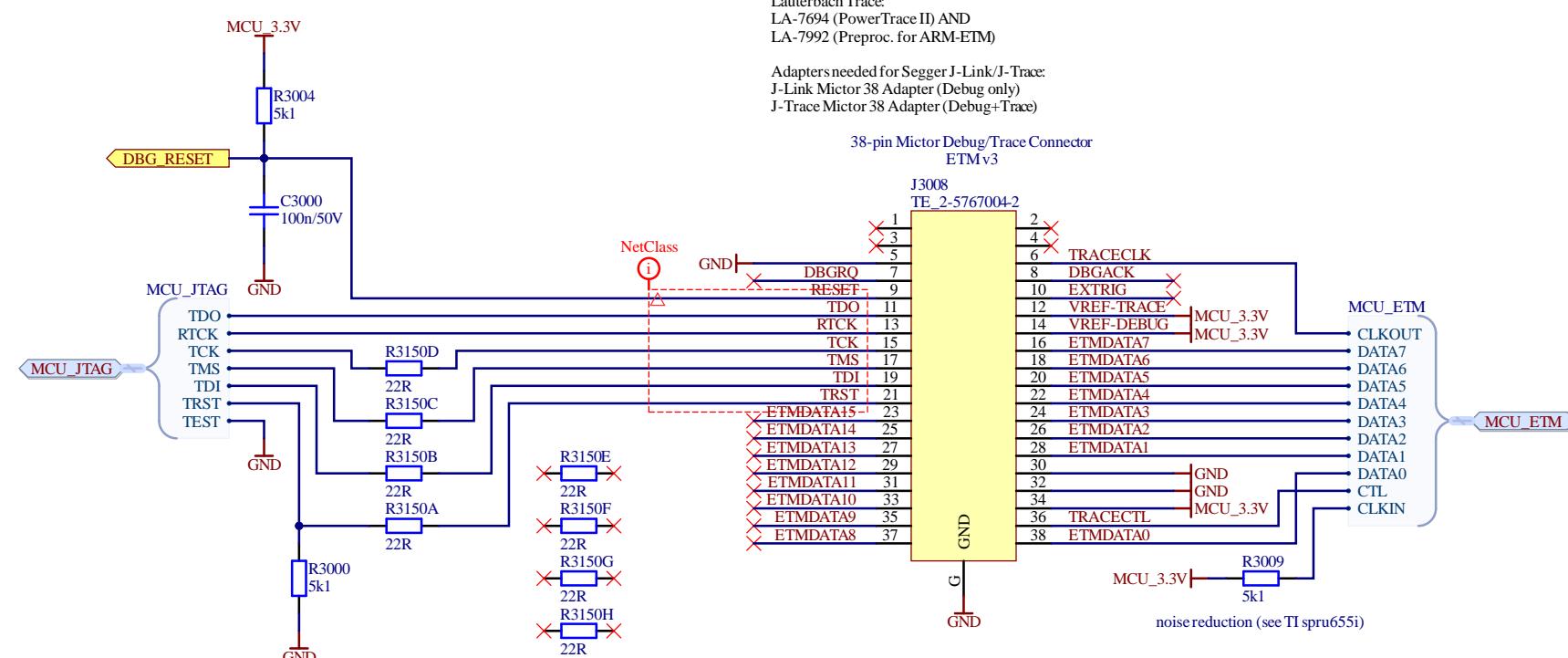


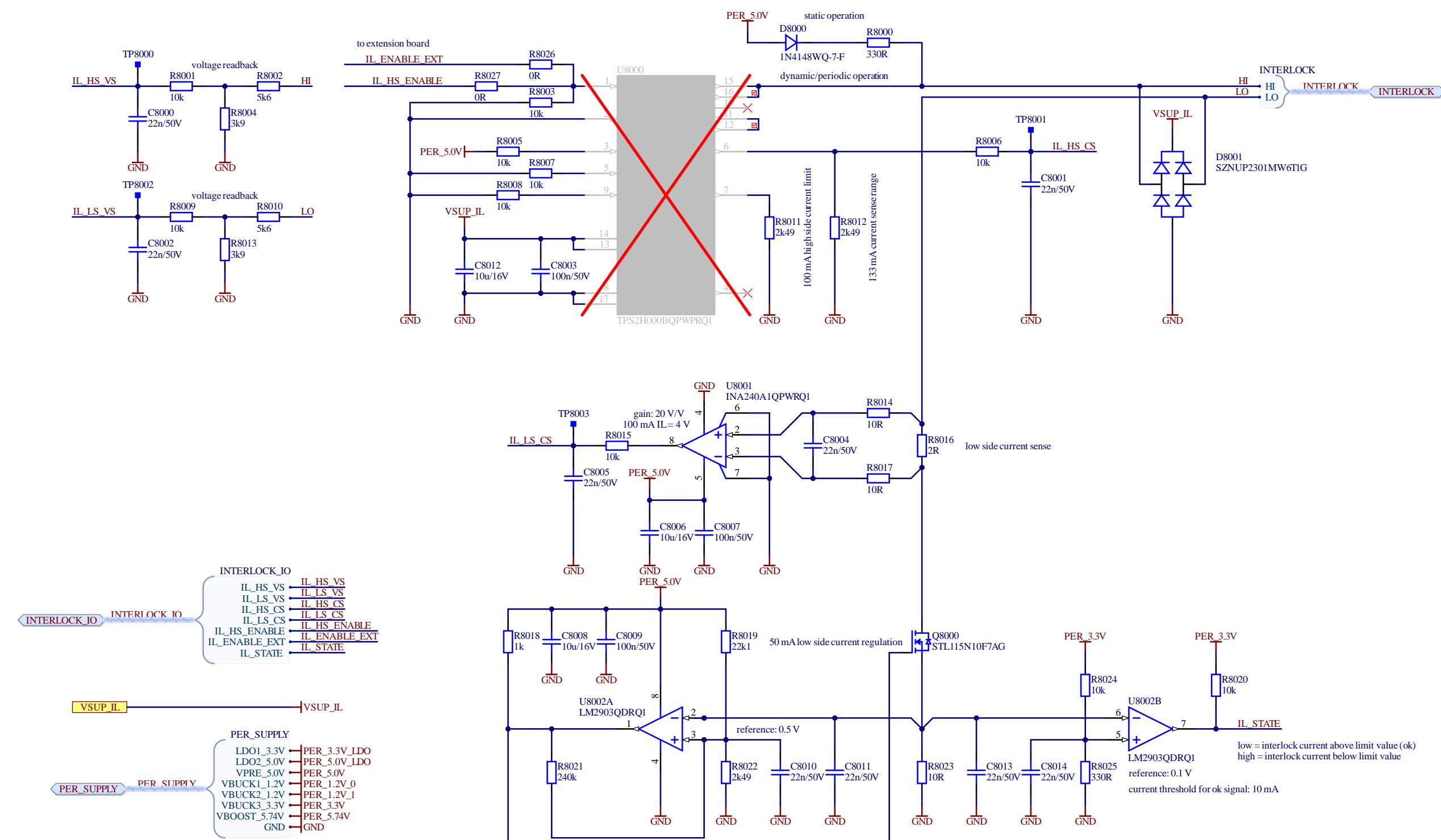


Lauterbach Debug:
LA-3500 (PowerDebug Module USB 3.0) AND
LA-7843 (Debugger for Cortex-A/R) AND
LA-3722 (Adapter ARM-20 to Mictor-38)

Lauterbach Trace:
LA-7694 (PowerTrace II) AND
LA-7992 (Preproc. for ARM-ET)

Adapters needed for Segger J-Link/J-Trace
J-Link Mictor 38 Adapter (Debug only)
J-Trace Mictor 38 Adapter (Debug+Trace)





design based on TI application note TIDA-01445

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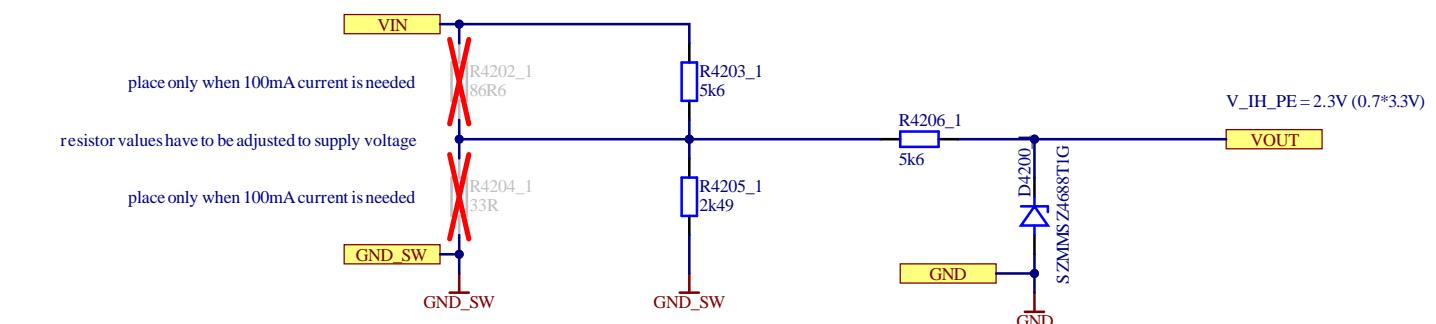
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VIN = 12V from Buck-Boost Converter switched via contactor auxiliary contacts
 VIN = VSUP when Buck-Boost Converter is bypassed



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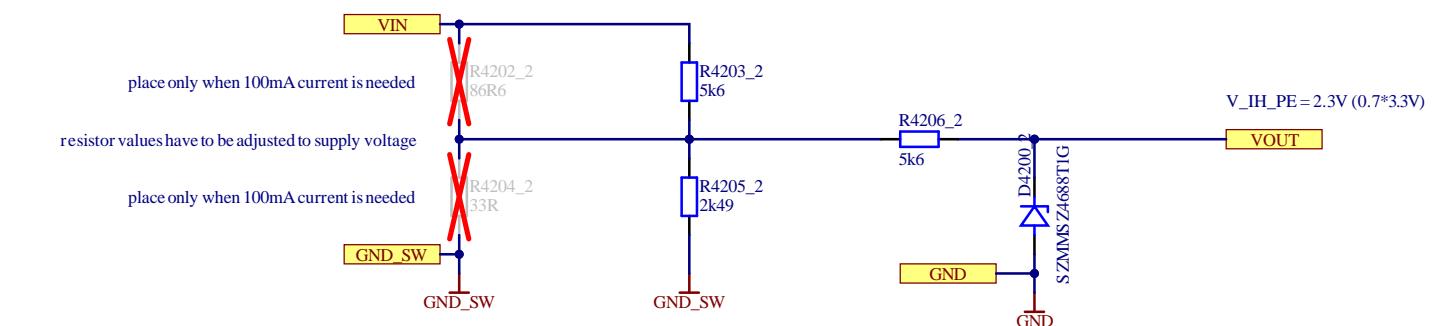
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VIN = 12V from Buck-Boost Converter switched via contactor auxiliary contacts
 VIN = VSUP when Buck-Boost Converter is bypassed



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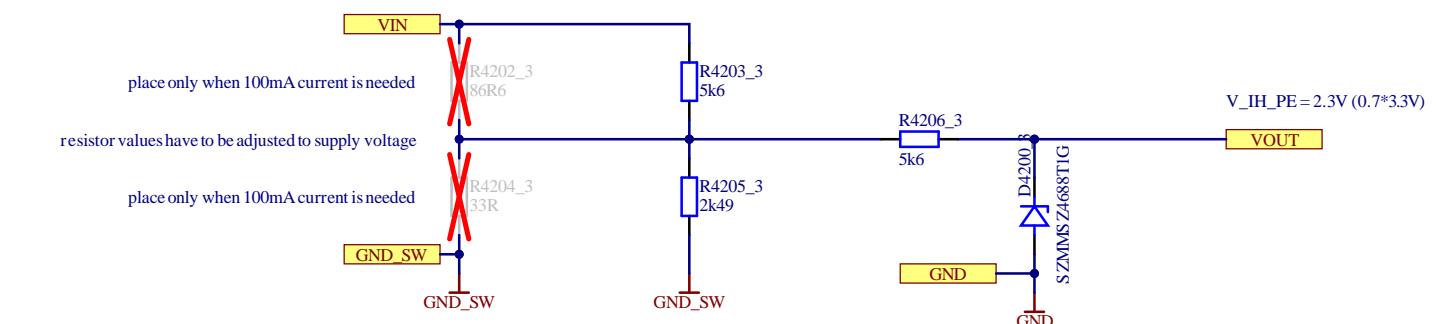
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VIN = 12V from Buck-Boost Converter switched via contactor auxiliary contacts
 VIN = VSUP when Buck-Boost Converter is bypassed



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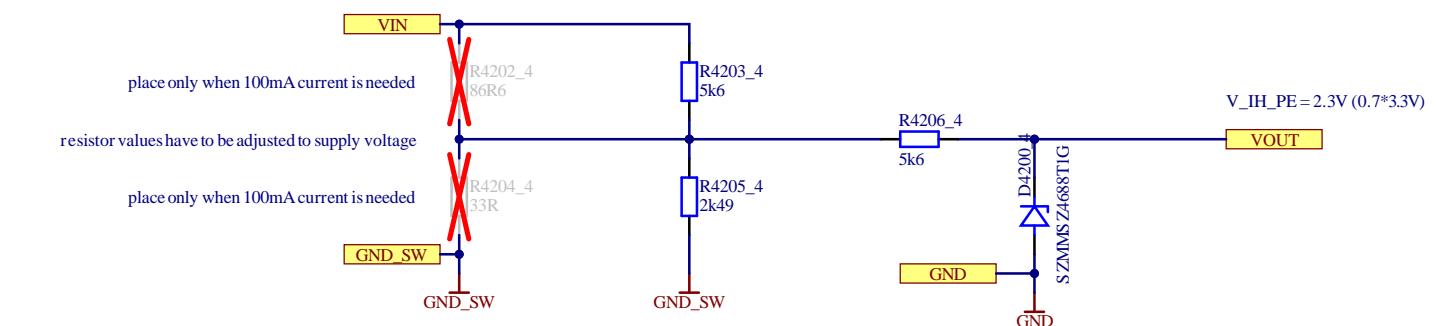
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VIN = 12V from Buck-Boost Converter switched via contactor auxiliary contacts
 VIN = VSUP when Buck-Boost Converter is bypassed



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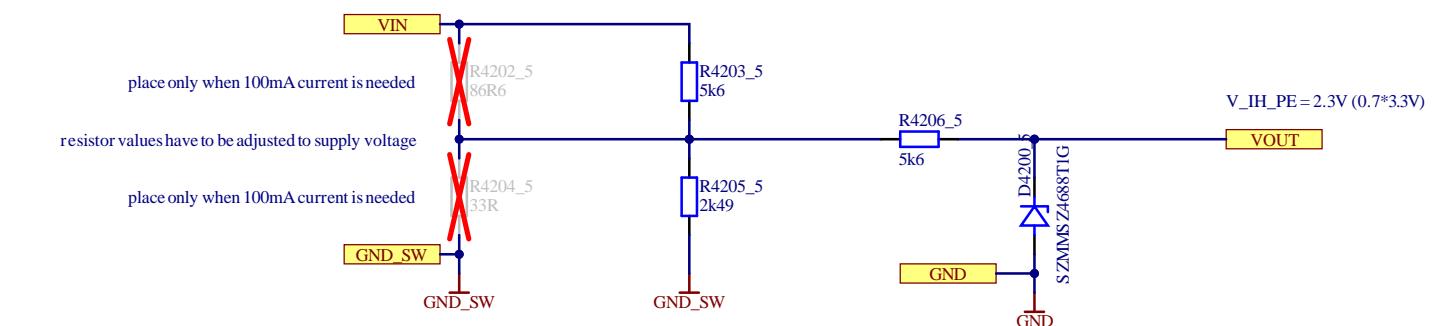
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VIN = 12V from Buck-Boost Converter switched via contactor auxiliary contacts
 VIN = VSUP when Buck-Boost Converter is bypassed



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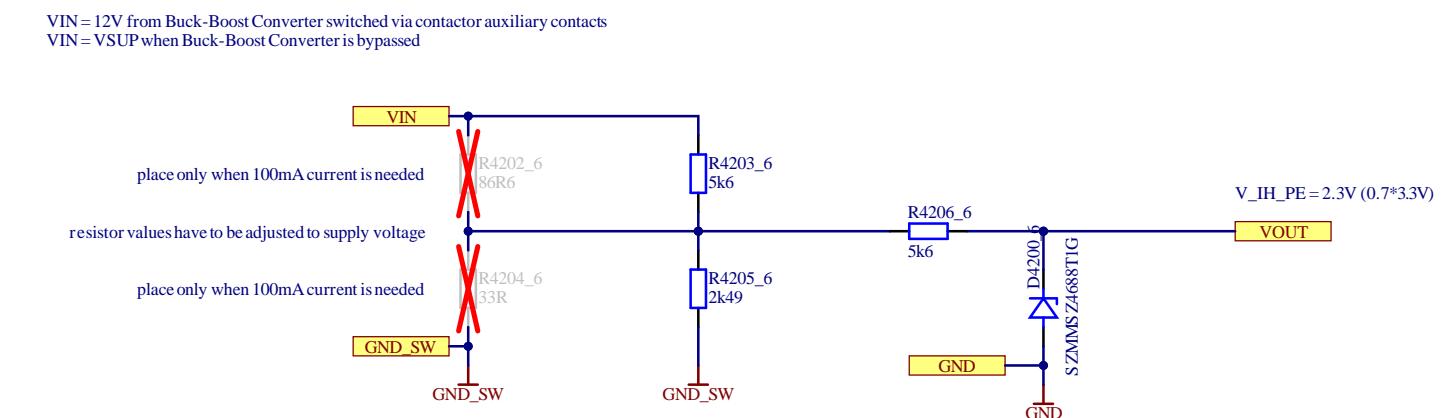
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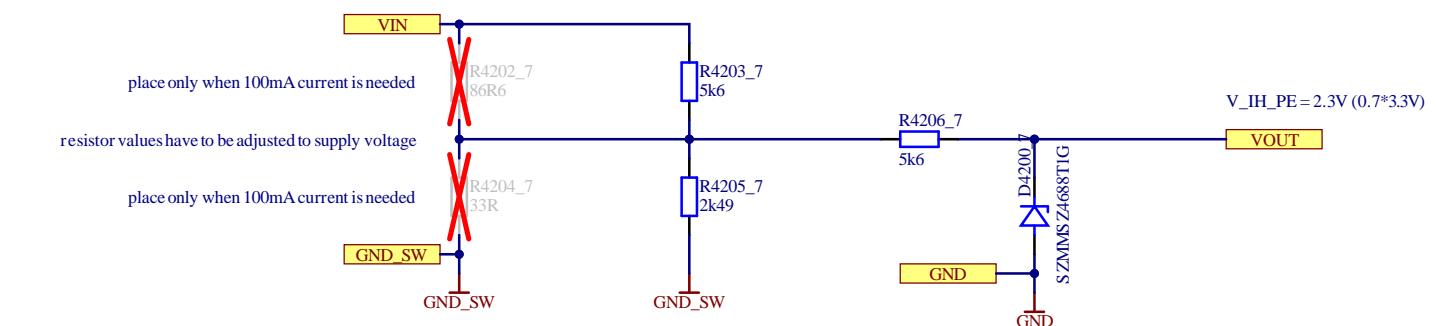
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VIN = 12V from Buck-Boost Converter switched via contactor auxiliary contacts
 VIN = VSUP when Buck-Boost Converter is bypassed



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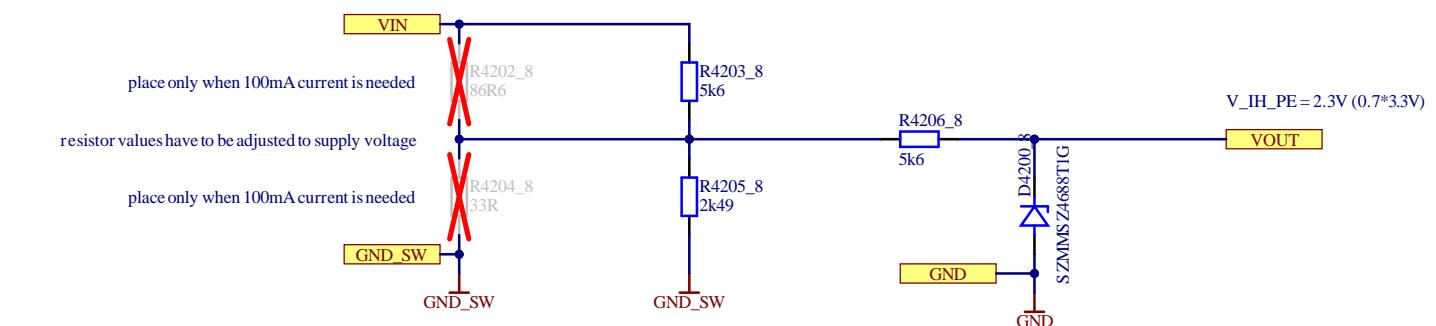
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VIN = 12V from Buck-Boost Converter switched via contactor auxiliary contacts
 VIN = VSUP when Buck-Boost Converter is bypassed



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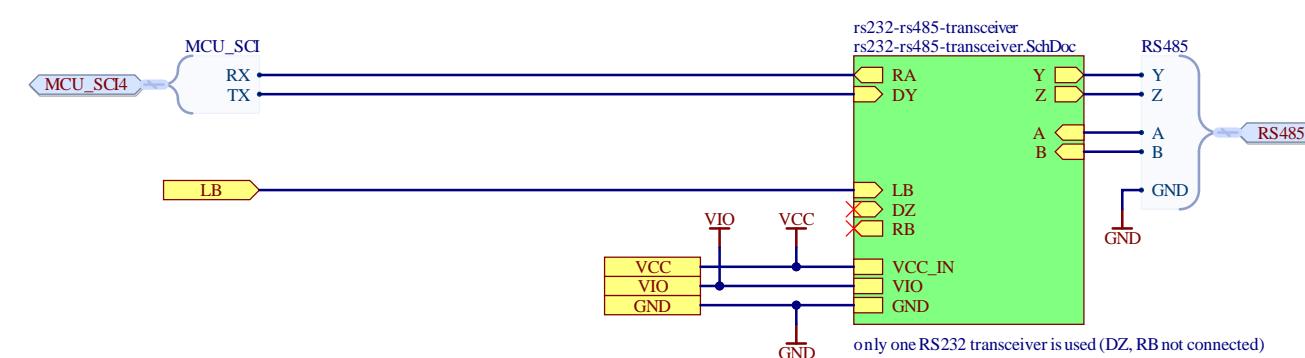
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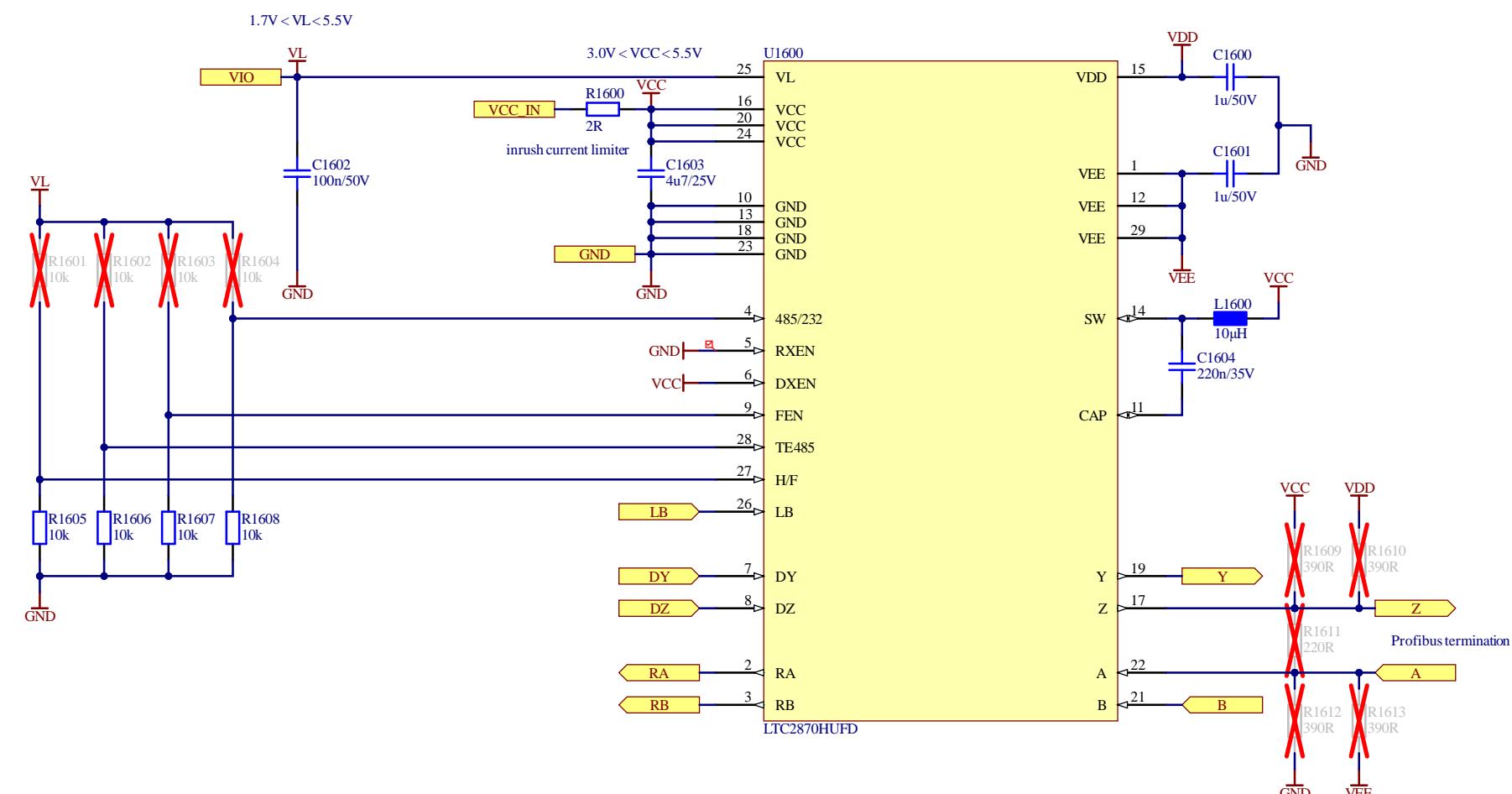
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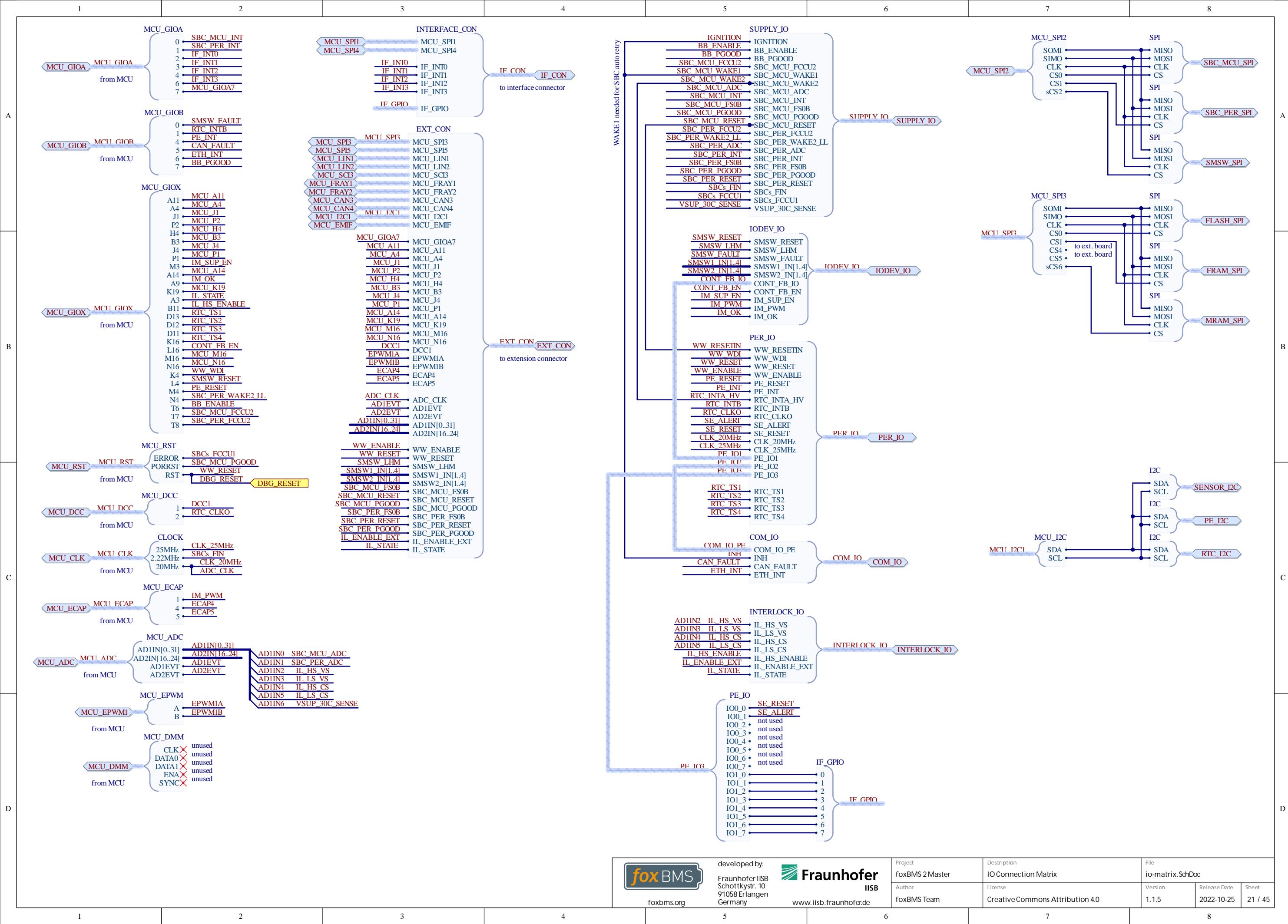
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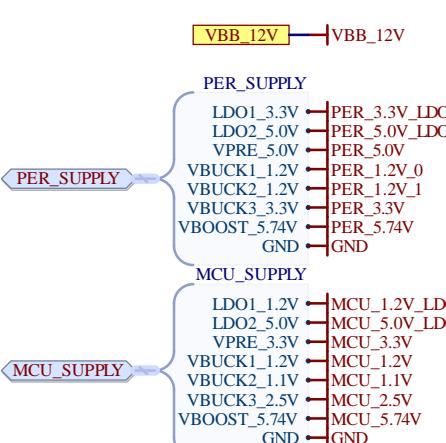
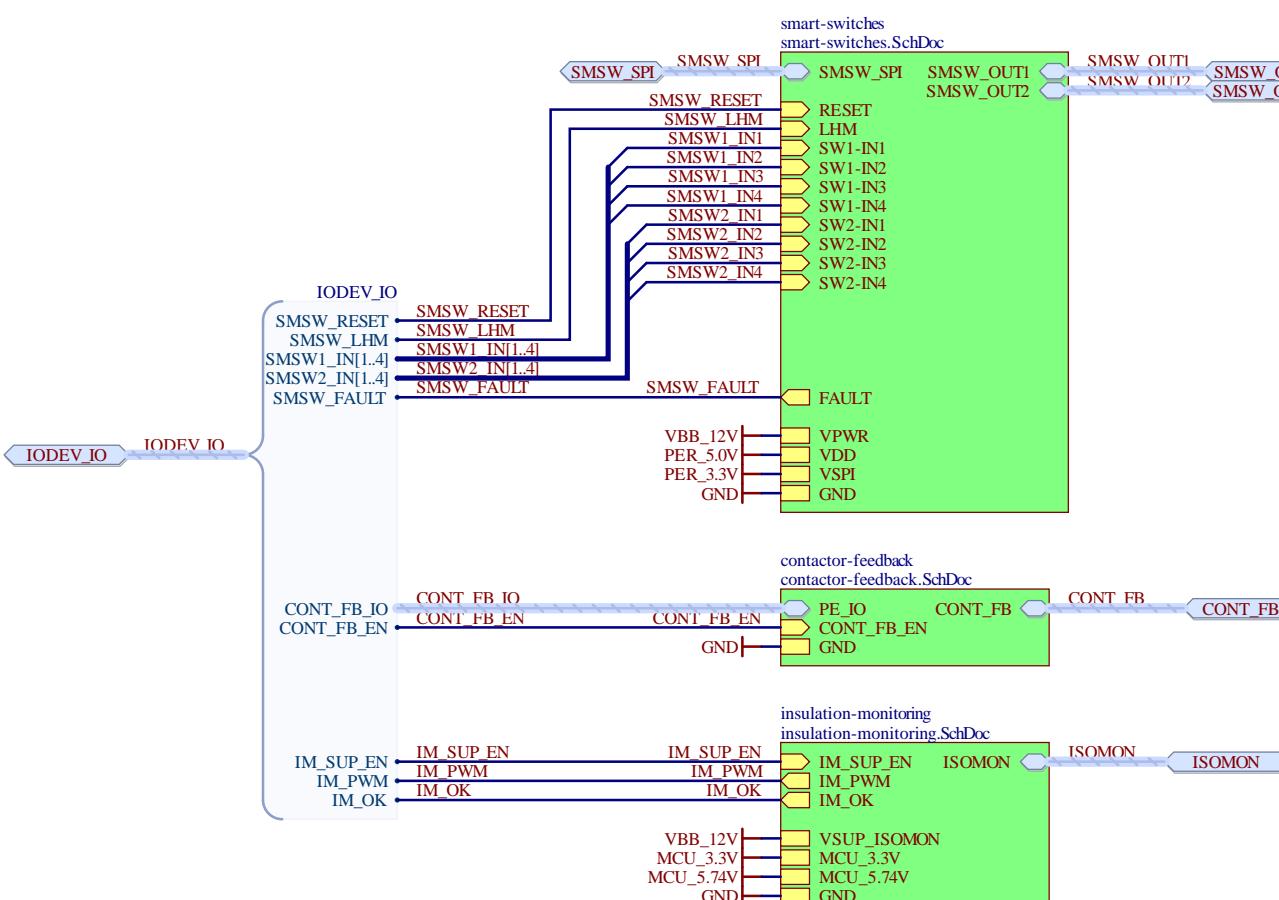
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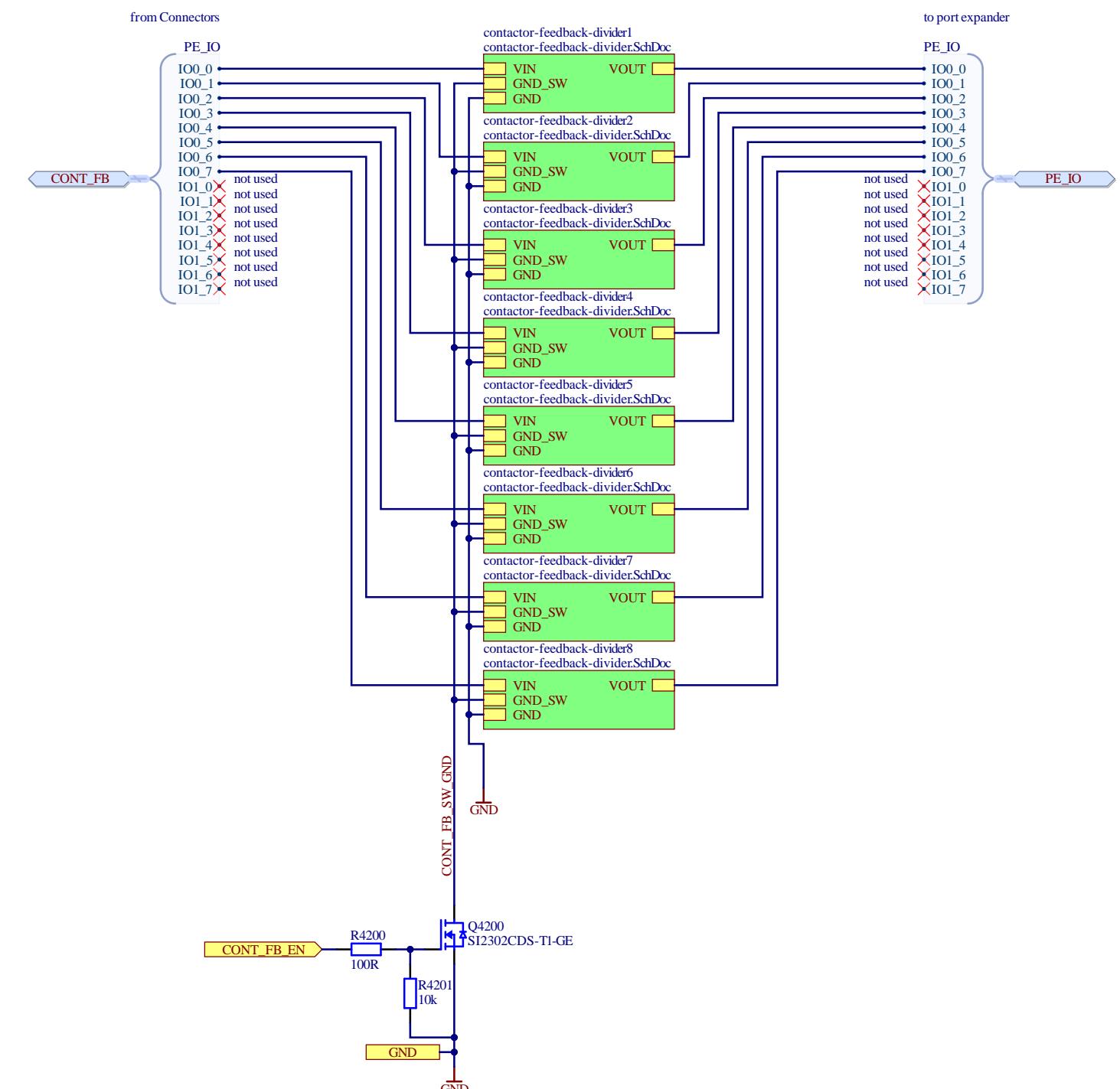
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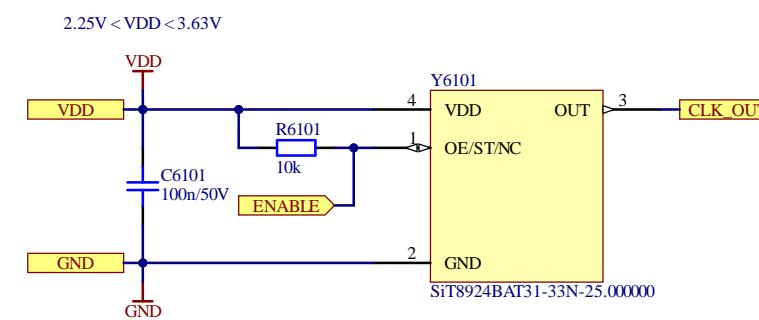


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Pin 1 can be programmed as standby, output enable or not connected.

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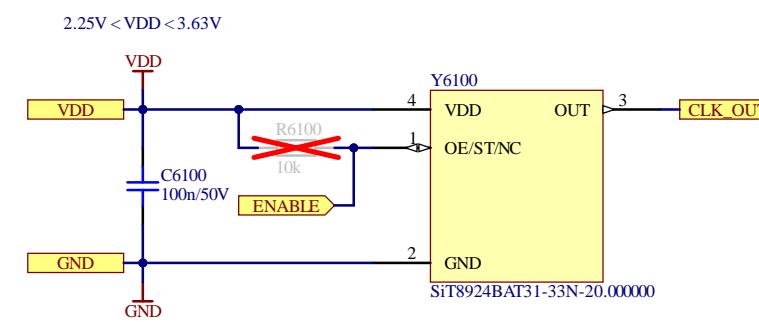
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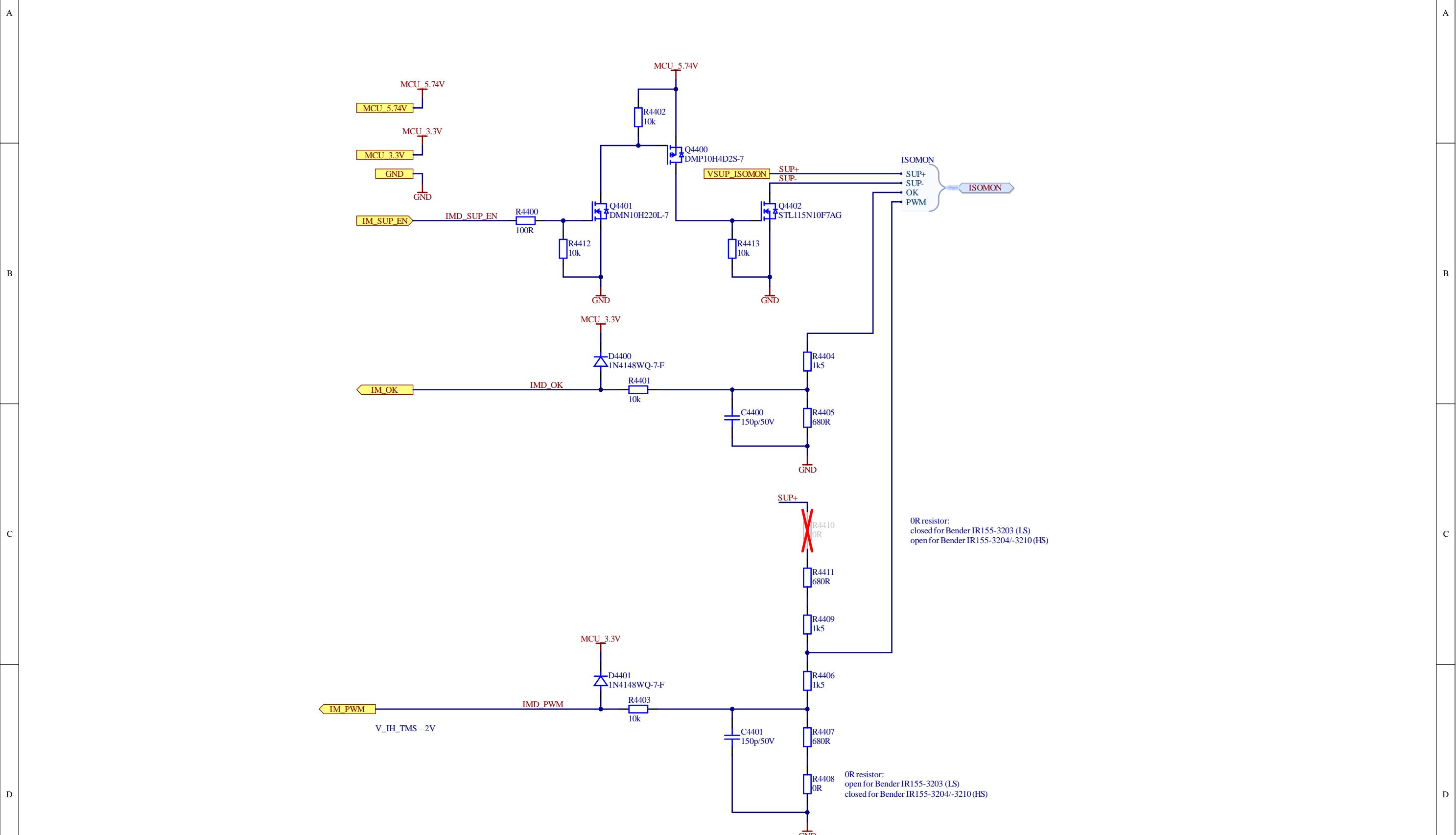


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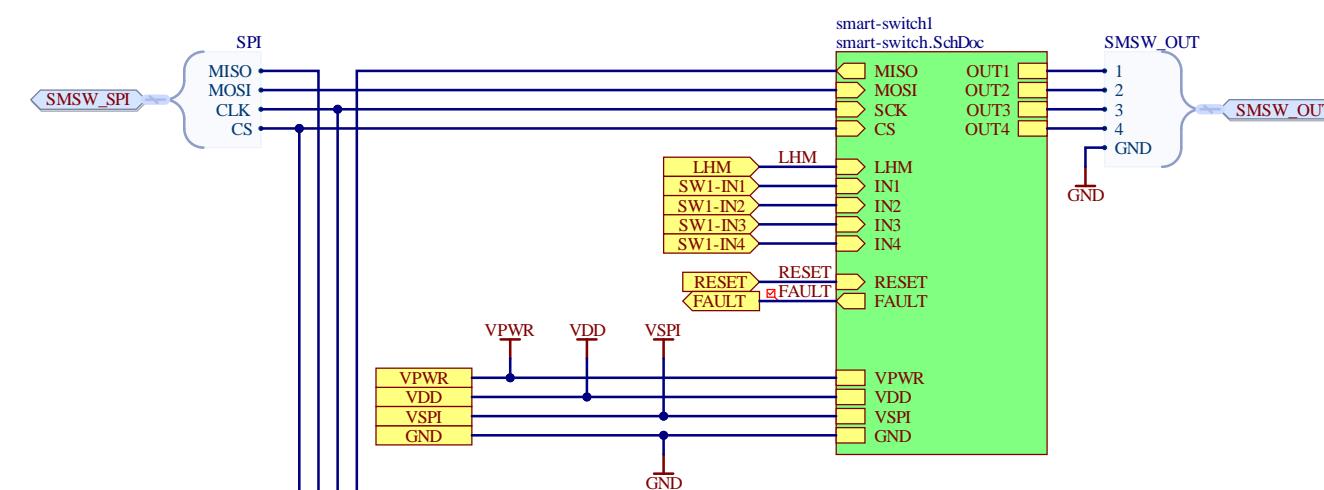
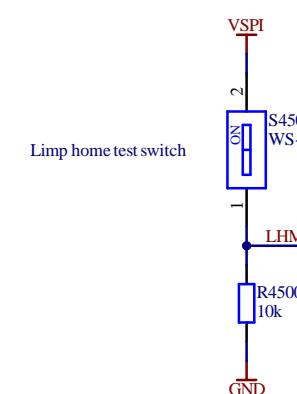
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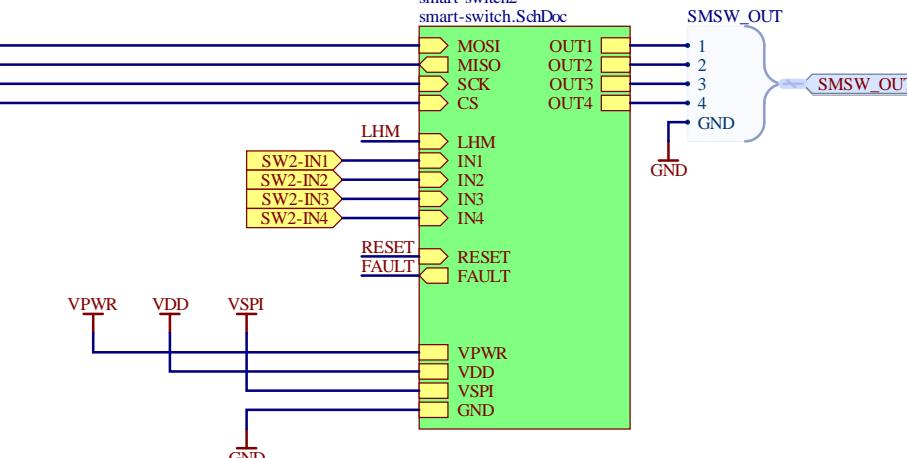
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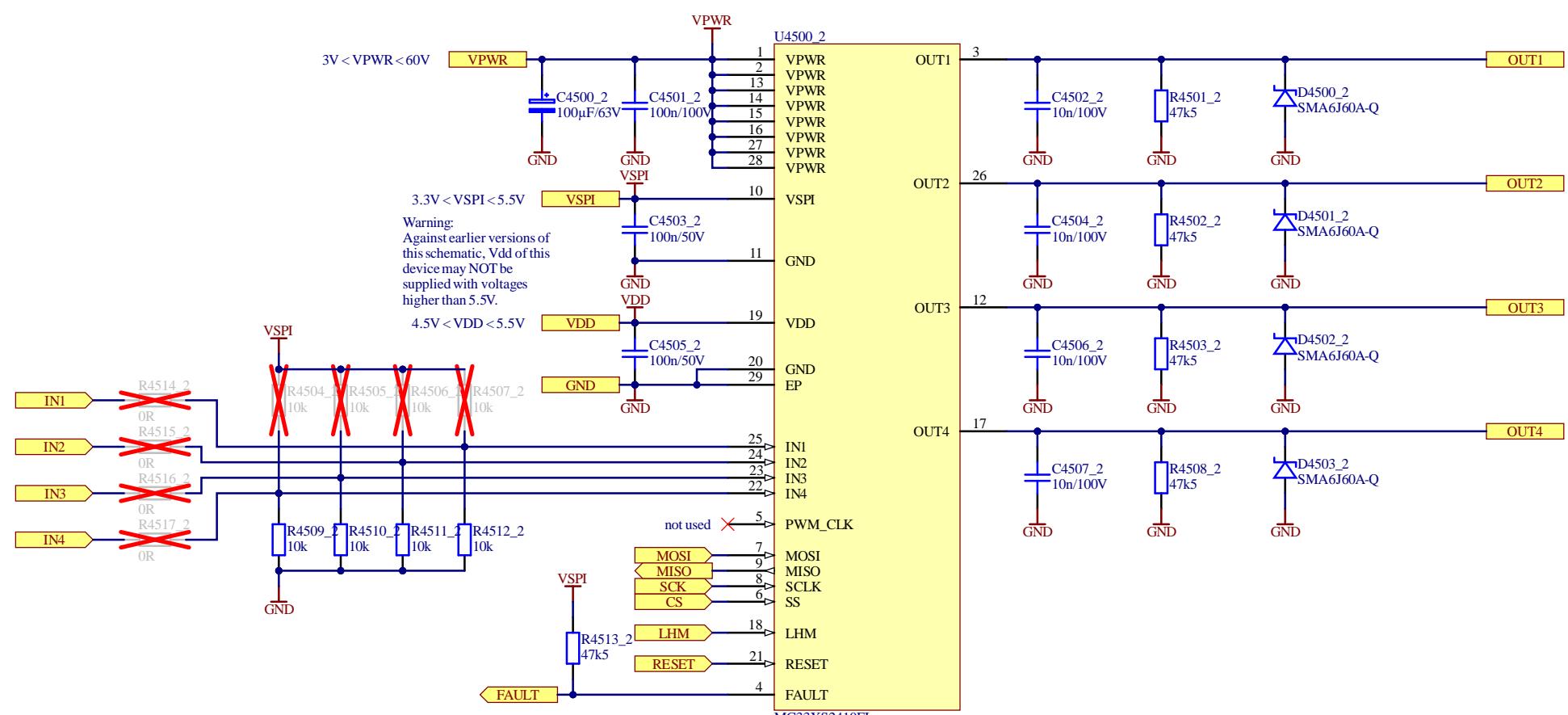
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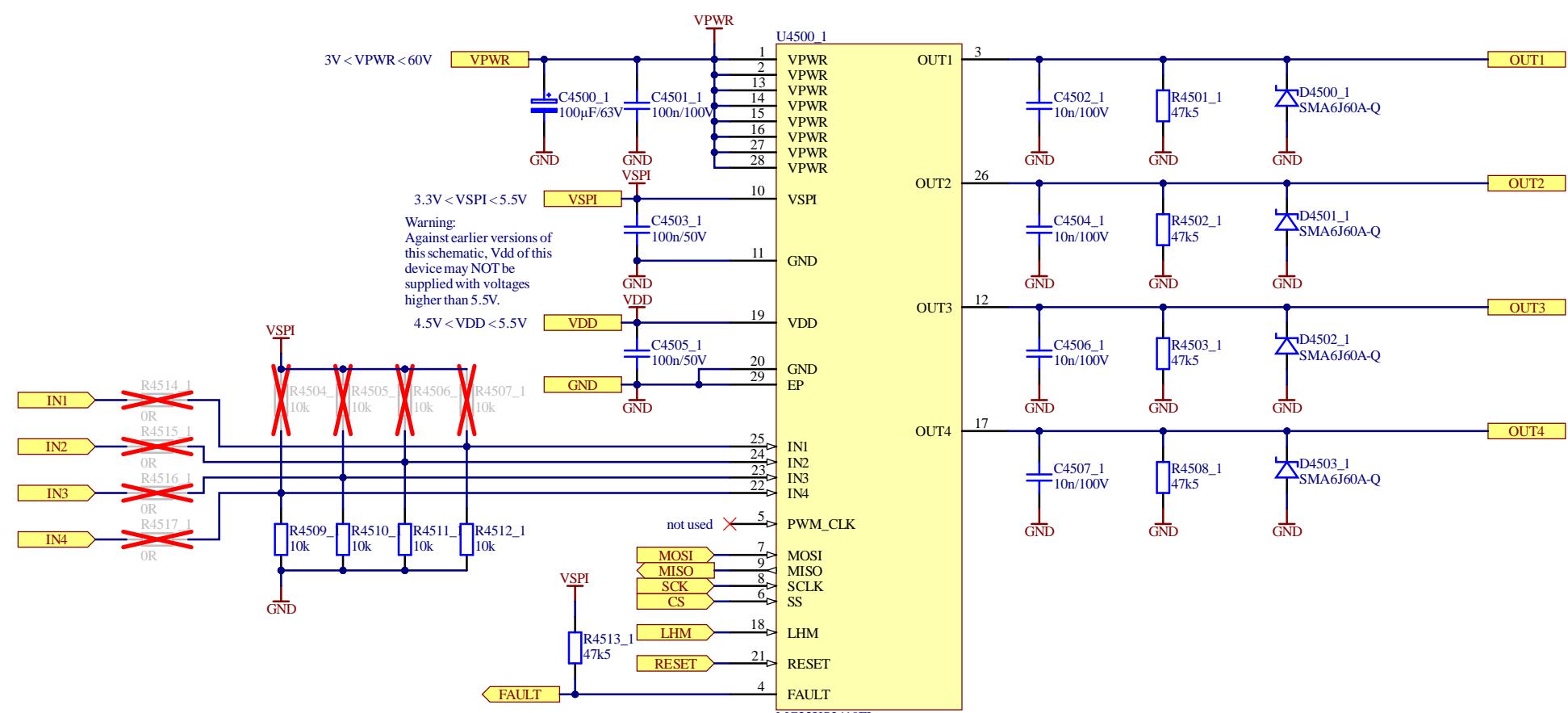
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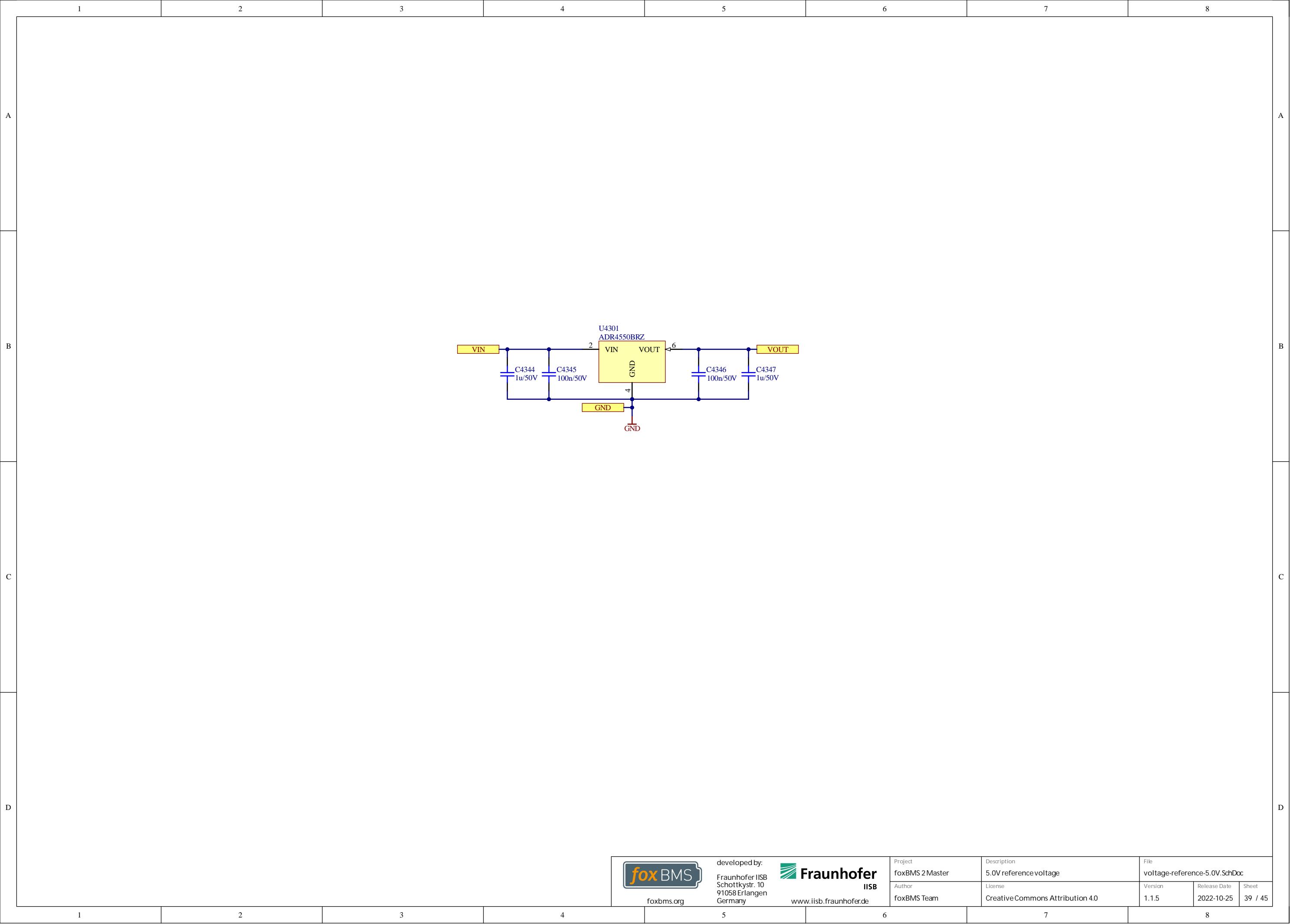
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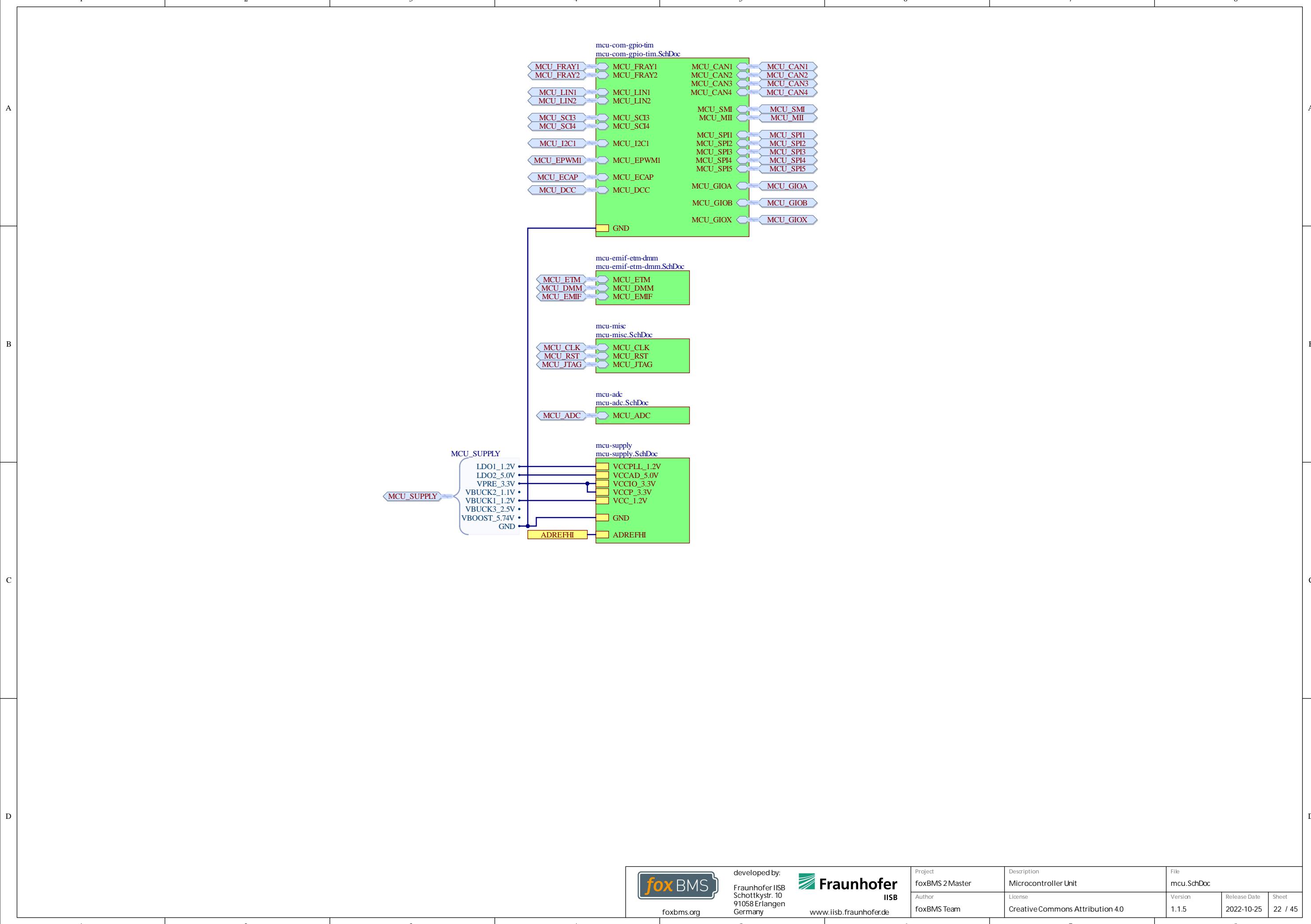
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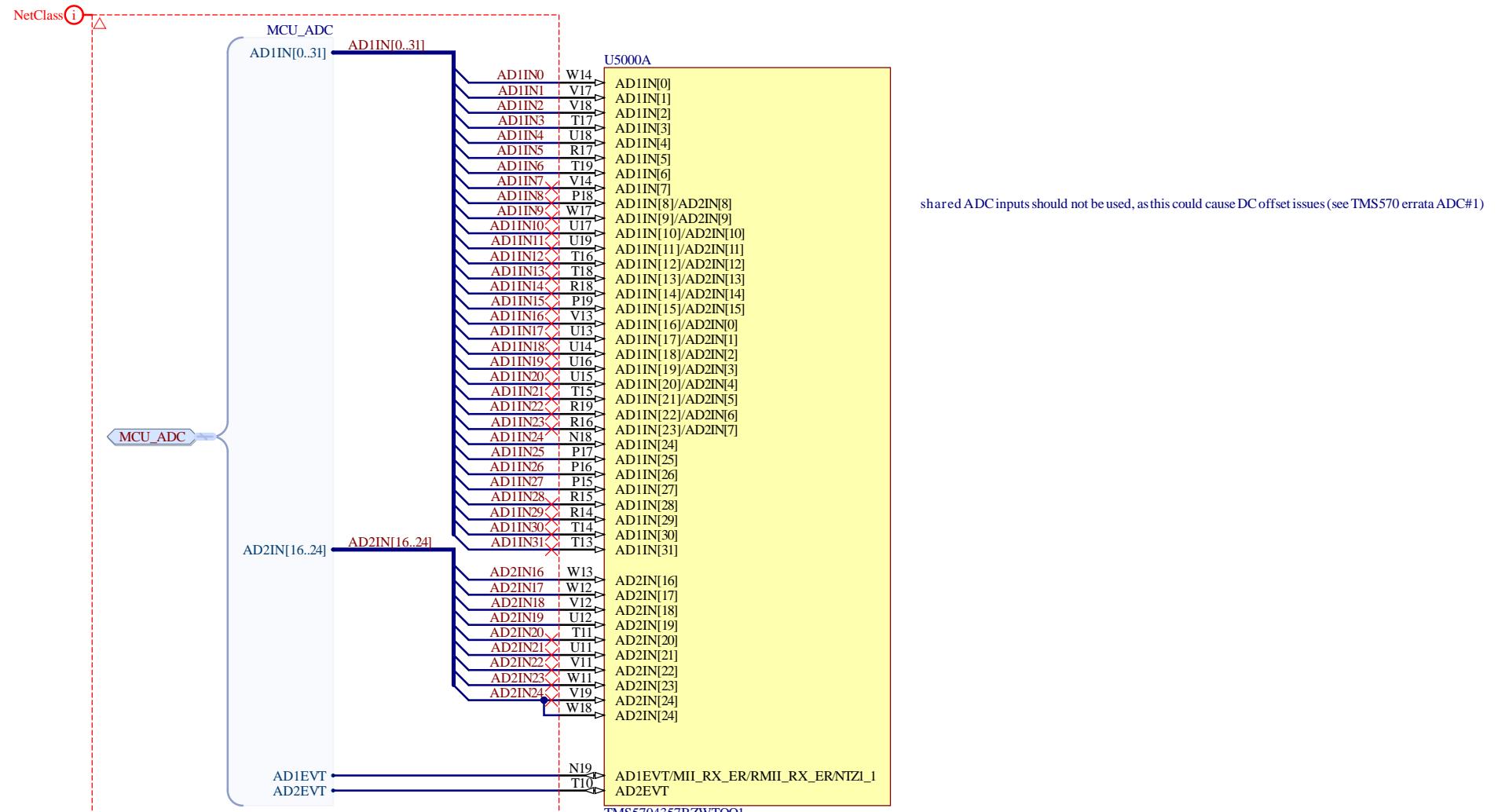






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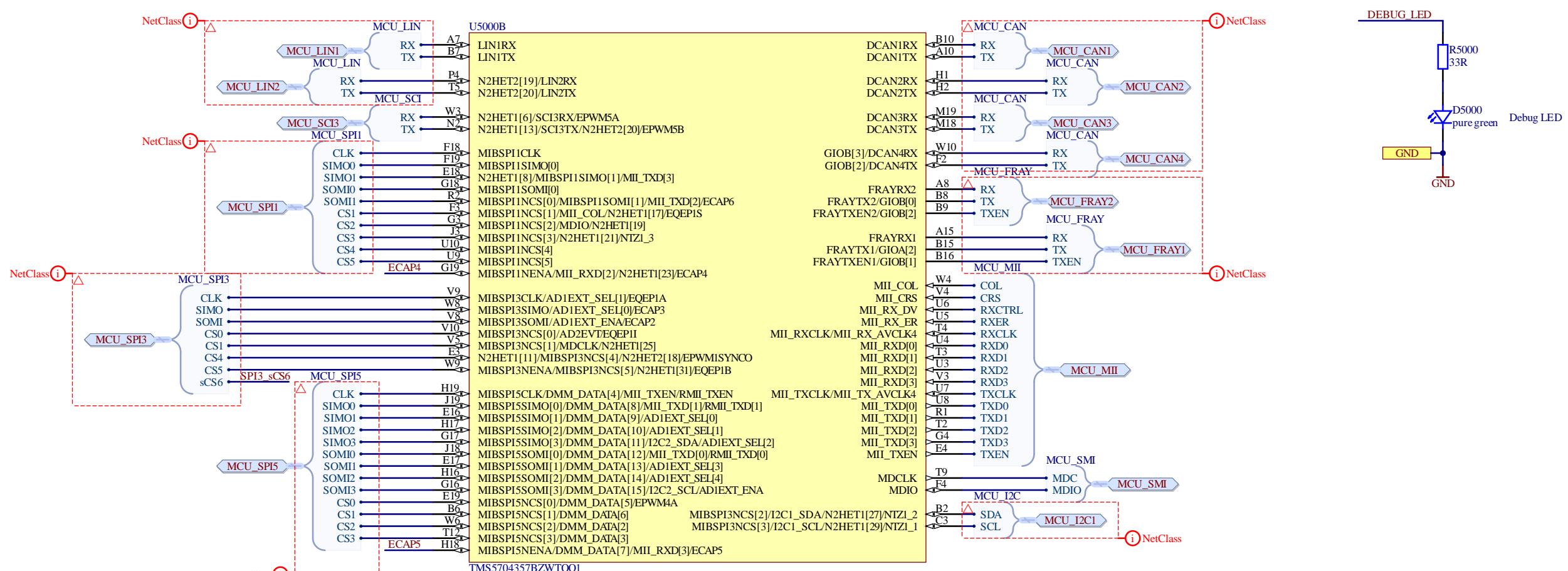
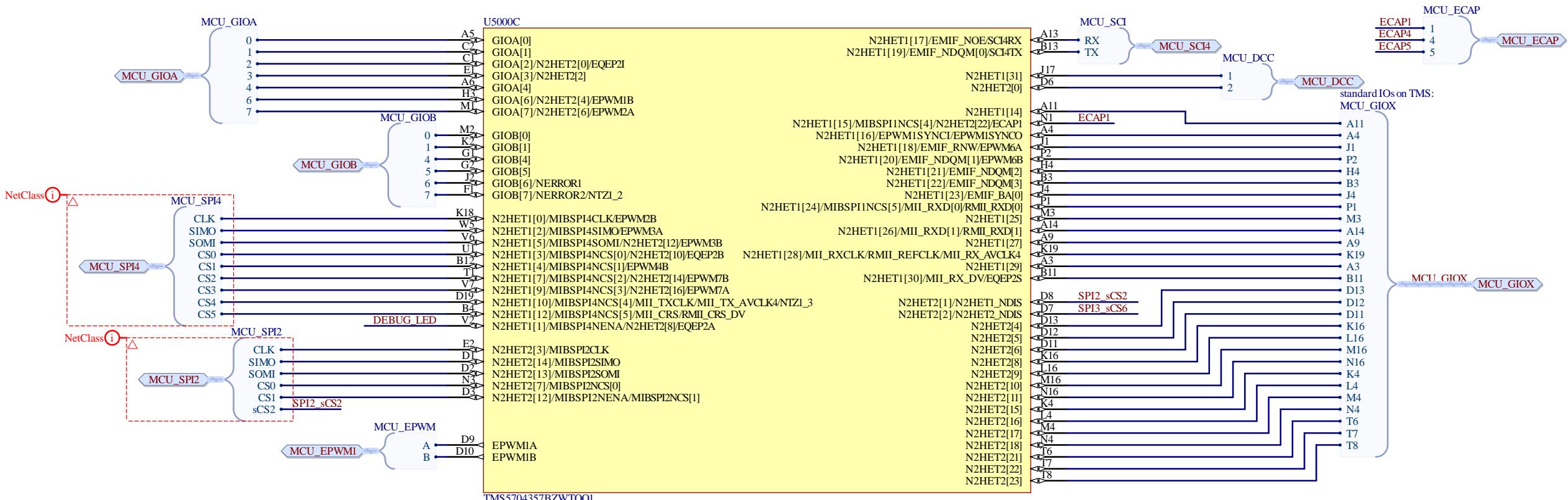
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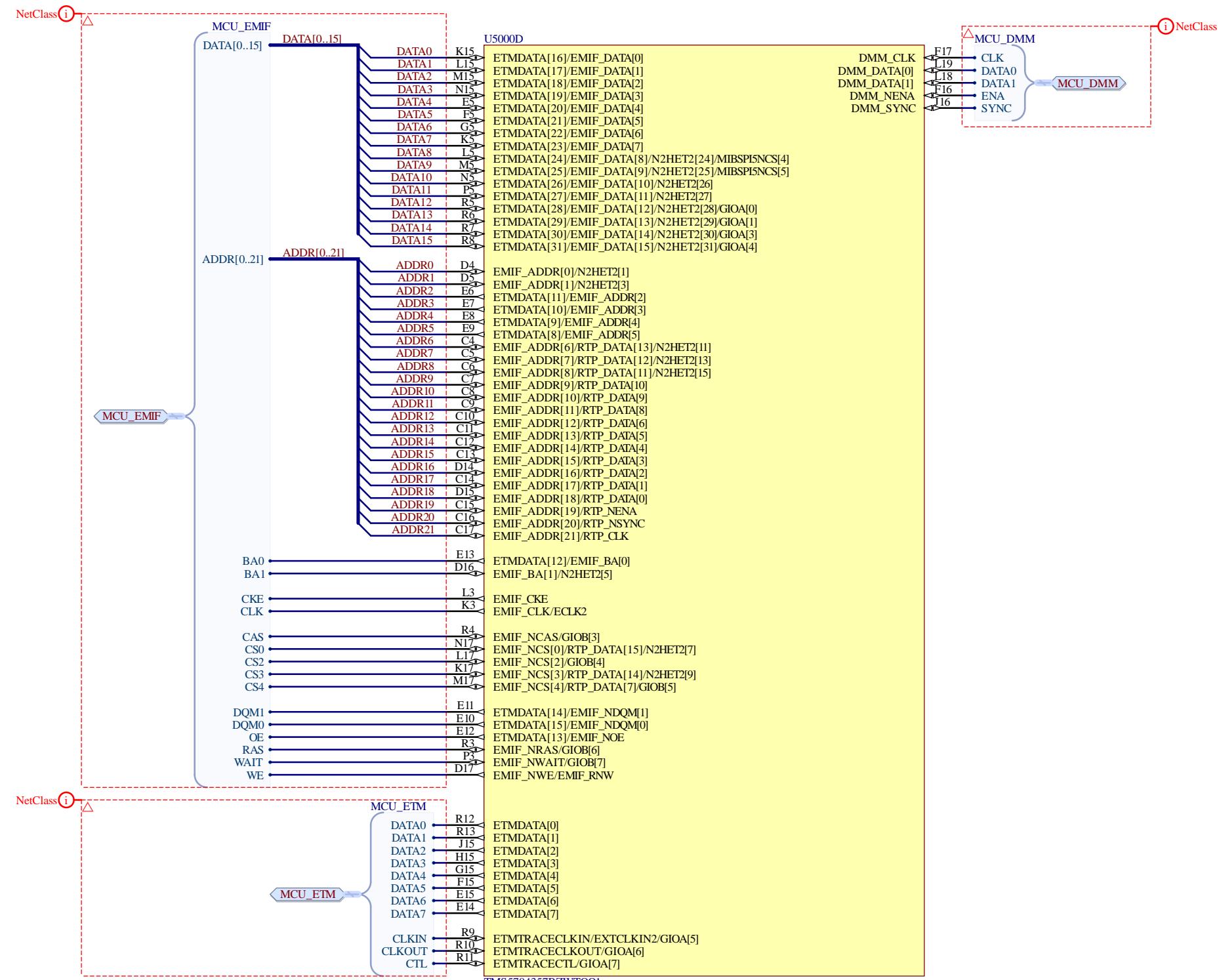
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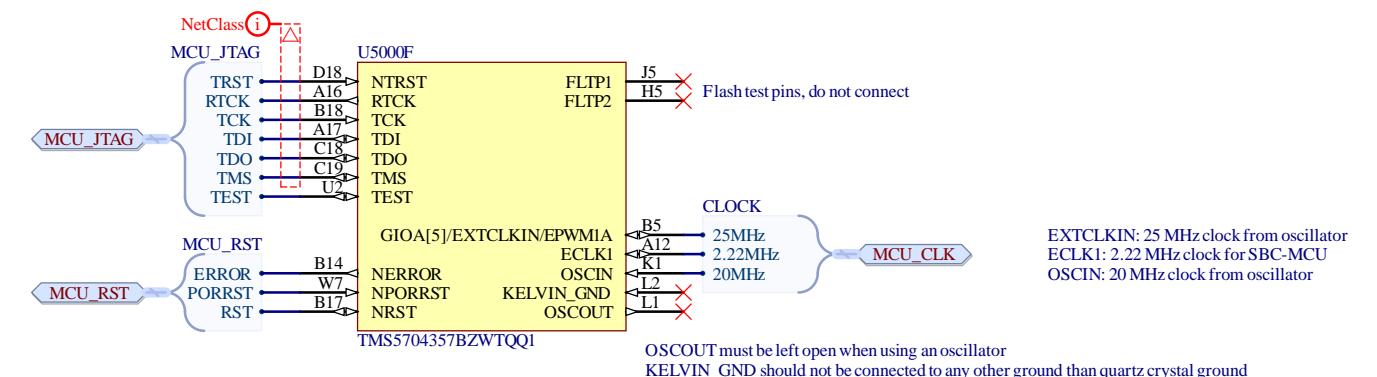


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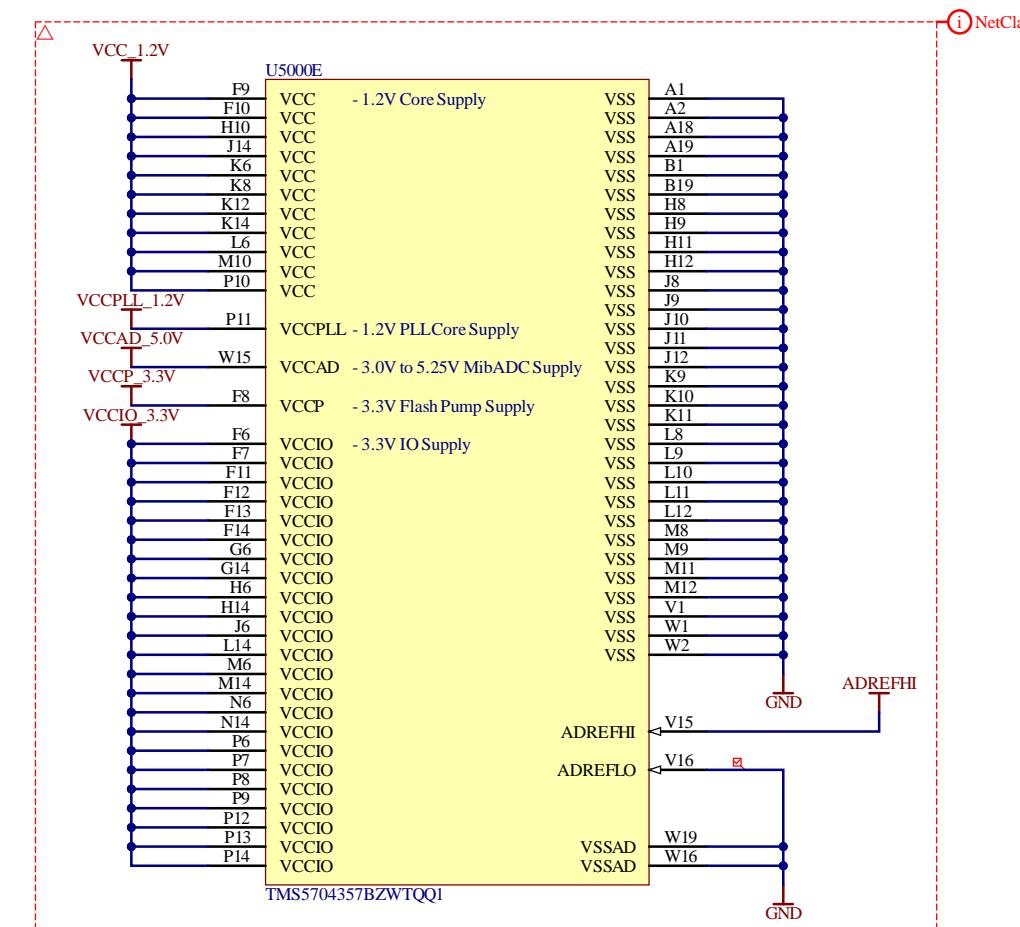
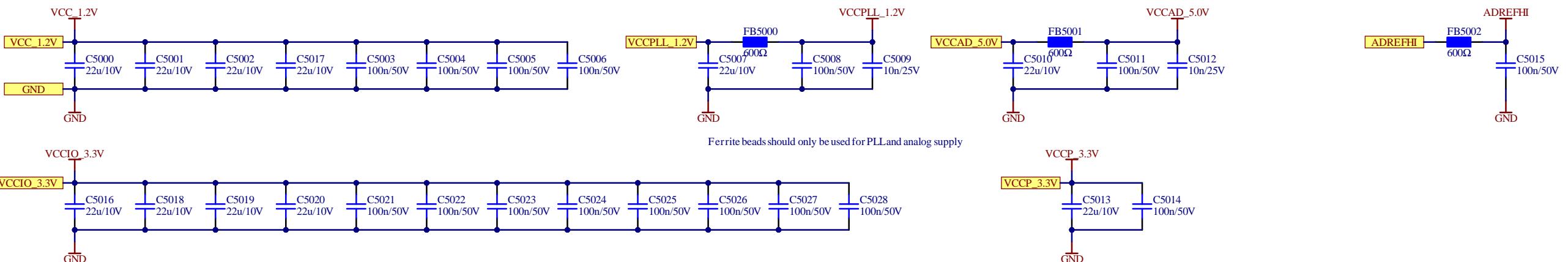


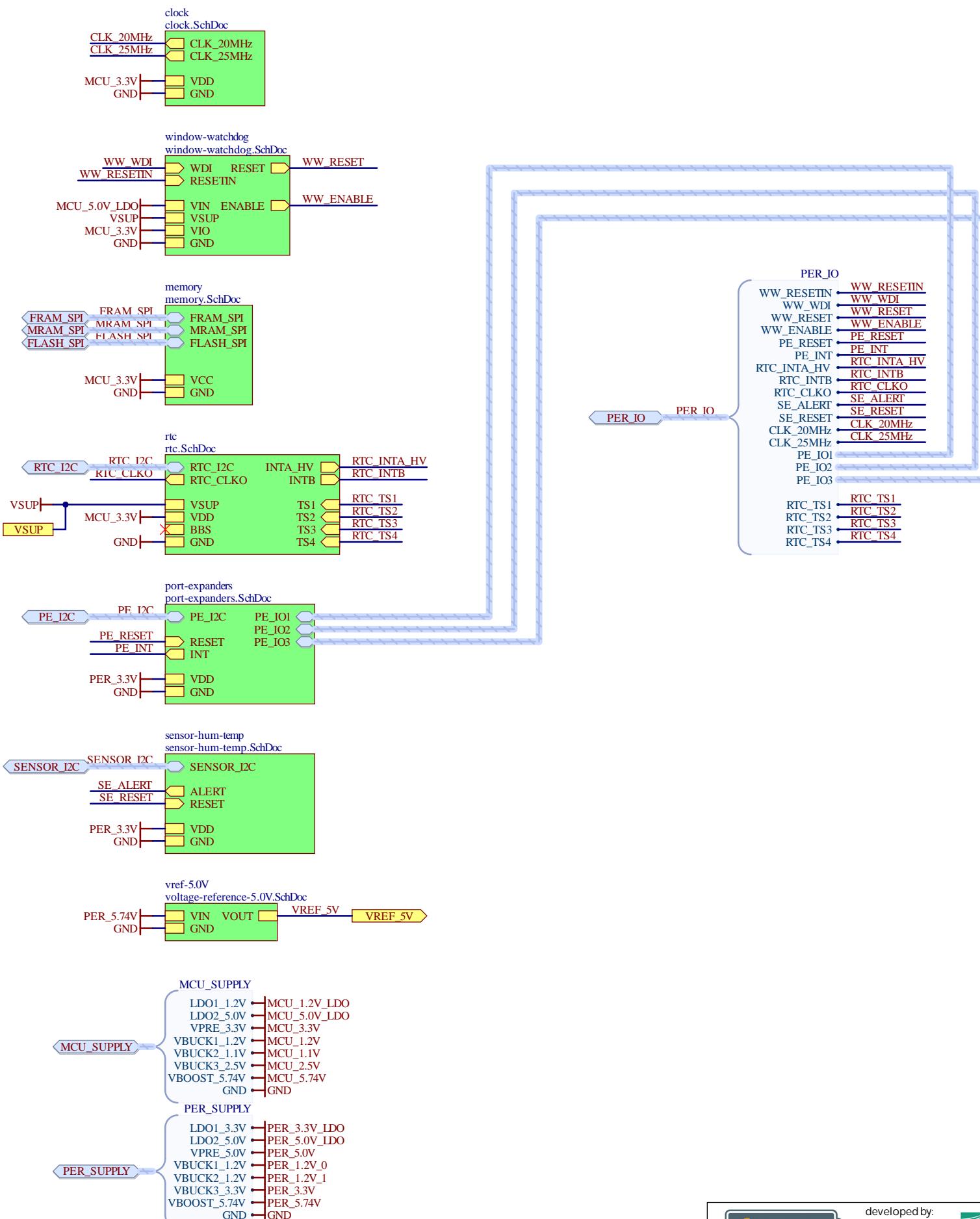
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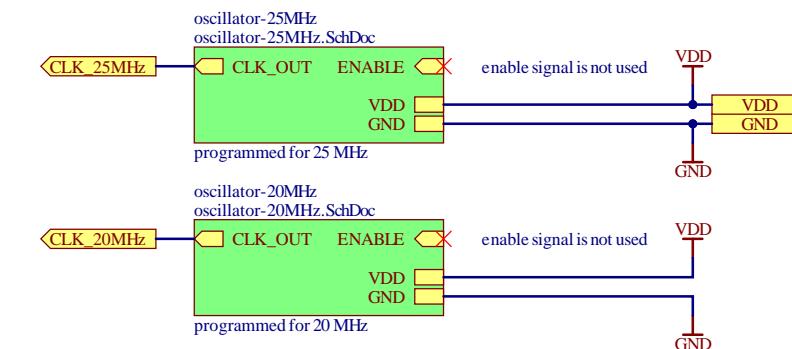
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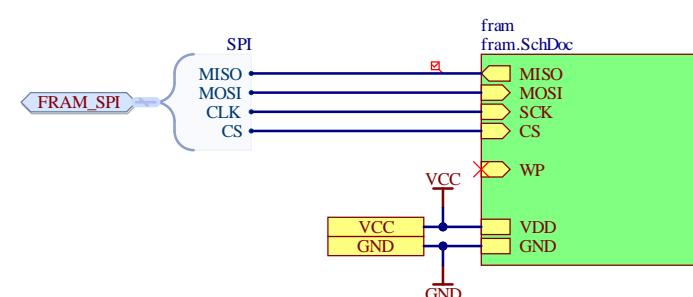
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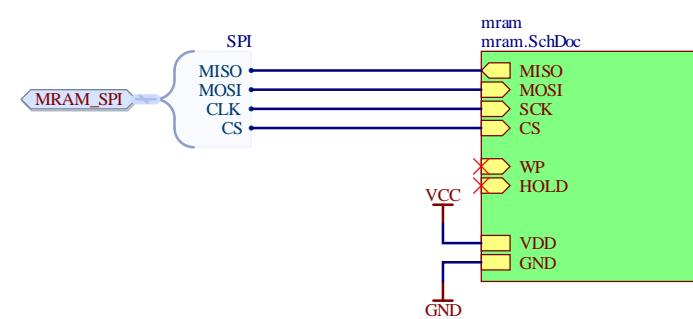
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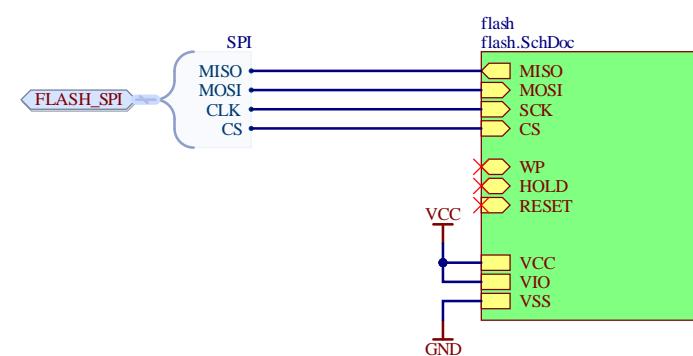
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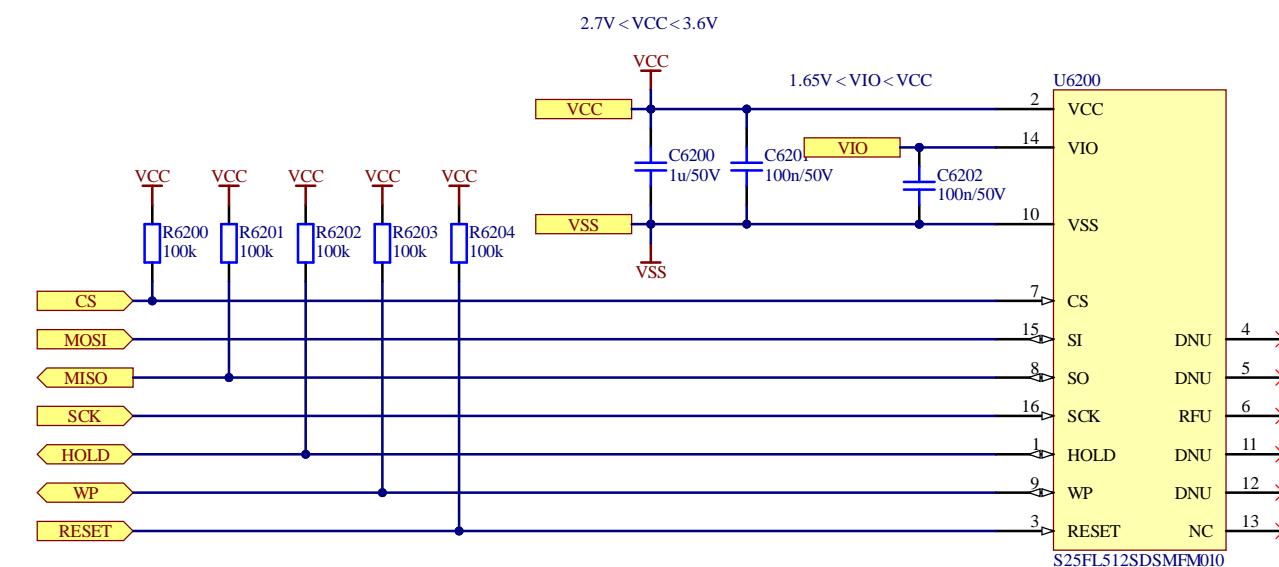
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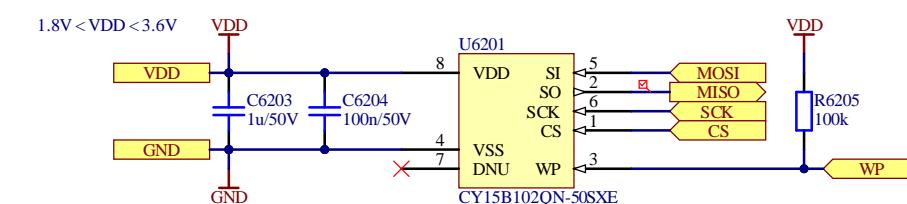
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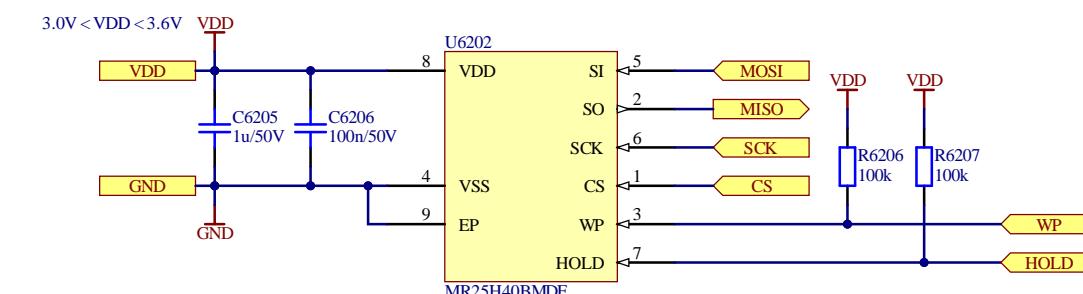
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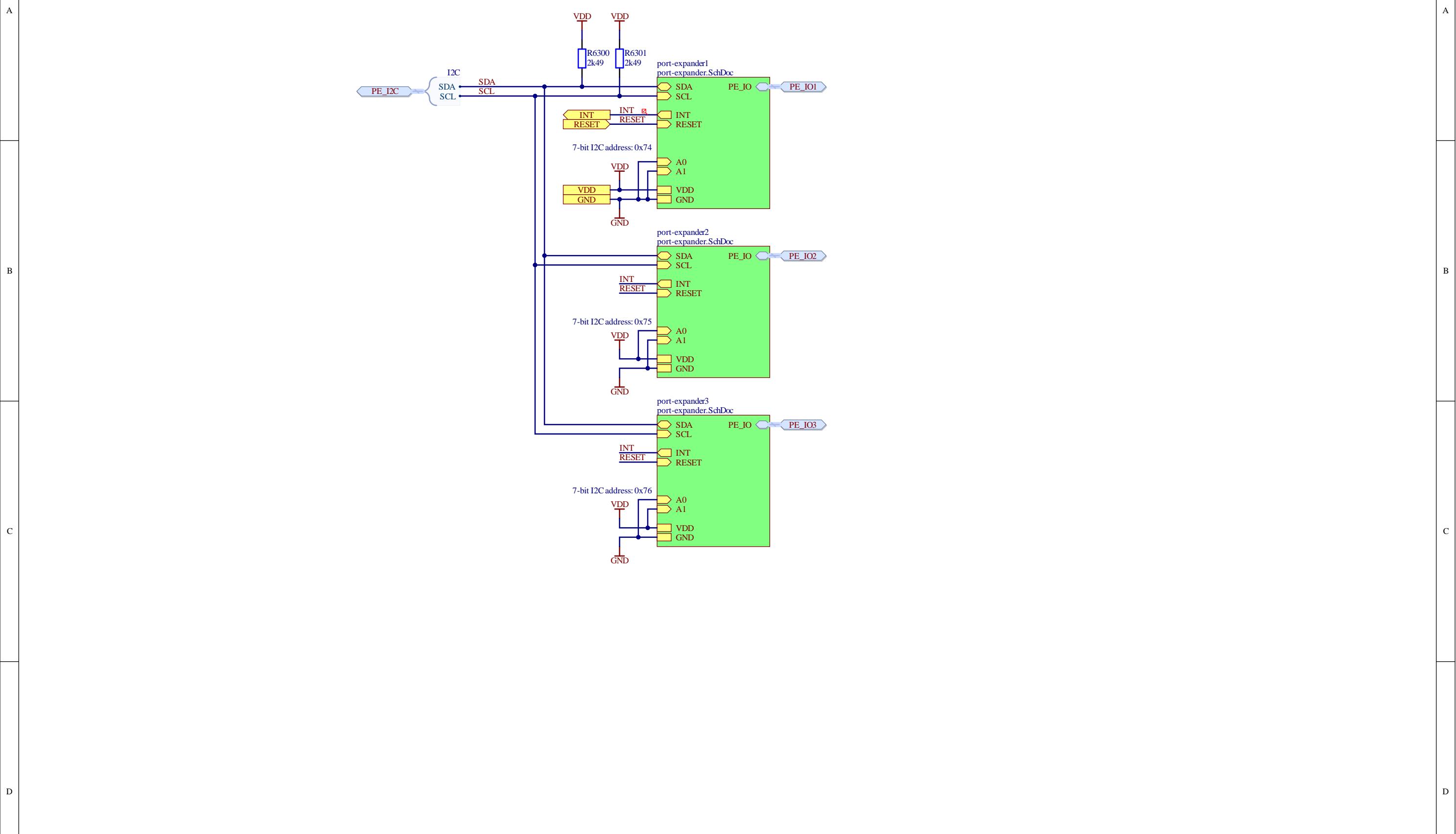
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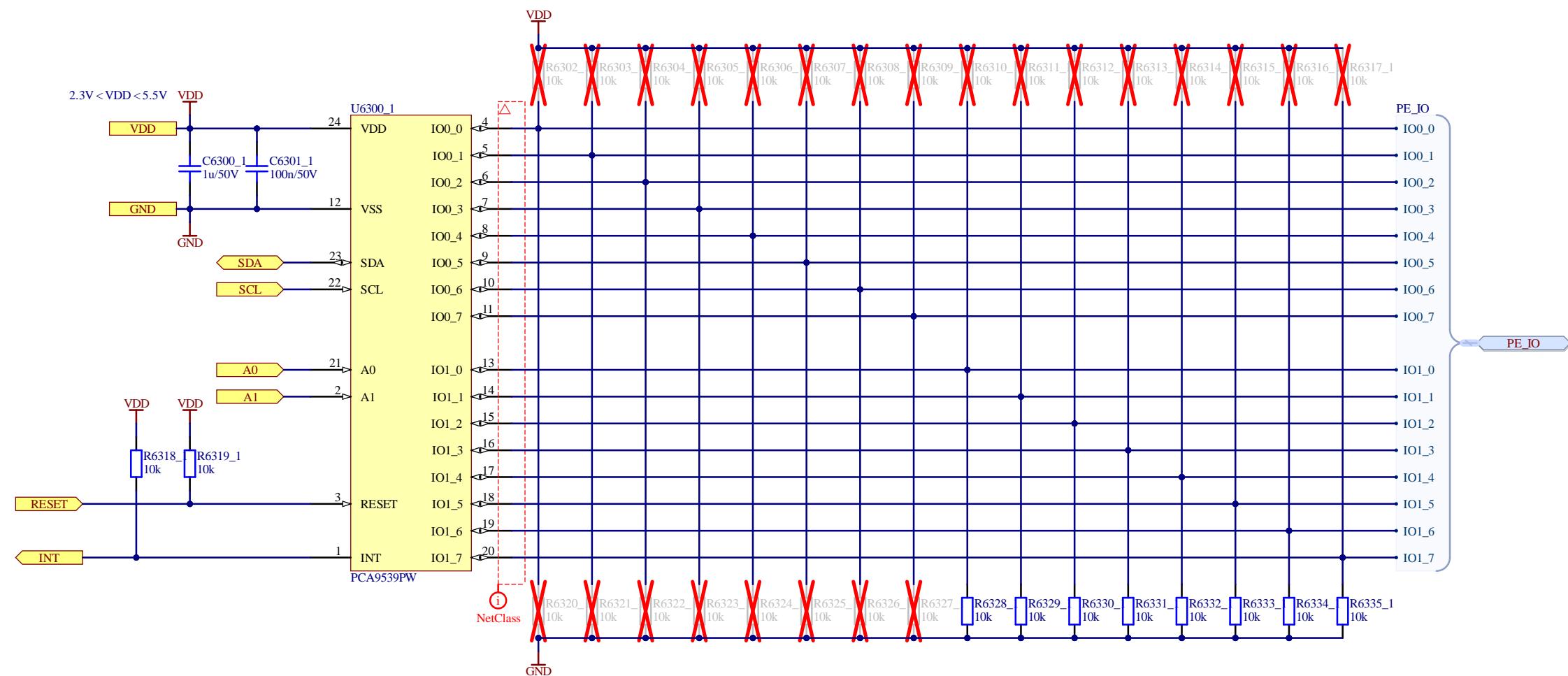
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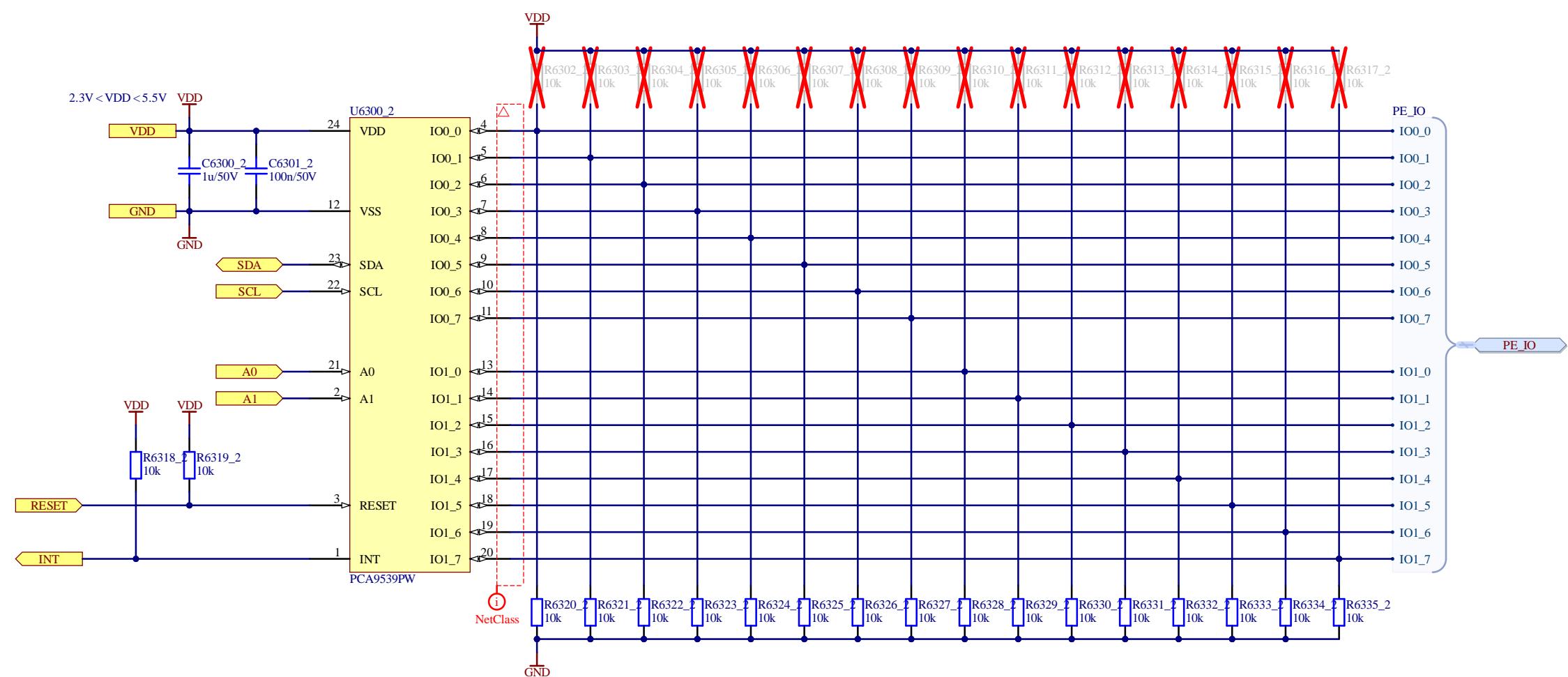


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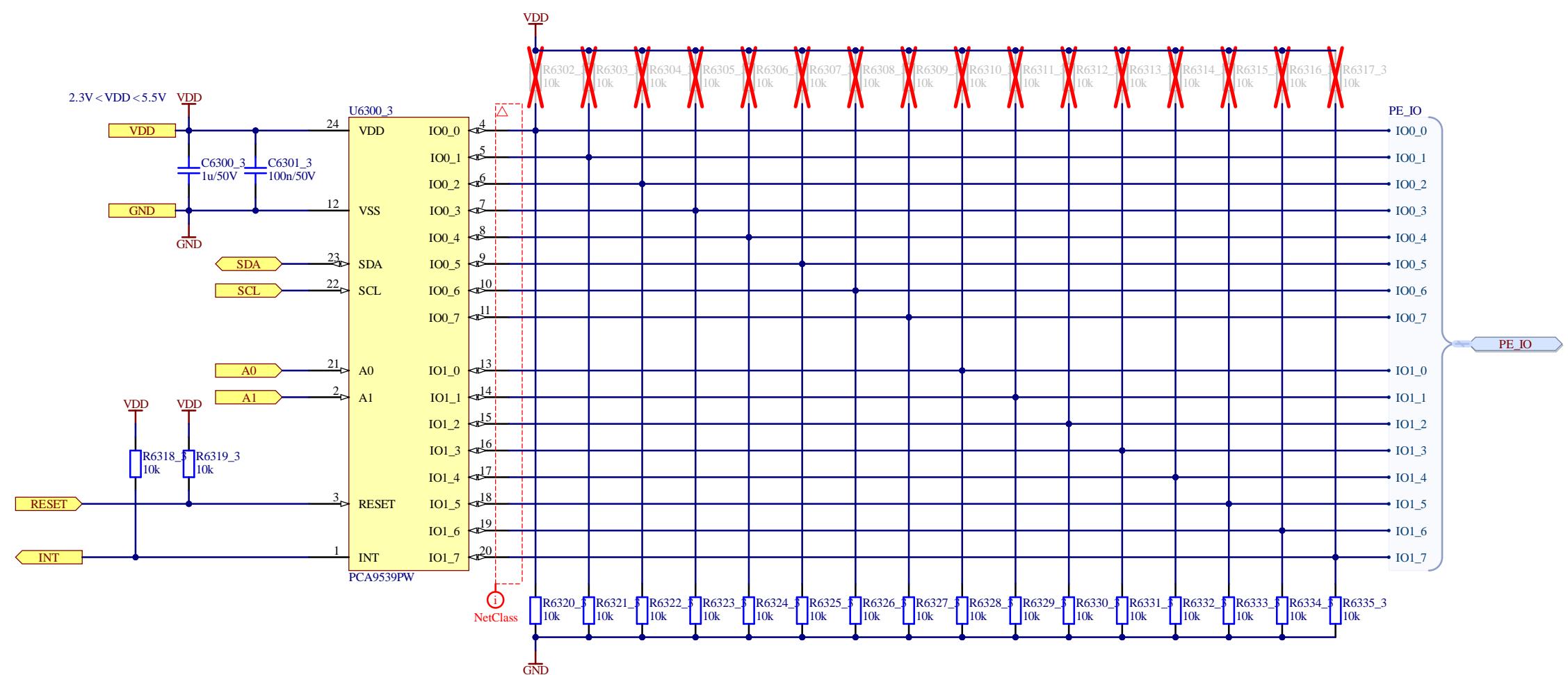
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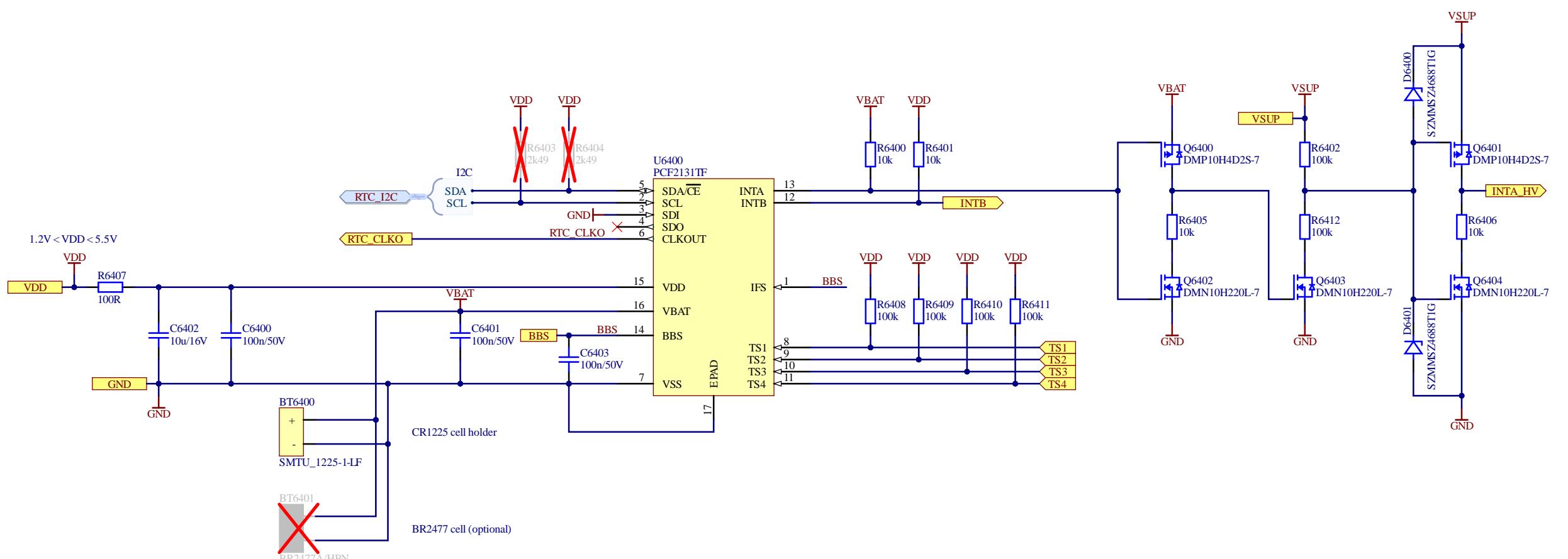
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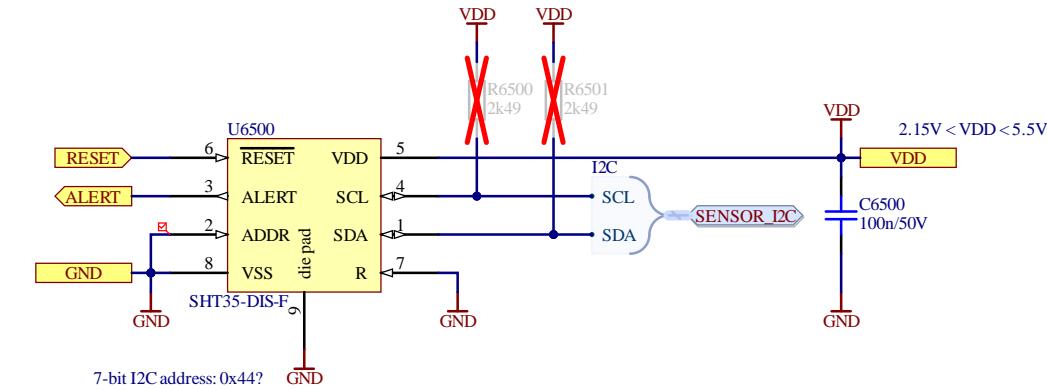
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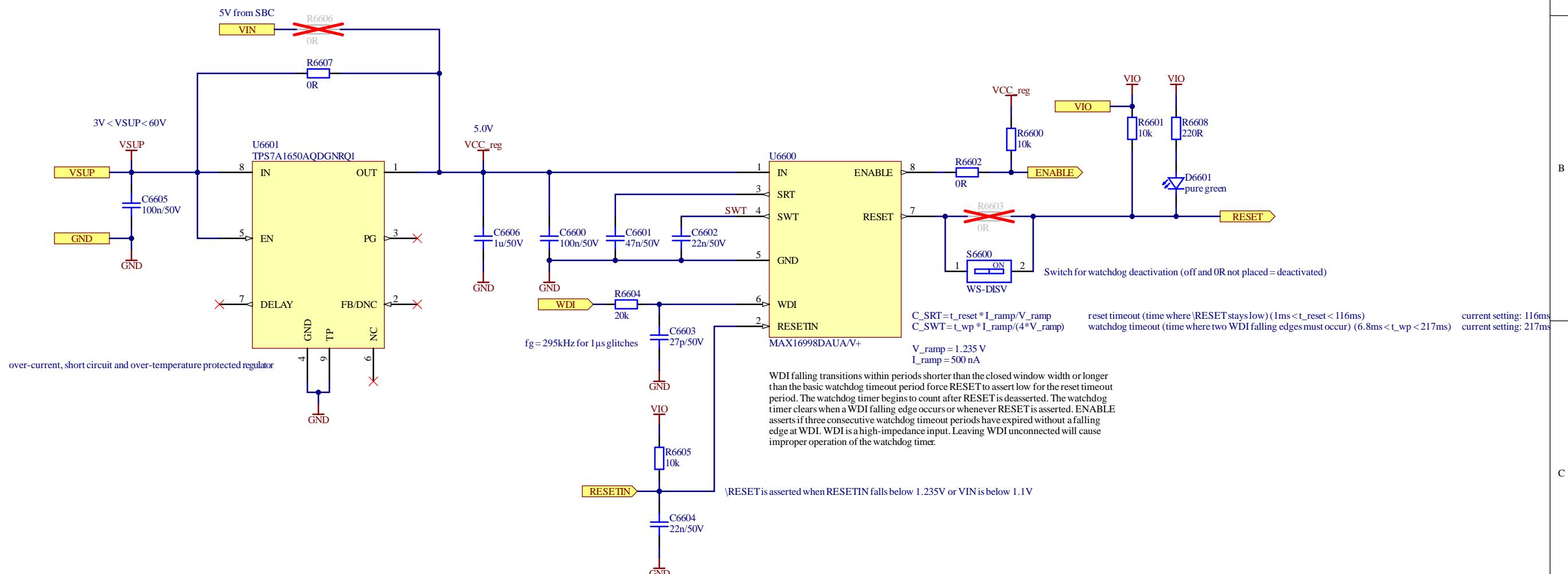
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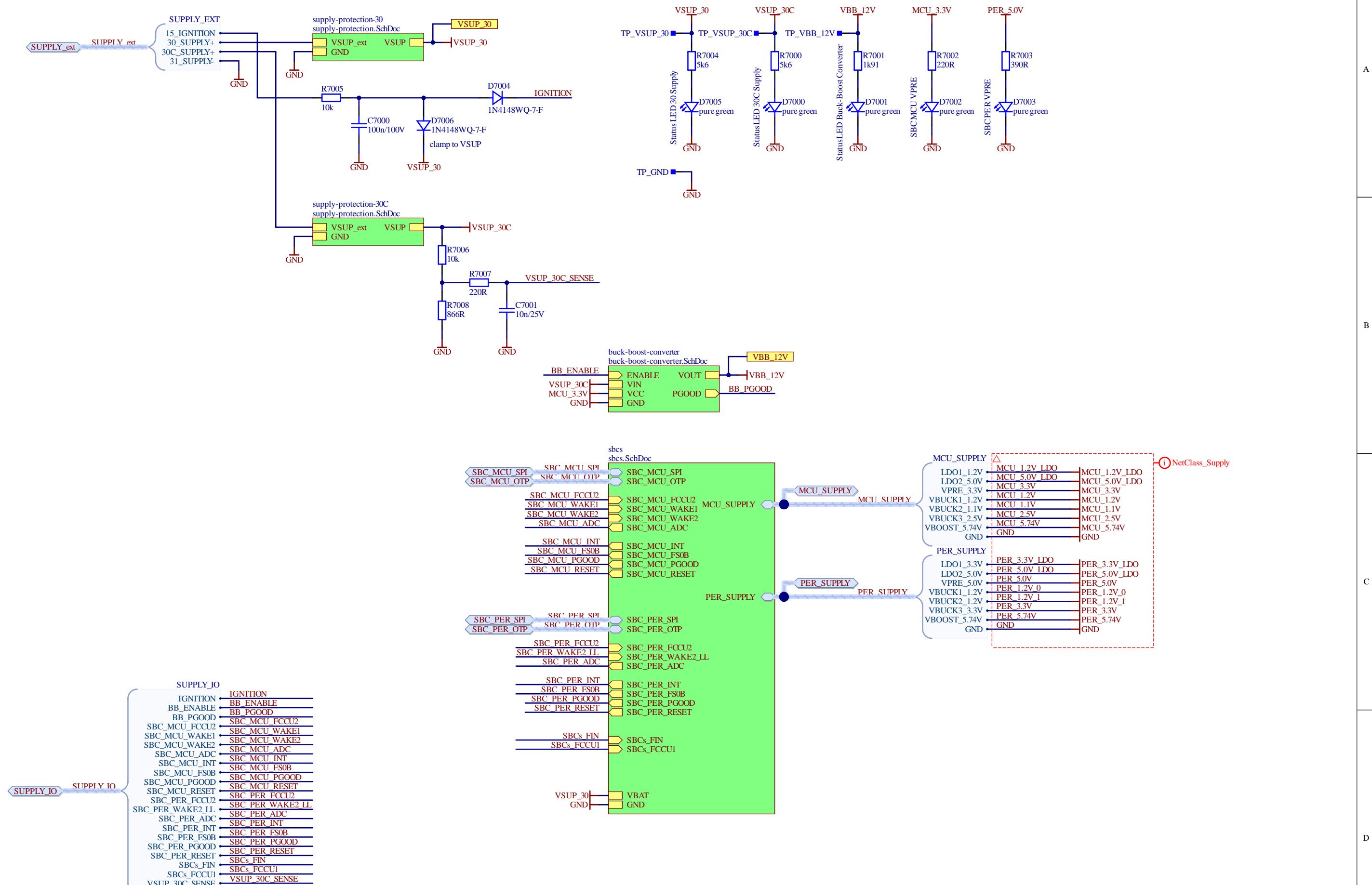
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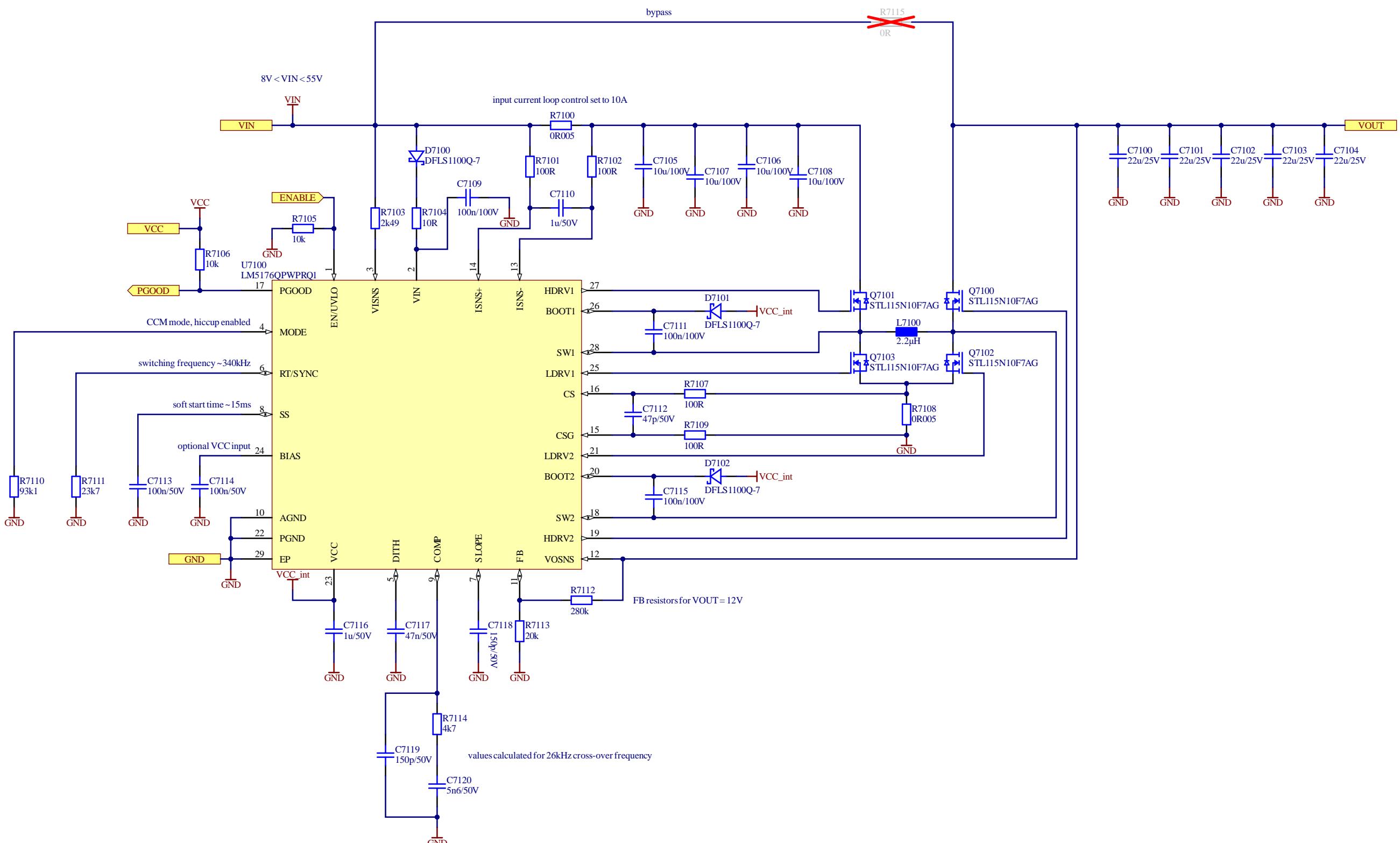
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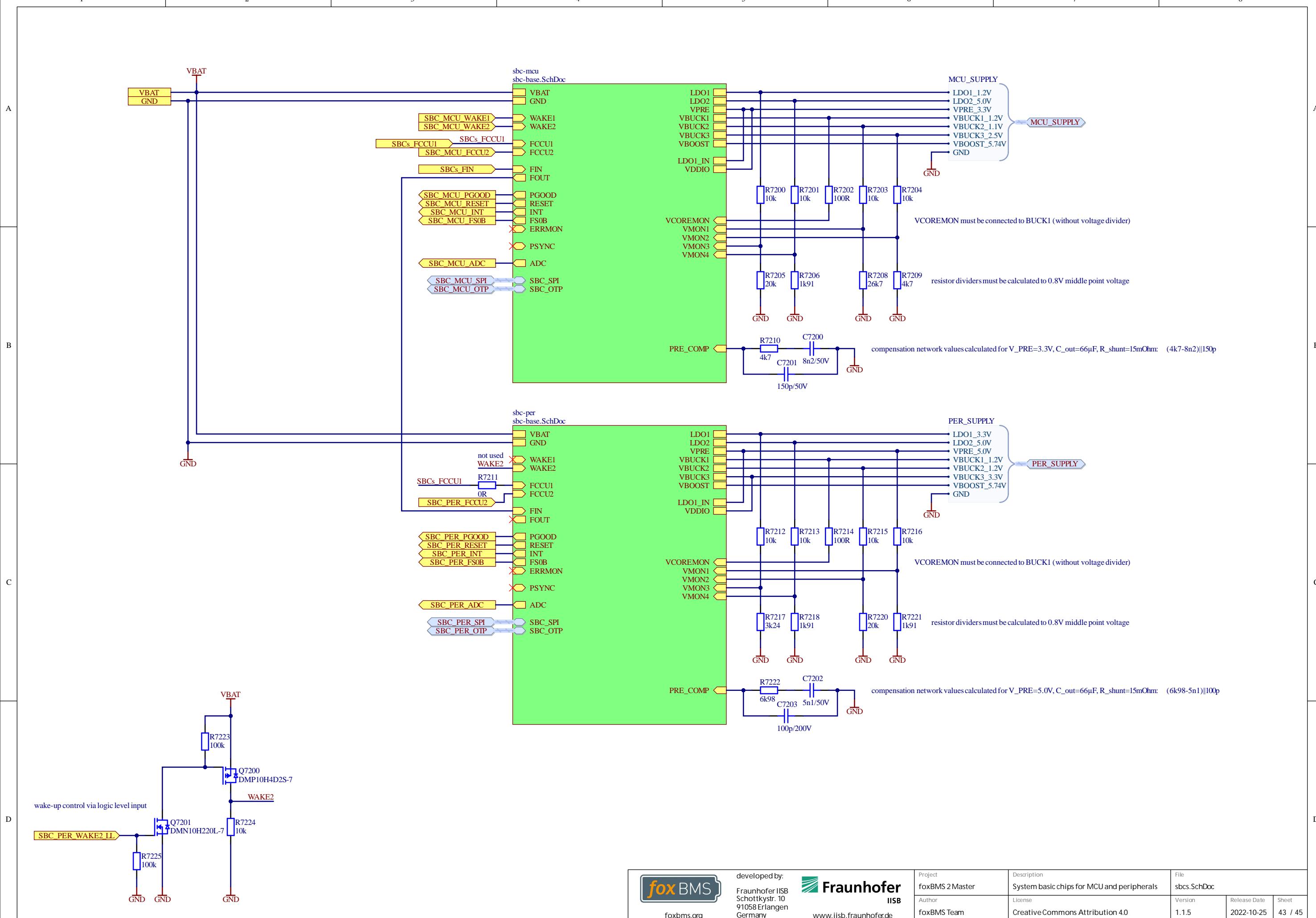
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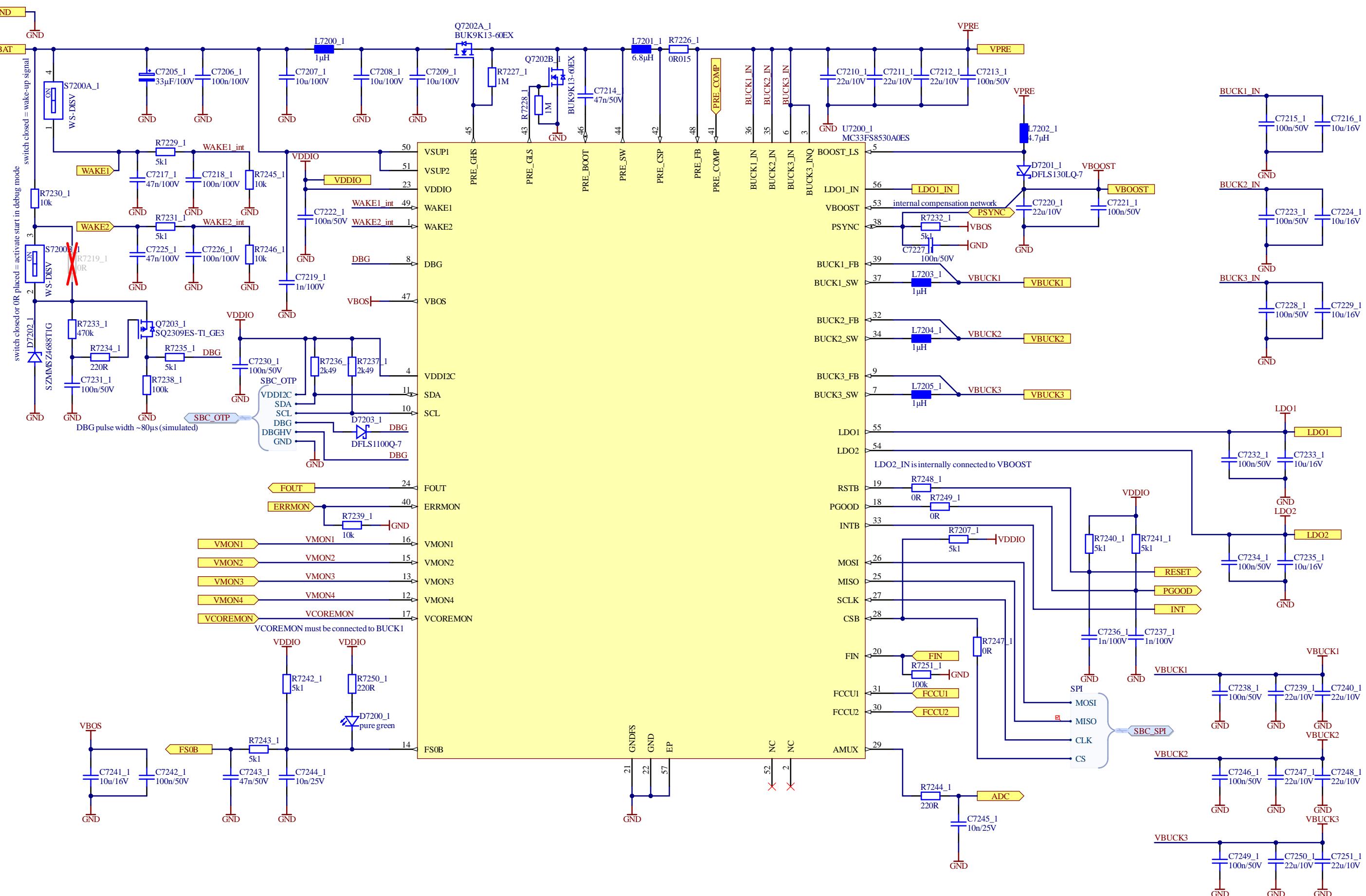


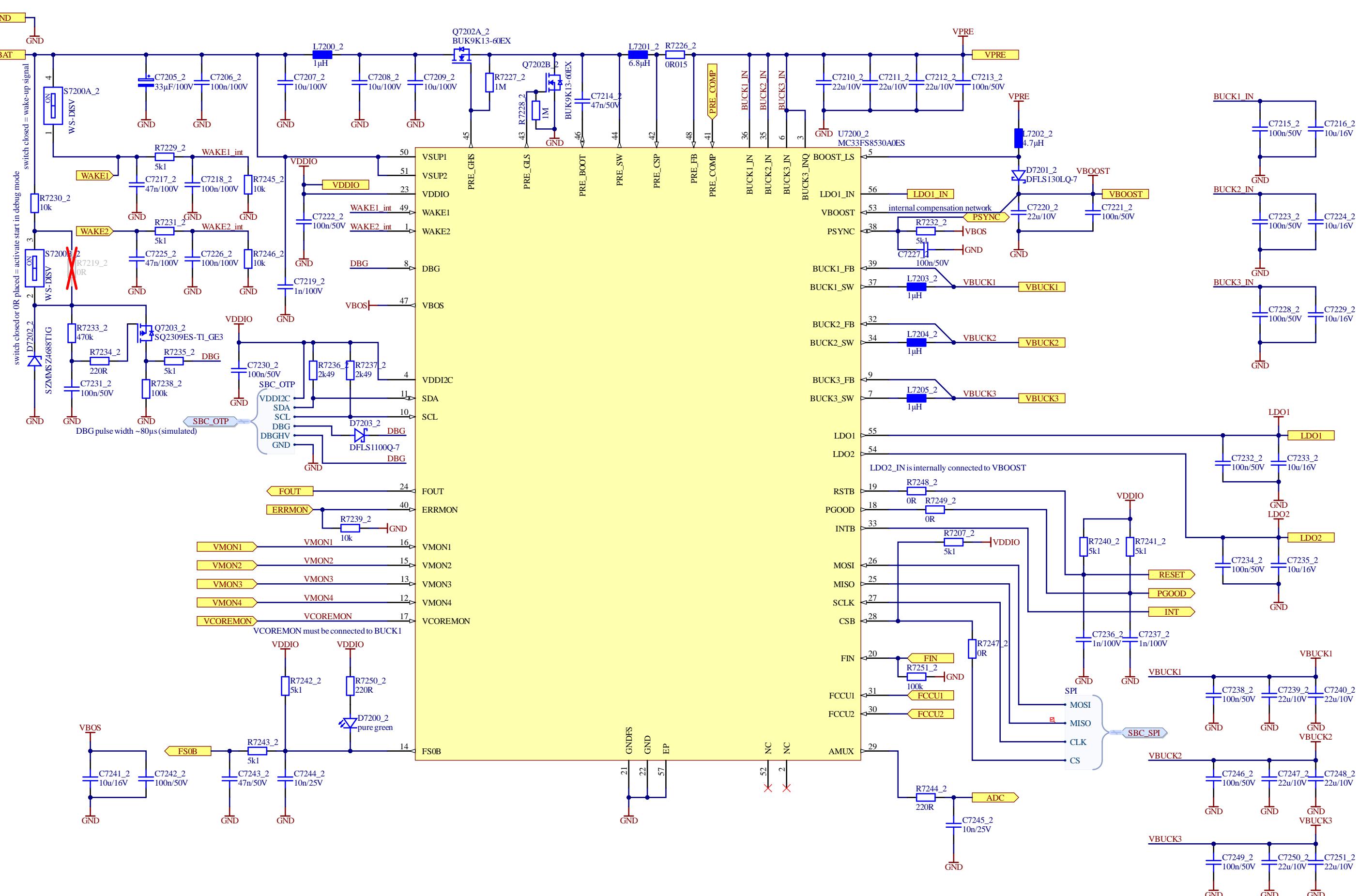






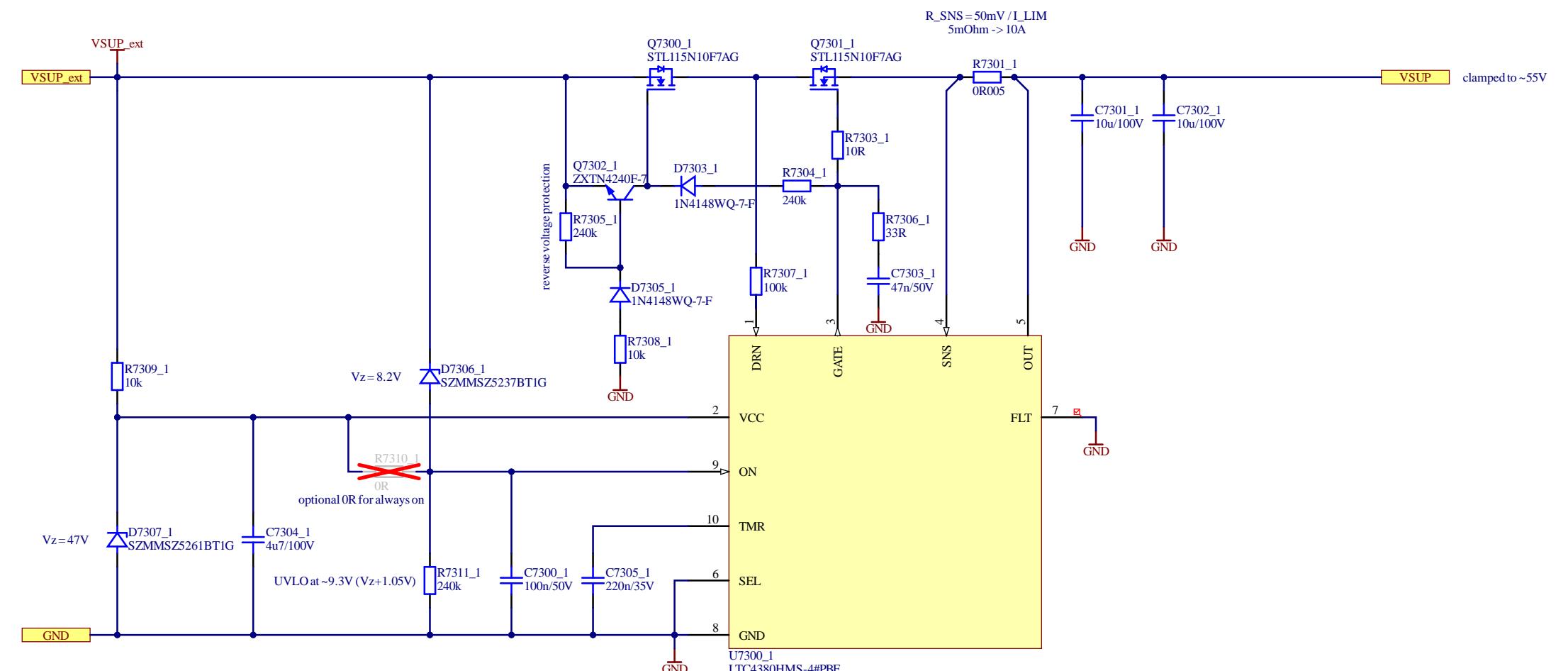






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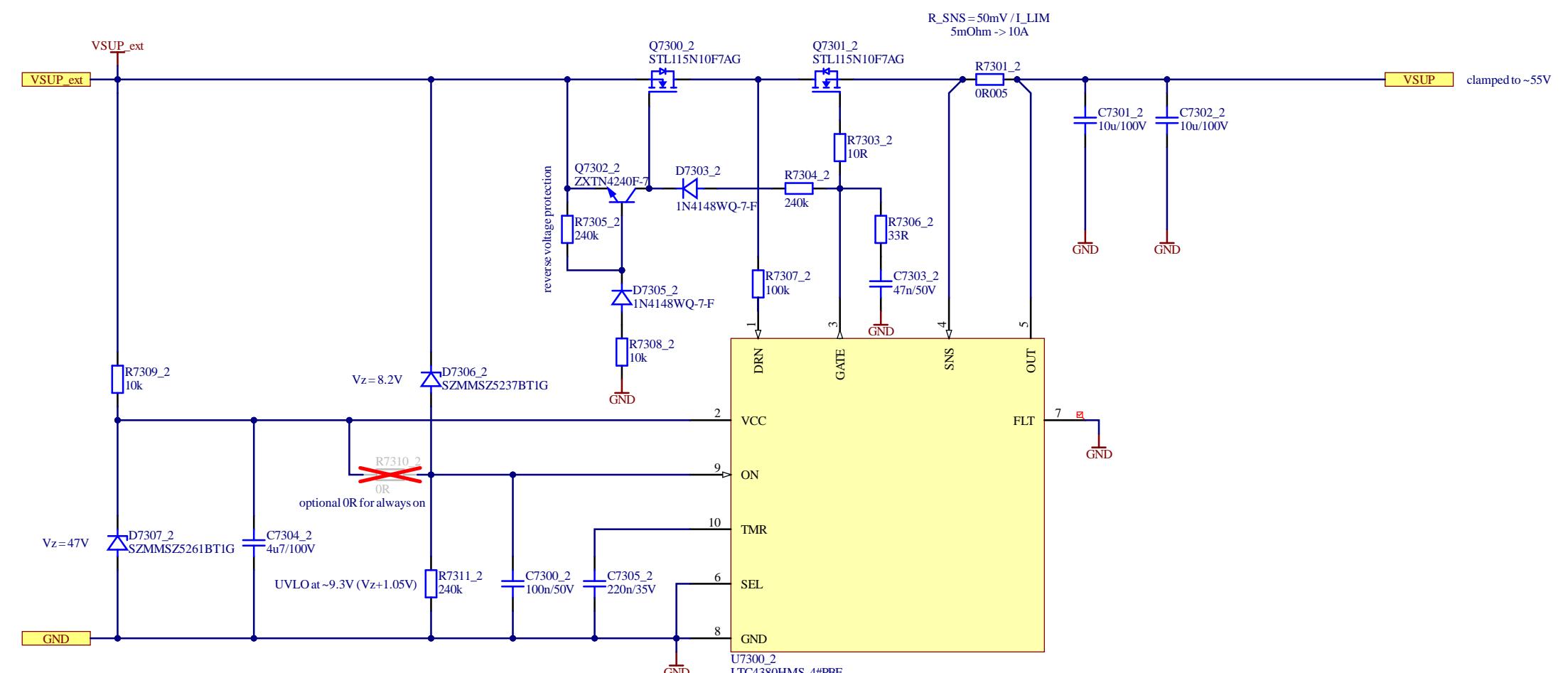
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