# **CPC X-MEM**Get Started!



©2014 Arnold Computer Multi-Expansion

# X-MEM Floppy Disc Utilities

#### 1. X-MEM Init Pass

This program allows to clear all the 32 ROMs stored inside your X-MEM board.  ${\tt RUN''INIT''}$ 

The process takes one minute to erase all the slots. After that, your X-MEM will be not able to boot if you don't install it first. Don't forget to set the BOOT switch (2) to "CPC" position to continue.

#### 2. X-MEM Install Pass

This program allows to install the X-MEM after the Init pass. Also, it can update the Firmware and BASIC, targetting your CPC and keyboard layout. RUN"INSTALL"

The process takes few seconds for reprogramming the Lower ROM and ROM 0. Then you will be able to choose the CPC target model for programming the X-MEM. Note that ROM 1 will be used in the future to extend the Firmware 3.14. Currently, a 448K RAM Drive "C" is installed. Use |C from BASIC or C: from CP/M.

#### 3. X-MEM Rescue Pass

If your X-MEM is programmed with one (or more) defective ROM(s) that prevent your computer to boot properly, please turn it off. After that, set the READ ROM jumper (4) to "NO" and turn it on again.

RUN"RESCUE"

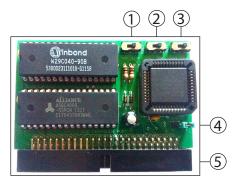
In all these cases, make sure that your X-MEM ROM switch (3) is set to "FREE". For software updates and support, please visit: <a href="http://www.centpourcent.net">http://www.centpourcent.net</a>

## Introduction

Thank you for choosing this new ACME hardware for your good old CPC! The X-MEM is the state of the art of the memory expansions. It embeds up to 512K RAM/ROM and allows to replace the Lower ROM and ROM 0 to use different Firmwares and BASIC to push all CPC at the same level of compatibility.

# X-MEM Diagram

- 1. CPC 464/6128 switch
- 2. BOOT CPC/ROM switch
- 3. ROM LOCK/FREE switch
- 4. READ ROM NO/YES jumper
- 5. Expansion port connector
- = Set RAM mode for 464/664 or 6128
- = Set boot mode from CPC or X-MEM
- = Set ROM write protect, like a floppy tab
- = Ignore the ROM part. Rescue mode only!
- = To MotherX4 or CPC using a ribbon cable



## Firmware 3.14

#### 1. FW Boot

Display the real amount of RAM and the CRTC type of your computer. You can skip the ROMs initialization by keeping the ESC key pressed while (re)booting. It's a safe way to run conflicting programs and avoids to apply a rescue pass.

#### 2. ROMs Init

The Firmware 3.14 initializes the first 32 ROMs with RSX support on all CPCs. The ROMs messages are disabled to boot faster and not scroll the screen.

### 3. Burning ROMs

You can program from BASIC the X-MEM: CALL &B8DD,<source>,<rom\_id> i.e. "CALL &B8DD.&4000.15" set the ROM 15 with 16K loaded at &4000.

# **Troubleshooting**

Q1: After plugging properly the expansion, I get random bugs or no display. A1: Check the boot switch (2) position and clean your CPC Expansion port.

Q2: All programs don't detect the extra RAM on my 464/664 and fail to run. A2: Check the RAM switch (1) position, then your +5V power supply.

Q3: My favorite utilities fail to program the X-MEM ROMs. (RMA, ROMAN,...)
A3: Check the ROM switch (3) position. Use only the provided programs.

Q4: After programming some ROMs, my CPC gets sticked at FW initialization.
A4: Try to boot with the ESC key pressed, then apply the X-MEM rescue pass.

## **RAM Mapping**

8x 64K banks (of 4x16K pages) are available from &7Fxx,&C0 to &7Fxx,&FF

E	BANK		S	S MM		CPC BASE RAM / X-MEM EXTENDED RAM								
5	4	3	2	1 0		#0000 - #3FFF	#4000 - #7FFF	#8000 - #BFFF	#C000 - #FFFF					
	-		0	0	0	CPC page 0	CPC page 1	CPC page 2	CPC page 3					
	В		0	0	1	CPC page 0	CPC page 1	CPC page 2	Bank B, page 3 Bank B, page 3					
	В		0	1	0	Bank B, page 0	Bank B, page 1	Bank B, page 2						
	В		0	1 1		CPC page 0	CPC page 3	CPC page 2	Bank B, page 3					
	В		1	F		CPC page 0	Bank B, page P	CPC page 2	CPC page 3					

Due to a Gate Array limitation, the "C3" mode with upper selected will wrongly map the ROM at #4000.

# **ROM Mapping**

32 ROMs (4x8) of 16K each are available from the buffer &DFxx,<ROM ID>

X-MEM 8bit buffer								Firmware	X-MEM ROM ID							
7	6	5	4	3	2		0	Initialization	(bit5=0)							
0	0	-	0	0	Р	Р	Р	FW1.0	0	1	2	3	4	5	6	
0	0	-	0	1	Р	Р	Р	FW2.0, FW3.0	8	9	10	11	12	13	14	15
0	0	-	1	0	Р	Р	Р		16	17	18	19	20	21	22	23
0	0	-	1	1	Р	Р	Р	FW3.14	24	25	26	27	28	29	30	31

The ROM 7 is not altered by the X-MEM device. The Firmware is used instead. You must set the bit5 to 1 for writing the Lower ROM (&47) and ROM 0 (&40).

Special thanks to Mauricio, Gérald, Yves and Arnaud. X-MEM - 2014 ACME