Revision history

Revision No.	Data	Description
2.2	Sep. 23, 2014	(1) Change "XRTCLK" pin to "NC" in Table 2-28.
2.1	Apr. 2, 2014	(1) Add SSIO_DT about PA21 pin in Table 2-2.
2.0	Sep. 26, 2013	(1) Update Figure 1-1: JZ4780 Diagram.
1.2	Aug. 29, 2013	(1) Modify power of USB_VBUS to 5V in Table 2-22.
1.1	Apr. 19, 2013	(1) Add support DDR3L and DDR3U in chapter 1.2.5: Memory Interface. (2) Modify description of $t_{D\_PPRST\_}$ in table 3-17.
1.0	Feb. 5, 2013	Initial release.