Hardware Accelerator for the Training of Neural Networks

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Abstract

The goal of the thesis is to ...

Preface

This thesis was prepared at DTU Compute in fulfilment of the requirements for acquiring an M.Sc. in Engineering.

The thesis deals with ...

The thesis consists of ...

Lyngby, 27-June-2019

James Erik Groving Meade

Acknowledgements

I would like to thank my....

Contents

Chapter 1

Introduction

The following text is a message to the student and should be removed during the writing process.

Please note the following instructions regarding an MSc thesis outlined in the study handbook:

"During the first month, the student is to submit a project plan outlining the objective of the thesis and justification for same to his/her supervisor. In the project plan, the student is also to take into account the overarching learning objectives listed above. When submitting the thesis, the student is to enclose a separate document presenting the original project plan and a revision of same, where appropriate. In addition, the document is to include a brief auto-evaluation of the project process."

To learn more about the rules for an MSc thesis, please consult the rules for your own MSc programme at `http://sdb.dtu.dk`.

1.1 Project plan

We note that the contents of the project plan is also something we would like to see in the introductory chapter of your thesis. In fact, you can reuse your final project plan (possibly extended) as the introduction. If you prefer to write an introduction from scratch, it is, of course, important that it is consistent with the final project plan.

1.2 The "separate document"

It is also important to note that the separate document containing

- original project plan
- possibly revised project plan.
- brief self-evaluation

mentioned above will be passed on to the external examiner and since it contains the learning goals and the objectives for your thesis, it will be taken into account when your thesis is assessed.

Chapter 2

Background

Chapter 3

Software Model

3.1 Overview

A general-purpose neural network model was developed to verify the algorithmic soundness of the proposed forward and backward passes.

3.2 Testing

3.3 Usage

Chapter 4

Hardware Model and Implementation

Chapter 5

Hardware Model Testing

Chapter 6

Results

Chapter 7

Analysis

Chapter 8

Future Work

Chapter 9

Discussion

Chapter 10

Conclusion

Appendix A

Stuff

This appendix is full of stuff ...

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