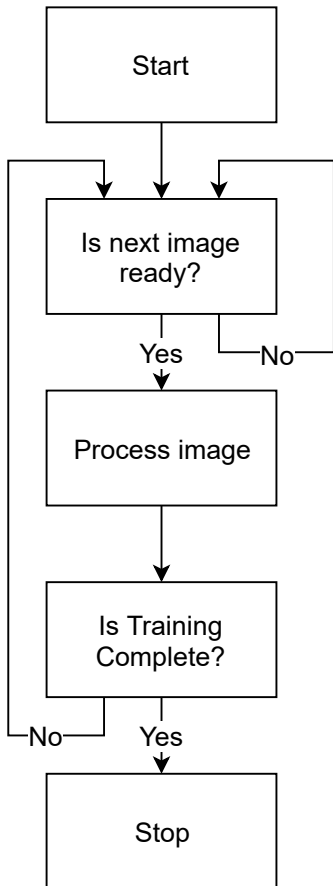


FPGA Control Flow



PS Control Flow

