# Hardware Accelerator for the Training of Neural Networks

James Erik Groving Meade



Kongens Lyngby 2019

Technical University of Denmark
Department of Applied Mathematics and Computer Science
Richard Petersens Plads, building 324,
2800 Kongens Lyngby, Denmark
Phone +45 4525 3031
compute@compute.dtu.dk
www.compute.dtu.dk

## **Abstract**

The goal of the thesis is to  $\dots$ 

#### Preface

This thesis was prepared at DTU Compute in fulfilment of the requirements for acquiring an M.Sc. in Engineering.

The thesis deals with ...

The thesis consists of ...

Lyngby, 27-June-2019

Not Real

James Erik Groving Meade

# Acknowledgements

I would like to thank my....

#### Contents

A	bstra	knowledgements v Introduction 1						
P	Preface							
A	ckno	$\mathbf{wledgements}$	$\mathbf{v}$					
1	Inti		_					
	1.1	Project plan	$^{2}$					
	1.2	The "separate document"	2					
2	Bac	ekground and Motivation	3					
	2.1	A Brief Summary of Neural Networks	3					
	2.2	Related Work	3					
3	Software Model							
	3.1	Overview	5					
	3.2	Motivation	5					
	3.3	Design	6					
		3.3.1 Layers	6					
		3.3.2 Training	6					
	3.4	Structure	8					
	3.5	Testing	10					
	3.6	Usage	10					
4	Hardware Model and Implementation 11							
5	Har	dware Model Testing	13					
6	Res	ults	15					

viii		CONTENTS	
7	Analysis	17	
8	Future Work	19	
9	Discussion	21	
10	Conclusion	23	
A	Stuff	25	
Bibliography		27	

#### CHAPTER 1

#### Introduction

The following text is a message to the student and should be removed during the writing process.

Please note the following instructions regarding an MSc thesis outlined in the study handbook:

"During the first month, the student is to submit a project plan outlining the objective of the thesis and justification for same to his/her supervisor. In the project plan, the student is also to take into account the overarching learning objectives listed above. When submitting the thesis, the student is to enclose a separate document presenting the original project plan and a revision of same, where appropriate. In addition, the document is to include a brief auto-evaluation of the project process."

To learn more about the rules for an MSc thesis, please consult the rules for your own MSc programme at http://sdb.dtu.dk.

2 Introduction

#### 1.1 Project plan

We note that the contents of the project plan is also something we would like to see in the introductory chapter of your thesis. In fact, you can reuse your final project plan (possibly extended) as the introduction. If you prefer to write an introduction from scratch, it is, of course, important that it is consistent with the final project plan.

#### 1.2 The "separate document"

It is also important to note that the separate document containing

- original project plan
- possibly revised project plan.
- brief self-evaluation

mentioned above will be passed on to the external examiner and since it contains the learning goals and the objectives for your thesis, it will be taken into account when your thesis is assessed.

#### Chapter 2

# Background and Motivation

- 2.1 A Brief Summary of Neural Networks
- 2.2 Related Work

#### CHAPTER 3

#### Software Model

#### 3.1 Overview

This section documents the general-purpose neural network framework that was written in C++ for this thesis. There is an example program that trains on the MNIST dataset and documents epoch-by-epoch training statistics. MNIST is a dataset of handwritten digits, containing 60,000 training images and 10,000 test images. The source code for the software model can be found in the appendix as well as online on github.<sup>1</sup>

#### 3.2 Motivation

The software neural network framework was written so that the FPGA hardware model could be benchmarked against a CPU-based model that performs neural network inference and backward passes using the same method as the hardware model. This benchmark could be used to evaluate the performance of the hardware model. In addition, it could be benchmarked against professional

 $<sup>^{1} \</sup>texttt{https://github.com/erikgroving/NeuralNetworkHardwareAccelerator/tree/master/SWModel.}$ 

6 Software Model

open-source deep-learning frameworks that make use of advanced algebraic methods to perform computation such as matrix multiplication that inherently offer more efficiency. Furthermore, by developing a software model, the algorithmic integrity of the proposed network was able to verified and tested in an expedient manner by using a well-known testing framework, Google Test. Finally, if high floating-point precision were needed for training a network, then the software model could be used to learn the weights and parameters, and then subsequently be loaded into the weight BRAM of the FPGA hardware model.

#### 3.3 Design

#### 3.3.1 Layers

The software model was designed to be flexible such that any neural network architecture may be constructed so long as the layer types were implemented. The model currently supports 2D convolutional, fully-connected, and pooling layers.

All layers are derived from a base class, Layer. Certain methods such as forward() and backward() must be implemented by all derived classes. There is then a Net class that contains a vector of Layer objects. This allows for a flexible design, as one only need add layers to the Net object. Furthermore, the model can easily be extended to other layer types so long as the layer type derives from Layer.

The non-linear activation function used in the model is ReLU because the derivative is trivial to compute. Compared to the sigmoid function, ReLU is much more computationally feasible for an FPGA hardware implementation, and thereofre, ReLU was used in the software model so that both models would use the same activation function.

#### 3.3.2 Training

The Softmax Function and Computing Loss Gradients The network uses an implicit Softmax function for the last layer since this converts the logits in the last layer to numbers that can be interpreted as probabilities, ideal for

3.3 Design 7

image classification. The softmax function is as shown below:

$$\sigma(\mathbf{x})_i = \frac{e^{x_i}}{\sum_{j=1}^N e^{x_j}}$$

In this function,  $x_i$  is the output of neuron i in the last layer.

The loss gradients for the neurons in the last layer are computed using multiclass cross entropy loss. Cross entropy loss is computed using probabilities and is defined as:

$$\mathcal{L}(x) = \sum_{i=1}^{N} q(x_i) \log(p(x_i))$$

In this function,  $q(x_i)$  is the true probability of x belonging to class i, therefore,  $q(x_i) = 1$  when x is of class i and 0 otherwise. Conversely,  $p(x_i)$  is equal to the predicted probability resultant from the softmax function. Therefore, only one probability will account for loss, however, since each probability is an output from the softmax function which takes in all neuron outputs as input, all neurons in the last layer will have a loss gradient.

The derivative of this loss function is needed to perform backpropagation. We define  $\mathcal{L}_i$  as the loss for neuron i in the last layer and  $z_i$  as the output of neuron i. We also introduce  $y_i$ , which is 1 if x is an instance of class i and 0 otherwise. We can then compute the loss gradient for neuron i in the last layer quite simply as follows:

$$\frac{\delta \mathcal{L}_i}{\delta z_i} = z_i - y_i$$

**Batch Size** The software model supports batch training and thus a batch size is to be specified when creating an instance of a new network.

Learning Rate and Momentum The software model learns using stochastic gradient descent. As such, the network is configured with a learning rate and momentum. The learning rate may be manually readjusted during training epochs. Momentum is a learning technique in that previous updates to a parameter should impact the update in in a geometrically decreasing fashion. We first define a few parameters:

m — the momentum parameter

v — 'velocity'

lr — the learning rate

dx — the loss gradient for some weight or bias x.

8 Software Model

The momentum-based update in the software model can then be mathematically represented in the following manner:

$$v = (m \times v) - (lr \times dx)$$
$$x = x + v$$

We can observe that each time we update x, the previous updates update will have an effect, with the most recent updates having more effect than older ones. A typical value for momentum is 0.9.

#### 3.4 Structure

The software model contains a Makefile and three folders: data, src and test. The data folder contains the MNIST binary data files, and is loaded by the example program that trains on the MNIST dataset. The src folder contains the source code of the neural network framework. The test folder contains test made using the Google Test C++ testing framework. The Makefile is used to build the source as well as tests. This section will detail the source files in the src folder that are core to the software model framework. The files main.cpp and parse\_data{.cpp, .h} will be described in section 3.6 that focuses on usage.

net{.cpp, .h} These files contain the definition of the Net class, the highest-level class of the network. After initializing a Net object, layers can be added to the neural network by calling the addLayer() method which will add a Layer object to a vector. The Net class also stores intermediate activations from the current inference, which are required when performing backward pass to calculate loss gradients. The key parameters to the Net object are set in its constructor, and are defined in table 3.1.

The Net class has a method inference() that computes the forward pass for a batch of inputs, thus the argument is a 2-d vector, with each outer index corresponding to an input. The () operator has also been overloaded to call inference(). This is all that is needed to compute a forward pass.

To compute the backward pass, computeLossAndGradients() should be called first. This method takes in the label data as a vector for the inputs as an argument and computes the loss gradients for the outer layer of the network. Next, a call to backpropLoss() should be made; this method propagates the outer layer loss gradients back through the neural network. After the loss has been back-propagated, weights of each Neuron in the network should be updated

3.4 Structure 9

Name	Type	Description
in	uint32_t	Size of the input to the neural network.
out	uint32_t	Size of the output of the neural network.
bs	uint32_t	Size of the batch size to be used when training the
		net.
lr	uint32_t	The learning rate to be used during training of the
		network. Can be set and read using the functions
		${\tt setLearningRate()} \ {\tt and} \ {\tt getLearningRate()}.$
momentum	uint32_t	The momentum to be used when performing upda-
		tes to the weights and biases of the network.

Table 3.1: Description of parameters for the constructor Net class.

by calling update(). Previously cached forward pass activation data should then be cleared with a call to clearSavedData().

layer.h This file contains the Layer class, which serves as the base class for all the different types of layer classes in the framework. It contains virtual methods forward() and backward(), representing the forward and backward pass functionality that must be implemented. All layer classes must also contain a getType() method to identify the layer type, as well as methods for updateWeights(), clearData(), and getOutput().

convolutional {.cpp, .h} These files contain the definition of the ConvLayer class, which implements a 2D-convolutional layer. A unique method to the ConvLayer class is the getWindowPixels() method, which returns the pixels inside the filter window. The class' constructor and key parameters are described in table 3.2.

 $fully connected \{.cpp,.h\}$   $pooling \{.cpp,.h\}$ 

neuron{.cpp, .h}

10 Software Model

Name	Type	Description
dim	uint32_t	Dimensions of the input. The dimension is as-
		sumed square, meaning that rows = dim and
		columns = dim.
filt_size	uint32_t	Dimension of the filter used for the convolution,
		dimension also assumed square.
stride	uint32_t	Size of the stride
padding	uint32_t	Padding used for convolution.
in_channels	uint32_t	Amount of channels in the input.
out_channels	uint32_t	Amount of channels in the output.

 ${\bf Table~3.2:~Description~of~parameters~for~the~ConvLayer~class.}$ 

#### 3.5 Testing

#### 3.6 Usage

#### $_{\text{Chapter}}$ 4

# Hardware Model and Implementation

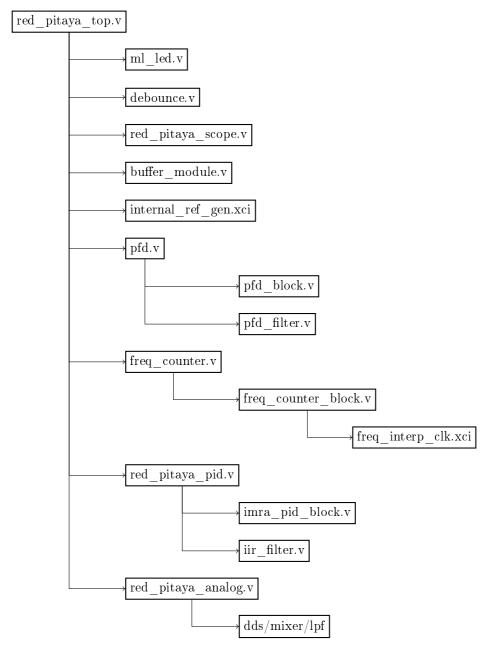


Figure 4.1: Hierarchy of the FPGA code.

## Chapter 5

# Hardware Model Testing

## Chapter 6

## Results

16 Results

## Chapter 7

# **Analysis**

18 Analysis

## CHAPTER 8

## Future Work

20 Future Work

#### Chapter 9

## Discussion

22 Discussion

#### CHAPTER 10

# Conclusion

24 Conclusion

## Appendix A

## Stuff

This appendix is full of stuff  $\dots$ 

26 Stuff

# Bibliography