

INF 5170: Models of Concurrency

Fall 2025

Group Session 2

04.09.2024

Topic: Synchronization, Critical Sections

Exercise 1 ([1, Exercise 2.17]) Consider the following program.

```
1 co  
2   <await (x ≥ 3) x := x - 3> # P1  
3   ||  
4   <await (x ≥ 2) x := x - 2> # P2  
5   ||  
6   <await (x = 1) x := x + 5> # P3  
7 oc
```

For which initial values of x does the program terminate (under weakly fair scheduling)?
What are the corresponding final values? Explain your answer.

Exercise 2 ([1, Exercise 2.18]) Consider the following program.

```
1 co  
2   <await (x > 0) x := x - 1;> # P1  
3   ||  
4   <await (x < 0) x := x + 2;> # P2  
5   ||  
6   <await (x = 0) x := x - 1;> # P3  
7 oc
```

For which initial values of x does the program terminate (under weakly fair scheduling)?
What are the corresponding final values? Explain your answer.

Exercise 3 ([1, Exercise 2.33]) Consider the following program.

```
1 int x := 10;  
2 bool c := true;  
3 co  
4   <await (x = 0)>; c := false # P1  
5  
6   ||  
7   while (c) <x := x - 1> # P2  
8  
9 oc
```

Do we have:

1. Termination under weak fairness?
2. Termination under strong fairness?
3. Add the following while statement as a 3rd branch of the **co**-statement:

```

1  while (c) { if (x < 0)  <x := 10> ;}  # P3
2

```

Do we now have:

- (a) Termination under weak fairness?
- (b) Termination under strong fairness?

Exercise 4 ([1, Exercise 3.1]) Listing 1 shows Dekker's algorithm, a solution to the critical section problem for two processes. Are the following properties satisfied?

1. Mutual exclusion
2. Absence of deadlock
3. Absence of unnecessary delay
4. Eventual entry

How many times can one process that wants to enter its critical section be bypassed by the other before the first gets in?

Listing 1: Dekker's algorithm

```

1  bool enter1 := false, enter2 := false;
2  int turn := 1;
3  process P1{
4    while (true){
5      enter1 := true           # entry protocol
6      while(enter2){
7        if(turn = 2){
8          enter1 := false;
9          while(turn = 2) skip;
10         enter1 := true;
11       }
12     }
13     critical section;
14     enter1 := false;         # exit protocol
15     turn := 2;
16     non-critical section;
17   }
18 }
19 process P2{
20   while (true){
21     enter2 := true           # entry protocol
22     while(enter1){
23       if(turn = 1){
24         enter2 := false;
25         while(turn = 1) skip;
26         enter2 := true;
27       }
28     }
29     critical section;
30     enter2 := false;         # exit protocol
31     turn := 1;
32     non-critical section;
33   }
34 }

```

Exercise 5 ([1, Exercise 3.7]) Consider the following code snippet (Lamport [2]).

```

1  int lock := 0;
2  process CS[i = 1 to n]{
3      while(true){
4          <await (lock = 0)>;
5          lock := i;
6          Delay;
7          while(lock != i){
8              <await (lock = 0)>;
9              lock := i;
10             Delay;
11         }
12         critical section;
13         lock := 0;
14         non-critical section;
15     }
16 }
```

1. Suppose the delay code is deleted. Are the following properties satisfied?
 - (a) Mutual exclusion
 - (b) Absence of deadlock
 - (c) Absence of unnecessary delay
 - (d) Eventual entry
2. Suppose the Delay code spins for long enough to ensure that every process *i* that waits for lock to be 0 has time to execute the assignment statement that sets lock to *i*. Reconsider your answers under that circumstances.

Exercise 6 ([1, Exercise 3.8]) Suppose your machine has the following atomic instructions.

```

1  flip (lock)
2  <lock := (lock + 1) % 2;      # flip the lock
3  return (lock);>             # return the new value
```

Someone suggests the following solution to the critical section problem for two processes.

```

1  int lock := 0;                # shared variable
2  process CS[i = 1 to 2]{
3      while(true){
4          while(flip(lock) != 1)
5              { while(lock != 0) skip; }
6          critical section;
7          lock := 0;
8          non-critical section;
9      }
10 }
```

1. Spot the defect in the code, violating mutual exclusion. That is, give an execution order that results in both processes being in their critical sections at the same time.
2. Suppose that the first line in the body of `flip` is changed to do addition modulo 3 instead of modulo 2. Will the solution now ensure mutual exclusion for two processes?

References

- [1] G. R. Andrews. *Foundations of Multithreaded, Parallel, and Distributed Programming*. Addison-Wesley, 2000.
- [2] L. Lamport. A fast mutual exclusion algorithm. *ACM Transactions on Computer Systems*, 5(1), 1987.