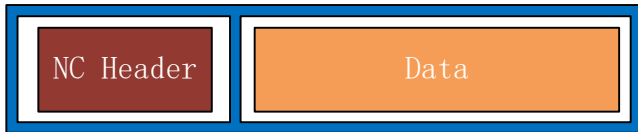
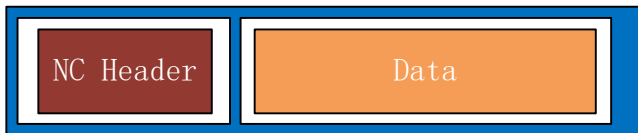


p_1  $\longleftrightarrow len_k \longrightarrow$ p_k  p_n 

2

2

4

4

4

4

源端口

目的端口

Base

Start

RecvWnd

NCACK

n

WS

len1

 α_1

len2

 α_2

...