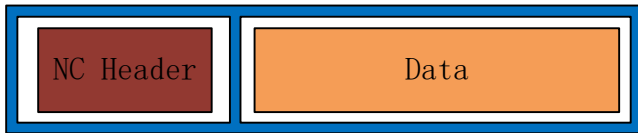
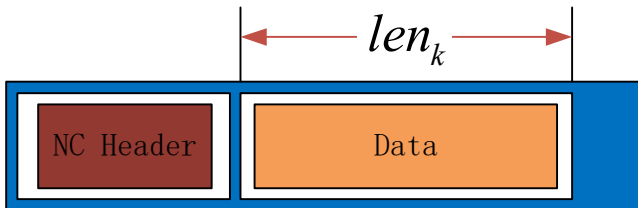
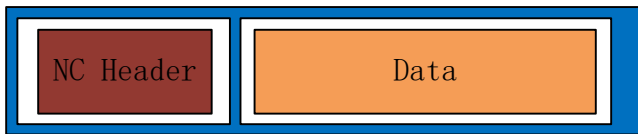


p_1  p_k  p_n 

2

2

1

4

4

2

1

2

1

源端口

目的端口

n

Base

Start

len1

 α_1

len2

 α_2

...