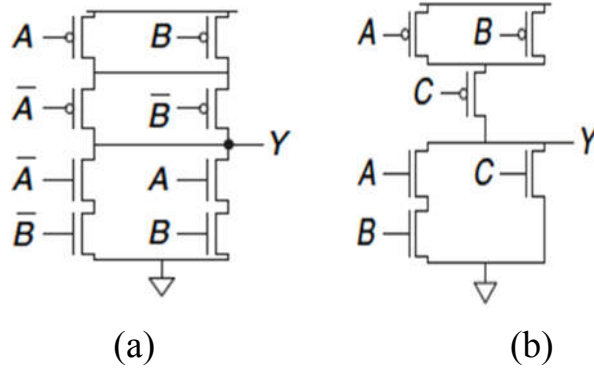# Logic Design - Homework 2

(a)   (b)

(1) Write a truth table for the function performed by each gate in the figures above.

(2) Sketch a transistor-level circuit for the following CMOS gates.
(a) Two-input OR gate
(b) Three-input AND gate