

**EGE UNIVERSITY**  
**LOGIC DESIGN LABORATORY**  
**EXPERIMENT-2**

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**Logic Diagrams of Boolean Functions**

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**EXPERIMENTAL WORK**

Draw the logic diagrams of the following Boolean functions. DO NOT minimize the given Boolean functions.

- $F1(X, Y, Z) = XYZ' + XY' + X'Z$
- $F2(X, Y, Z) = X + X'(X' + Y) + (Y' + Z)$

Implement these functions using 7408 AND, 7432 OR and 7404 NOT gates. Use switches for the inputs and connect the output to a led.

