

**EGE UNIVERSITY**  
**LOGIC DESIGN LABORATORY**  
**EXPERIMENT-6**

**Arithmetic Circuits**

**EXPERIMENTAL WORK**

Design an arithmetic circuit that **multiplies** two bit numbers  $a_1a_0$  and  $b_1b_0$ . You will need two **half adders** for the addition operations. Connect the circuit and check its operation.

	$a_1$	$a_0$
<b>x</b>	$b_1$	$b_0$
	$a_1b_0$	$a_0b_0$
$a_1b_1$	$a_0b_1$	
$a_1b_1$	$a_1b_0 + a_0b_1$	$a_0b_0$

**Required Equipment:** You can use any gate you want to design the circuit: **7408** AND, **7432** OR, **7404** NOT, **7400** NAND, **7402** NOR and **7486** XOR gates. Use switches for the inputs and LEDs for the outputs.

