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| Name: | Edward Sayers |
| **WW:** | **12/13-12/19/2014** |
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1. **Plan:** Meet with capstone team and research GPS jamming methods
2. **Actual Progress:**

* Met with team and discussed pre-term plans
* Started reviewing documents posted to the Wiki

1. **Plan for the upcoming week:**

* Continue reading documents
* Research capabilities of the FPGA board

1. **Miscellaneous Information:**

* None