

```

18 .wrap_target
19 bitloop:
20   out x, 1      side 0 [T3 - 1] ; Side-set still takes place when instruction stalls
21   jmp !x do_zero side 1 [T1 - 1] ; Branch on the bit we shifted out. Positive pulse
22 do_one:
23   jmp bitloop  side 1 [T2 - 1] ; Continue driving high, for a long pulse
24 do_zero:
25   nop           side 0 [T2 - 1] ; Or drive low, for a short pulse
26 .wrap

```

$T_1 = 2$
 $T_2 = 5$
 $T_3 = 3$

