	ADDRESS	OFFSET	NAME	VALUE	Notes
PIO0_BASE	0x50200000	NA	Base		
CTRL	0x50200000	0x000		reserved,0x0,0x0,0x0	NO RESET
FSTAT	0x50200004	0x004		reserved, 0,0,0,1	FIFO FULL
FDEBUG	0x50200008	0x008		reserved, 0,reserved,0,reserved,0,reserved,0	NO OVERFLOW/UNDERFLOW
FLEVEL	0x5020000c	0x00c		0,0,0,0,0,0,1,0	RX0 HIGH
TXF0	0x50200010	0x010		0	pio_sm_put_blocking
TXF1	0x50200014	0x014		0	NO TRANSMIT
TXF2	0x50200018	0x018		0	NO TRANSMIT
TXF3	0x5020001c	0x01c		0	NO TRANSMIT
RXF0	0x50200020	0x020		1	RX0 HIGH
RXF1	0x50200024	0x024		0	NO USE
RXF2	0x50200028	0x028		0	NO USE
RXF3	0x5020002c	0x02c		0	NO USE
IRQ	0x50200030	0x030		reserved,0	NO CLEAR
IRQ_FORCE	0x50200034	0x034		reserved,0	NO FORCE
INPUT_SYNC_BYPASS	0x50200038	0x038		0	DEFAULT
DBG_PADOUT	0x5020003c	0x03c		0,0,0,0,0,0,0,0,0,0,1,1,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0	GPIO 11 AND 12
DBG_PADOE	0x50200040	0x040		0,0,0,0,0,0,0,0,0,0,1,1,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0	GPIO 11 AND 12
DBG_CFGINFO	0x50200044	0x044		reserved,0,reserved,4,16	4 SM PER PIO
INSTR_MEM0	0x50200048	0x048		reserved,0	WRITE NOT ENABLED
INSTR_MEM1	0x5020004c	0x04c		reserved,0	WRITE NOT ENABLED
INSTR_MEM2	0x50200050	0x050		reserved,0	WRITE NOT ENABLED
INSTR_MEM3	0x50200054	0x054		reserved,0	WRITE NOT ENABLED
INSTR_MEM4	0x50200058	0x058		reserved,0	WRITE NOT ENABLED
INSTR_MEM5	0x5020005c	0x05c		reserved,0	WRITE NOT ENABLED
INSTR_MEM6	0x50200060	0x060		reserved,0	WRITE NOT ENABLED
INSTR_MEM7	0x50200064	0x064		reserved,0	WRITE NOT ENABLED
INSTR_MEM8	0x50200068	0x068		reserved,0	WRITE NOT ENABLED
INSTR_MEM9	0x5020006c	0x06c		reserved,0	WRITE NOT ENABLED
INSTR_MEM10	0x50200070	0x070		reserved,0	WRITE NOT ENABLED
INSTR_MEM11	0x50200074	0x074		reserved,0	WRITE NOT ENABLED
INSTR_MEM12	0x50200078	0x078		reserved,0	WRITE NOT ENABLED
INSTR_MEM13	0x5020007c	0x07c		reserved,0	WRITE NOT ENABLED
INSTR_MEM14	0x50200080	0x080		reserved,0	WRITE NOT ENABLED
INSTR_MEM15	0x50200084	0x084		reserved,0	WRITE NOT ENABLED
INSTR_MEM16	0x50200088	0x088		reserved,0	WRITE NOT ENABLED
INSTR_MEM17	0x5020008c	0x08c		reserved,0	WRITE NOT ENABLED
INSTR_MEM18	0x50200090	0x090		reserved,0	WRITE NOT ENABLED
INSTR_MEM19	0x50200094	0x094		reserved,0	WRITE NOT ENABLED
INSTR_MEM20	0x50200098	0x098		reserved,0	WRITE NOT ENABLED
INSTR_MEM21	0x5020009c	0x09c		reserved,0	WRITE NOT ENABLED
INSTR_MEM22	0x502000a0	0x0a0		reserved,0	WRITE NOT ENABLED
INSTR_MEM23	0x502000a4	0x0a4		reserved,0	WRITE NOT ENABLED
INSTR_MEM24	0x502000a8	0x0a8		reserved,0	WRITE NOT ENABLED
INSTR_MEM25	0x502000ac	0x0ac		reserved,0	WRITE NOT ENABLED
INSTR_MEM26	0x502000b0	0x0b0		reserved,0	WRITE NOT ENABLED
INSTR_MEM27	0x502000b4	0x0b4		reserved,0	WRITE NOT ENABLED
INSTR_MEM28	0x502000b8	0x0b8		reserved,0	WRITE NOT ENABLED
INSTR_MEM29	0x502000bc	0x0bc		reserved,0	WRITE NOT ENABLED
INSTR_MEM30	0x502000c0	0x0c0		reserved,0	WRITE NOT ENABLED
INSTR_MEM31	0x502000c4	0x0c4		reserved,0	WRITE NOT ENABLED
SM0_CLKDIV	0x502000c8	0x0c8		0,0,reserved	65536 FREQ
SM0_EXECCTRL	0x502000cc	0x0cc		0(25-bit),reserved,0(5-bit)	NA
SM0_SHIFTCTRL	0x502000d0	0x0d0		0(16-bit), reserved	NA
SM0_ADDR	0x502000d4	0x0d4		reserved,0(5-bit)	SM0
SM0_INSTR	0x502000d8	0x0d8		reserved,0	NO READ/WRITE
SM0_PINCTRL	0x502000dc	0x0dc		0	NO IN/OUT EFFECT
SM1_CLKDIV	0x502000e0	0x0e0		0,0,reserved	65536 FREQ
SM1_EXECCTRL	0x502000e4	0x0e4		0(25-bit),reserved,0(5-bit)	NA
SM1_SHIFTCTRL	0x502000e8	0x0e8		0(16-bit), reserved	NA
SM1_ADDR	0x502000ec	0x0ec		0(16-bit), reserved	SM0
SM1_INSTR	0x502000f0	0x0f0		reserved,0	NO READ/WRITE
SM1_PINCTRL	0x502000f4	0x0f4		0	NO IN/OUT EFFECT
SM2_CLKDIV	0x502000f8	0x0f8		0,0,reserved	65536 FREQ
SM2_EXECCTRL	0x502000fc	0x0fc		0(25-bit),reserved,0(5-bit)	NA
SM2_SHIFTCTRL	0x50200100	0x100		0(16-bit), reserved	NA
SM2_ADDR	0x50200104	0x104		0(16-bit), reserved	SM0
SM2_INSTR	0x50200108	0x108		reserved,0	NO READ/WRITE
SM2_PINCTRL	0x5020010c	0x10c		0	NO IN/OUT EFFECT
SM3_CLKDIV	0x50200110	0x110		0,0,reserved	65536 FREQ
SM3_EXECCTRL	0x50200114	0x114		0(25-bit),reserved,0(5-bit)	NA
SM3_SHIFTTCTRL	0x50100118	0x118		0(16-bit), reserved	NA

Register	Address	Offset	Reset Value	Access
SM3_ADDR	0x5020011c	0x11c	0(16-bit), reserved	SM0
SM3_INSTR	0x50200120	0x120	reserved,0	NO READ/WRITE
SM3_PINCTRL	0x50200124	0x124	0	NO IN/OUT EFFECT
INTR	0x50200128	0x128	reserved,0,0,0,1,0,0,0,0,0,0,0,0	SM0 USE
IEQ0_INTE	0x5020012c	0x12c	reserved, 0,0,0,1,0,0,0,0,0,0,0,0	SM0 USE
IEQ0_INTF	0x50200130	0x130	reserved, 0,0,0,1,0,0,0,0,0,0,0,0	SM0 USE
IRQ0_INTS	0x50200134	0x134	reserved, 0,0,0,1,0,0,0,0,0,0,0,0	SM0 USE
IEQ1_INTE	0x50200138	0x138	reserved, 0,0,0,1,0,0,0,0,0,0,0,0	SM0 USE
IEQ1_INTF	0x5020013c	0x13c	reserved, 0,0,0,1,0,0,0,0,0,0,0,0	SM0 USE
IEQ1_INTS	0x50200140	0x140	reserved, 0,0,0,1,0,0,0,0,0,0,0,0	SM0 USE