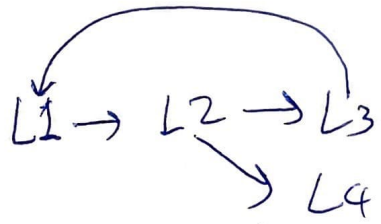


```

1 .wrap target
2 bitloop:
3   out X, 1 side 0 [T3-1]
4   jmp !X do_zero side 1 [T1-1]
5 do_one:
6   jmp bitloop side 1 [T2-1]
7 do_zero:
8   nop side 0 [T2-1]
9 .wrap

```

Current Instruction	is data in Fifo	no. of delay cycles	Value of OSR Register	Value of sm(x)	is sm stalled	State of LED Pin
L1	y	2	0x007f8000	0	n	0
L2	y	1	0x007f8000	0	n	1
L3	y	4	0x007f8000	0	n	0
L4	y	2	0x003f6000	0	n	0



Program loops using .wrap target & .wrap

