

```

/**
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 *
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 */

#include <stdio.h>
#include <stdlib.h>
#include <boards/adafruit_qtpy_rp2040.h>

#include "pico/stdlib.h"
#include "hardware/pio.h"
#include "hardware/clocks.h"
#include "ws2812.pio.h"

#define IS_RGBW true
#define NUM_PIXELS 150 → no. of pixels
#define PICO_DEFAULT_WS2812_PIN → Macro for NEOPixel DATA PIN
#define WS2812_PIN PICO_DEFAULT_WS2812_PIN
#else
// default to pin 2 if the board doesn't have a default WS2812 pin
defined
#define WS2812_PIN 11
#endif

36 static inline void put_pixel(uint32_t pixel_grb) {
    pio_sm_put_blocking(pio0, 0, pixel_grb << 8u); This function uses
} PIO to output data
to the Neopixel.

static inline uint32_t urgb_u32(uint8_t r, uint8_t g, uint8_t b) {
    return ((uint32_t) (r) << 8) | Takes R,G,B & returns
        ((uint32_t) (g) << 16) | unsigned 32 bit data
        (uint32_t) (b); for NEOPixel
}

void pattern_snakes(uint len, uint t) {
    for (uint i = 0; i < len; ++i) {
        uint x = (i + (t >> 1)) % 64;
        if (x < 10)
            put_pixel(urgb_u32(0xff, 0, 0));
        else if (x >= 15 && x < 25)
            put_pixel(urgb_u32(0, 0xff, 0));
        else if (x >= 30 && x < 40)
            put_pixel(urgb_u32(0, 0, 0xff));
    }
}

```

Header files of libraries to be needed.

36

This function uses PIO to output data to the Neopixel.

Takes R,G,B & returns unsigned 32 bit data for NEOPixel

```

        else
            put_pixel(0);
    }
}

void pattern_random(uint len, uint t) {
    if (t % 8)
        return;
    for (int i = 0; i < len; ++i)
        put_pixel(rand()); → generate a random no. (32 bit)
} & pass it in put_pixel function
34 void pattern_sparkle(uint len, uint t) {
    if (t % 8)
        return;
    for (int i = 0; i < len; ++i)
        put_pixel(rand() % 16 ? 0 : 0xffffffff);
}

void pattern_greys(uint len, uint t) {
    int max = 100; // let's not draw too much current!
    t %= max;
    for (int i = 0; i < len; ++i) {
        put_pixel(t * 0x10101);
        if (++t >= max) t = 0;
    }
}

typedef void (*pattern)(uint len, uint t);
const struct {
    pattern pat;
    const char *name;
} pattern_table[] = {
    {pattern_snakes, "Snakes!"},
    {pattern_random, "Random data"},
    {pattern_sparkle, "Sparkles"},
    {pattern_greys, "Greys"},
};

int main() {
    //set_sys_clock_48();
    1 int gpio = PICO_DEFAULT_WS2812_POWER_PIN; → Assigning GPIO for LED power pin from MACRO
    2 gpio_init(gpio);
    3 gpio_set_dir(gpio, GPIO_OUT); Set O/P
    4 gpio_set_outover(gpio, GPIO_OVERRIDE_HIGH); → override output to high.
    5 stdio_init_all(); → initializes modules
}
→ pattern table defining names of the function.

```

```
printf("WS2812 Smoke Test, using pin %d", WS2812_PIN);

// todo get free sm
PIO pio = pio0; → Declare an object pio, with PIO instance 0
int sm = 0; → declare variable for state machine
uint offset = pio_add_program(pio, &ws2812_program);
    ↳ Loads PIO into PIO instruction memory
ws2812_program_init(pio, sm, offset, WS2812_PIN, 800000, IS_RGBW);
    ↳ Initializes WS2812 program as PIO.

int t = 0;
while (1) { → Endless loop
    27 int pat = rand() % count_of(pattern_table);
    28 int dir = (rand() >> 30) & 1 ? 1 : -1;
    29 puts(pattern_table[pat].name); → writes a character to
    30 puts(dir == 1 ? "(forward)" : "(backward)"); → PIO stream
    31 for (int i = 0; i < 1000; ++i) { for loop
    32     pattern_table[pat].pat(NUM_PIXELS, t);
    33     sleep_ms(10); delay
    34     t += dir;
    35 }
}
```

Increase or  
decrease  
value of  $t$   
with our  
value -1 or  
+1

Call the  
functions to  
which  
pointer  
pattern  
is pointing  
to & pass  
parameter  
(o - no. of  
pixels & t

If  
dir = 1  
dir =  
forward  
else  
backward

generate a random number, right shift by 30.  
rd Bwise AND with 01-  
If true  
div = 1  
else  
div = -1

map a  
random  
no. to range  
of count  
of patterns  
ie 0 to 3

Let us now assume that the pattern being called is pattern-random.

```

// ----- //
// This file is autogenerated by pioasm; do not edit! //
// ----- //

#pragma once

#if !PICO_NO_HARDWARE
#include "hardware/pio.h" → Include hardware
#endif                                           marker

// ----- //
// ws2812 //
// ----- //

#define ws2812_wrap_target 0
#define ws2812_wrap 3

#define ws2812_T1 2
#define ws2812_T2 5
#define ws2812_T3 3

static const uint16_t ws2812_program_instructions[] = {
    // .wrap_target
    0x6221, // 0: out    x, 1           side 0 [2]
    0x1123, // 1: jmp    !x, 3          side 1 [1]
    0x1400, // 2: jmp    0             side 1 [4]
    0xa442, // 3: nop           side 0 [4]
    // .wrap
};

#if !PICO_NO_HARDWARE
static const struct pio_program ws2812_program = {
    .instructions = ws2812_program_instructions,
    .length = 4,
    .origin = -1,
};

static inline pio_sm_config ws2812_program_get_default_config(uint
offset) {
    pio_sm_config c = pio_get_default_sm_config(); get the default S.M.
    sm_config_set_wrap(&c, offset + ws2812_wrap_target, offset + config
    ws2812_wrap); → Set the wrap
    sm_config_set_sideset(&c, 1, false, false); address
    return c; configurations
    1b
    set the current GPIO pins for SM config.

```

```
}
```

```
#include "hardware/clocks.h"
static inline void ws2812_program_init(PIO pio, uint sm, uint offset,
uint pin, float freq, bool rgbw) {
    pio_gpio_init(pio, pin); >Selects GPIO to use OIP from PIO instance
    pio_sm_set_consecutive_pindirs(pio, sm, pin, 1, true); Set pin directions consecutively
    pio_sm_config c = ws2812_program_get_default_config(offset);
    sm_config_set_sideset_pins(&c, pin);
    sm_config_set_out_shift(&c, false, true, rgbw ? 32 : 24);
    sm_config_set_fifo_join(&c, PIO_FIFO_JOIN_RX);
    int cycles_per_bit = ws2812_T1 + ws2812_T2 + ws2812_T3;
    float div = clock_get_hz(clk_sys) / (freq * cycles_per_bit);
    sm_config_set_clkdiv(&c, div); Set the SM clock divider
    pio_sm_init(pio, sm, offset, &c);
    pio_sm_set_enabled(pio, sm, true); } #endif
```

Enable the PIO State machine

Load the configuration in the state machine & go to start address

Set the speed of execution of the state machine

Declare no. of cycles used

Used to manipulate FIFO for DMA  
It sets up the FIFO to join SM config.