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INTERFACING GUIDE

FOR

ICOM MODEL FD3700 SERIES FLOPPY DISK SYSTEMS

ICOM FRUGAL FLOPPYTM MODEL FF37

AND

CONTROLLER/FORMATTER MODEL CF3700

April, 1977

NOTICE

No information contained within this document may be reproduced, distributed or disclosed in full or in part without the prior written authorization of the iCOM Division of Pertec Computer Corporation.

3-13 SIGNAL FLOW DIAGRAM

Figure 3-4 presents the signal flow of a disk drive system, and the CF3700 in particular. Signal flow is from left to right. Thus, an item may appear more than once to illustrate input and output signals. The outputs of Z2 are referenced back to Z1 as inputs.

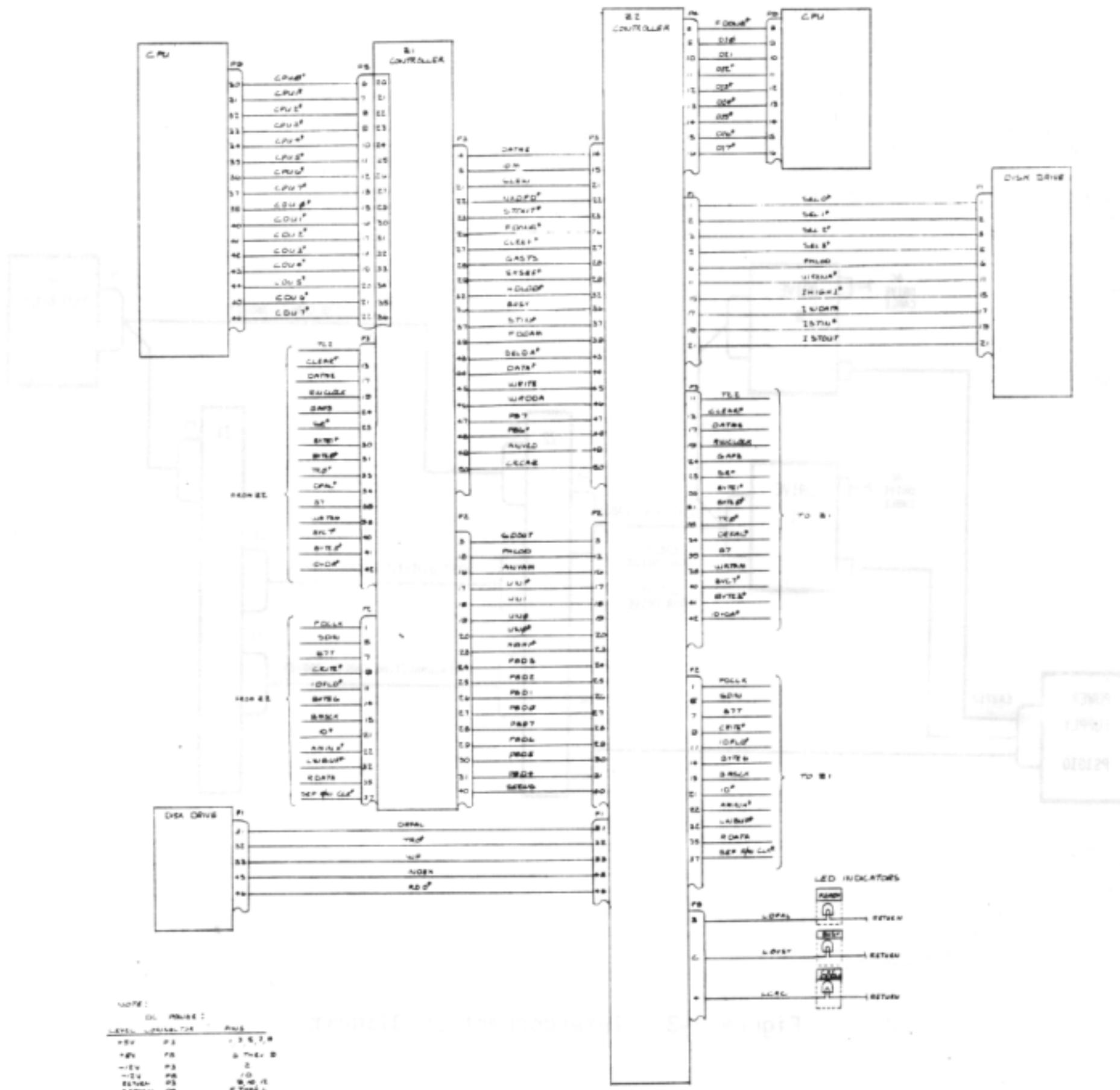


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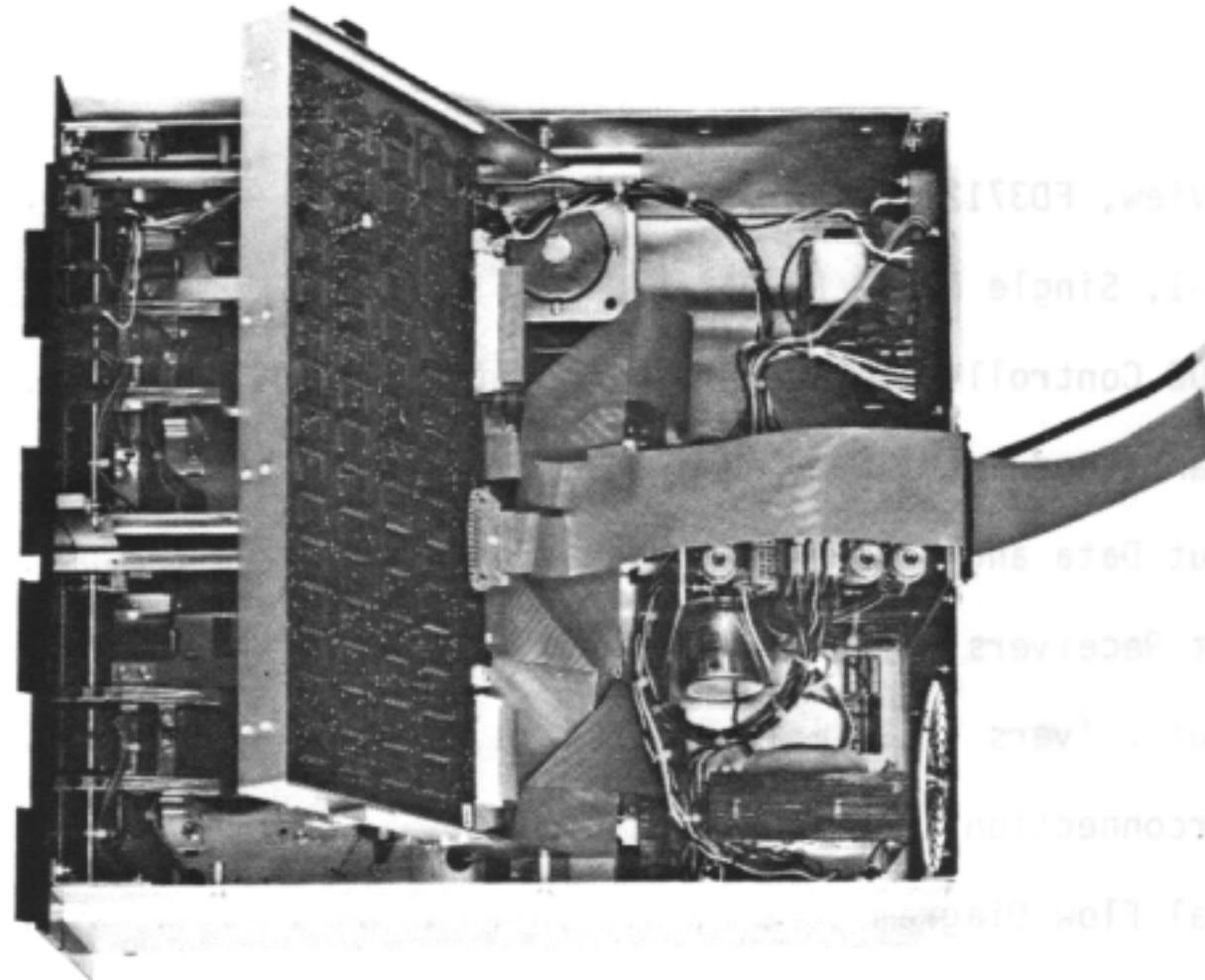


Figure 1-1. Top View, FD3712 Dual Drive
(cover removed and card cage elevated)

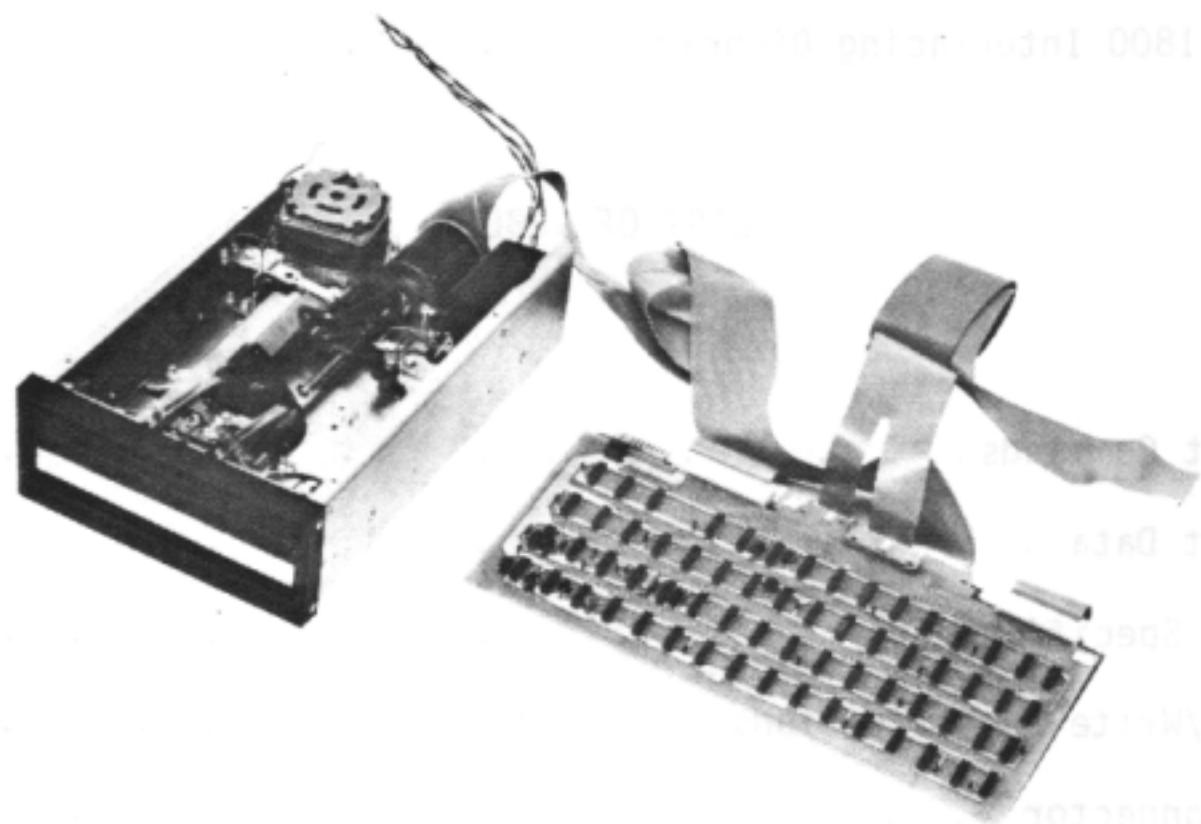


Figure 1-2. FF37-1, Single Drive Frugal Floppytm

SECTION I

GENERAL DESCRIPTION

1-1 INTRODUCTION

The Model CF3700 Floppy Disk Controller/Formatter is used in the iCOM FD3700 and FF37 series floppy disk systems (refer to figures 1-1 and 1-2). These floppy disk systems employ Pertec Model 511 AC driven disk drives. The controller is designed for easy integration into commercial, industrial, and developmental applications.

The CF3700 is capable of driving up to four floppy disk drives. Within the controller is a general purpose interface, which is compatible with most minicomputers and microcomputers. The Controller/Formatter consists of two printed circuit boards, designated Z1 and Z2. Together, they provide the necessary features for successful and convenient operation of a powerful floppy disk system.

1-2 FEATURES

The CF3700 offers many features which reduce computer service overhead. For example, the controller is compatible with IBM 3540 and 3740 data formats. All formatting and defragmenting is done automatically by the controller. Also, the controller automatically performs track, seek, and verification as well as cycle redundancy check generation and verification.

Programmed I/O or DMA (direct memory access) operation may be easily accommodated by the independent input and output buffers. Each buffer can hold one full sector consisting of 128 data bytes.

Interface signals between the CF3700 and the CPU/MPU are comprised of independent input and output data lines as well as an eight bit parallel control port. Controller status indications are routed onto the CPU input data lines when the appropriate input command is received from the CPU.

1-3 PHYSICAL DESCRIPTION

The CF3700 Controller/Formatter consists of two printed circuit boards, Z1 and Z2, that contain a total of 127 integrated circuits. The boards each measure 184 X 381 mm (7.25 X 15 in). All connectors are along one edge of the boards, making the need for card cages or back plane wiring unnecessary. The controller and its optional cables are detailed in figure 1-3.

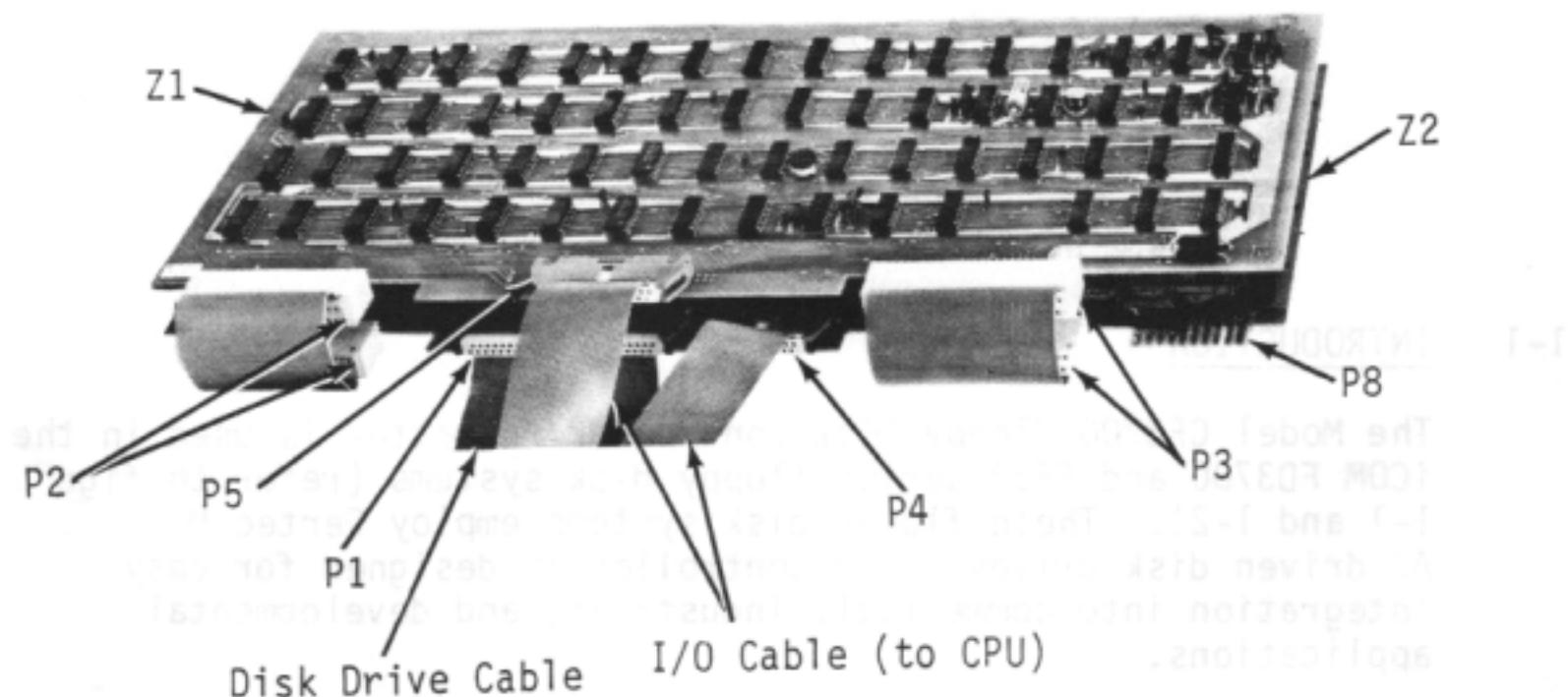


Figure 1-3. CF3700 Controller/Formatter, With Optional Cables.

1-4 POWER REQUIREMENTS

1-5 CF3700 CONTROLLER/FORMATTER

+5V DC, \pm 5% at 3 amps maximum

-12V DC, \pm 5% at .25 amps maximum

1-6 DISK DRIVE

The CF3700 is designed to be used with Pertec Model FD511 floppy disk drives.

The FD511 DC power requirements are:

+5 \pm .25V at 1.4 amps maximum

+24 \pm 1V at 1.4 amps maximum

-5 or -12 \pm .25V at 0.3 amps maximum

AC input options:

90-130 VAC, 60Hz, 32 watts maximum

90-120 VAC, 50 Hz, 33 watts maximum (special order)

190-250 VAC, 50 Hz, 28 watts maximum

SECTION II

OPERATIONAL DESCRIPTION

2-1 INPUT COMMANDS

The CF3700 controller receives commands on the CPU1* thru CPU7* data lines of connector P5 on the Z1 circuit board.

Table 2-1. Input Commands

Command	CPU Bit (Negative True)								Hex Code
	7	6	5	4	3	2	1	0	
Examine Status	0	0	0	0	0	0	0	0	00
Read	0	0	0	0	0	0	1	1	03
Write	0	0	0	0	0	1	0	1	05
Read CRC	0	0	0	0	0	1	1	1	07
Seek	0	0	0	0	1	0	0	1	09
Clear Error Flags	0	0	0	0	1	0	1	1	0B
Seek Track \emptyset	0	0	0	0	1	1	0	1	0D
Write with DDAM*	0	0	0	0	1	1	1	1	0F
Load Track Address	0	0	0	1	0	0	0	1	11
Load Unit/Sector	0	0	1	0	0	0	0	1	21
Load Write Buffer	0	0	1	1	0	0	0	1	31
Examine Read Buffer	0	1	0	0	0	0	0	0	40
Shift Read Buffer	0	1	0	0	0	0	0	1	41
Clear	1	0	0	0	0	0	0	1	81

* DDAM = Deleted Data Address Mark

2-2 INPUT DATA

Data outputs from the CPU are routed through the controller data lines CD00 thru CD07, connector P5 of the Z1 circuit board.

Table 2-2. Input Data

	CD0 Bit								Range
	7	6	5	4	3	2	1	0	
Track Address	0	MSB						LSB	00 to 4C (Hex)
Unit/Sector	Unit	MSB	LSB	0	MSB	Sector	LSB		Unit 0-3 Sector 1-1A (Hex)
Write Buffer	MSB							LSB	00 to FF (Hex)

2-3 OUTPUT DATA

The CF3700 output data to the CPU are DI0 thru DI7, present on connector P4 of Z2. When command bit six (CPU6*) is a one, these data lines contain status information.

2-4 OPERATIONAL SEQUENCE

The following command sequence may be used as guide in developing disk system application software. Numbers within parenthesis are in hex notation.

2-5 SEEK

To perform a Seek operation:

1. Set up CD00 thru CD07 with Unit and Sector.
2. Execute Load Unit/Sector (21). If track is different, set Track Address on DC00 thru CD07.
3. Execute Load Track Address (11).
4. Execute Seek (09).
5. Loop-on-busy by:
Executing Examine Status
Input data, check Busy (0 when completed) and CRC
bits.

2-6 SEEK TRACK 0

To perform Seek Track 0 operation:

Use in power-up.
Track address not necessary.

1. Execute Seek Track 0 (0D).
2. Loop-on busy as in Seek operation.

2-7 READ

To perform a Read operation:

1. Seek to correct unit, track, and sector.
2. Execute Read (03).
3. Check CRC. Re-read, if DI number is a one.
4. Execute Examine Read Buffer (40).
5. Enter input character.
6. Shift Read Buffer (41).

NOTE

Continue the entering of characters and the incrementing of the Shift Read Buffer, until 128 characters have been entered.

2-8 WRITE

To perform a Write operation:

1. Enter data CDO0 thru CD07.
 2. Execute Load Write Buffer (31).
 3. Repeat previous steps until 128 characters have been loaded.
 4. Seek to Unit, Track, and Sector.
 5. Execute Write (05).
 6. Execute Read CRC (07).
 7. Check CRC Bit, repeat steps 5 and 6, if CRC is one.

NOTE

Write Buffer recirculates, so it is not necessary to reload data to rewrite.

2-9 WRITE WITH DELETED ADDRESS MARK

This command is the same as Write, except that the written data is preceded by a Deleted Data Address Mark (DDAM). When the sector is read, the Found Deleted Data Address Mark Status bit will be set. Thus, this command may be used for data identification. For example, DDAM could be used to indicate the end of a long data field. This would be useful in some editing functions.

2-10 CLEAR ERROR FLAGS

This command is used to clear:

Deleted Data Address Mark
Cyclic Redundancy Check (CRC) status bits.

2-11 CLEAR

Clear halts any process in operation. It also clears Busy and pulses Done.

2-12 OPERATIONAL TIMING

The timing diagram for Commands and Input Data is given by figure 2-1. Figure 2-2 shows Output Data and Status timing. The specifications for Seek and Read/Write operations are presented in tables 2-3 and 2-4.

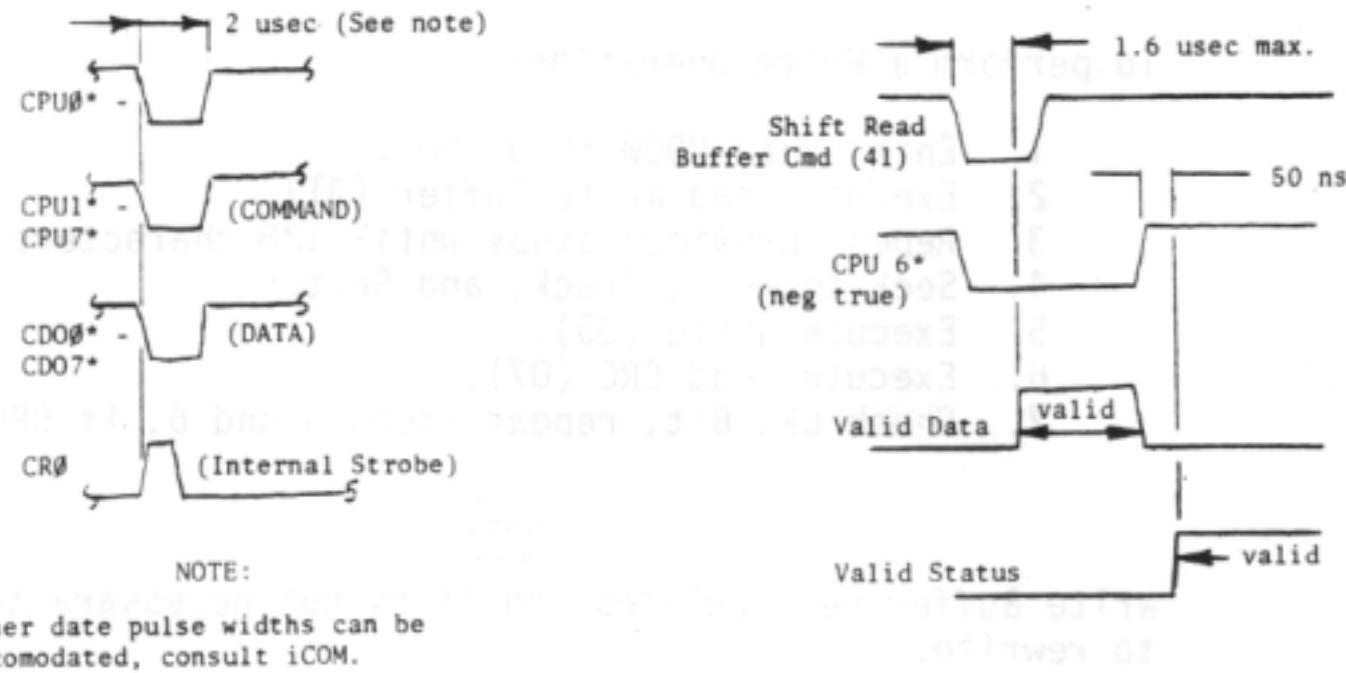


Figure 2-1. Commands and Input Data Timing

Figure 2-2. Output Data and Status Timing

Table 2-3. Seek Specifications

Track to Track	10 msec.
Head Load and Settling Time	40 msec, max.
Maximum seek time for 77 tracks	820 msec.

Table 2-4. Read/Write Specifications

Read/Write Time Per Sector	6 msec.
Latency ($\frac{1}{2}$ revolution)	83 msec.
Read/Write Buffer Shift Rate	500 kHz maximum

2-13 FUNCTIONAL CHARACTERISTICS

4T

2-14 SEEK

This command is used to change tracks on a disk. A Seek operation is begun by the present track location of the disk drive being read by the CF3700. The track location is contained in the ID field that precedes each sector.

The actual and desired track addresses are compared and the disk drive head is stepped in the appropriate direction until it aligns with the desired track. Then, the track address is read again to verify its location. If the head is at its correct location the Seek operation is completed.

In some formats, the track address may not directly correspond to the physical track number due to defective tracks. The Controller/Formatter will handle this by seeking until the correct track address is located.

Seek Track operation is necessary only to change tracks. To change sectors, the Load Unit/Sector command is used.

2-15 SEEK TRACK 0

Seek Track 0 is used to seek to track 0, regardless of present track. A transfer of the Track Address is not required.

A Seek Track 0 should be used during power-up and restart to initialize the disk drive head.

2-16 READ

A Seek must be performed first to bring the head to the desired track, unless the head is already positioned on this track. A Unit/Sector command is executed to select the sector to be read. The ID field that precedes each Data Sector is examined to give the correct sector.

On the standard IBM format diskette, sector one is positioned just following the index hole and sector 26 (1A) just before the index hole.

The CF3700 computes CRC automatically during read operations and if an error is detected, the CRC status bit is set at the end of read.

Data is shifted into the Read Buffer at a 250 kHz rate from the sector. Upon completion of read, the first character of the sector is at the front of the read buffer. Read Buffer command (40) places the Read Buffer output on the Data Input Lines. Shift Read shifts Read Buffer and places Read Buffer output on the Data Input Lines; 127 shifts are required to read the total 128 bytes.

Read CRC tests CRC and does not cause data to be loaded into Read Buffer. This command is often used after a Write to ensure integrity of data. A Write operation is executed and verified without destroying the contents of the Read Buffer. This is an important feature in edit operations and CPU's with limited memory.

2-17 WRITE

Write command operations writes the contents of the Write Buffer to the desired Unit and Sector. The location of the disk drive head is not important.

After Write Buffer is loaded, the Seek command will move the head to the desired track. Then the CF3700 will begin to examine ID field searching for the correct sector.

A field of six bytes, consisting of all logical zeros, now is written ahead of the intended location of the data sector. Next, the Address Mark is written or DDAM, if a Write Deleted Data Address.

The CF3700 generates CRC, consisting of two bytes, at the completion of the Write operation.

2-18 READ CRC

The data that was written should be read by a Read CRC command to ensure the validity of the Write operation.

2-19 STATUS

The seven Status Lines are present on the Data Input Lines (refer to table 2-2) when Command Bit, CPU 6* is 0 (negative true.)



looking into I/O connector

3-7 P5 Connector

P5, the 26 pin connector, mates with P5 of the Z1 board. The output signals from the CPU are routed to P5, the Controller/Formatter input signal connector.

Table 3-1 shows the P9 and P5 connections of the I/O cable.

The cable connector is 3M P/N 3399-0000 or 3399-1000 or equivalent.

NOTE

Reference index of P5 board connector begins at pin 15.

Table 3-1. I/O Cable, P5 Connector

P5 Cable End	P5 on Z1 Board	P9 (CPU end)	Signal	Definition
1-5	15-19		Not used	
2 Key				
5 Key		29 Key		
6	20	30	CPU0*	Command Strobe
7	21	31	CPU1*	Command Word
8	22	32	CPU2*	Command Word
9	23	33	CPU3*	Command Word
10	24	34	CPU4*	Command Word
11	25	35	CPU5*	Command Word
12	26	36	CPU6*	Command Word
13	27	37	CPU7*	Command Word
15	29	39	CPU0*	Data out bit 0
16	30	40	CDU1*	Data out bit 1
17	31	41	CDU2*	Data out bit 2
18	32	42	CDU3*	Data out bit 3
19	33	43	CDU4*	Data out bit 4
20	34	44	CDU5*	Data out bit 5
21	35	45	CDU6*	Data out bit 6
22	36	46	CDU7*	Data out bit 7
23-26	~37-40	47-50	Signal Ground	

5,10,15,20

3-8 P4 Connector

The Connector P4, which has 20 pins, mates with P4 of Z2. The CPU input signals are routed through P4, the Controller/Formatter output signal connector.

The P4 and P9 connectors of the I/O cable are detailed in table 3-2.

The cable connector to P4 of Z2 is 3M P/N 3421-0000 or equivalent.

Table 3-2. P4 Connector

P4	P9 (CPU)	Signal	Definition
1-7	1-7	Not Used	
6 Key			
8	8	F DONE*	Done
A	9	DI0/Busy	Data Input 0
B	10	DI1/UN0	Data Input 1
C	11	DI2/UN1	Data Input 2
D	12	DI3/CRC Error	Data Input 3
E	13	DI4/Write Protected	Data Input 4
F	14	DI5/Drive Fail	Data Input 5
G	15	DI6	Data Input 6
H	16	DI7/Deleted	Data Input 7
I		Data Address Mark	
J	17 Key		
E,J	18-20	GRD	

The Status Line Line signals are:

- 0 Busy
- 1 UN0, Unit Select Code Bit 0
- 2 UN1, Unit Select Code Bit 1
- 3 Media or CRC Error
- 4 Selected Unit Write Protect
- 5 Drive Fail
- 7 Found Deleted Data Address Mark

3-20 BUSY

A one indicates that an operation is in process, zero, no operation in process. Busy is cleared by Clear, Clear Error Flags, or head unloading.

3-21 UN0 AND UN1 UNIT SELECT CODE

The code for selecting the desired disk drive unit is:

<u>UN1</u>	<u>UN0</u>	<u>Unit Selected</u>
0	1	0
0	1	1
1	0	2
1	1	3

3-22 MEDIA OR CRC ERROR

These signals indicate a data error in Seek or Read (CRC) operation. Clear Error Flags, clears line before beginning a Read or Write operation.

3-23 SELECTED UNIT WRITE PROTECT

The disk is write protected when this line is low (logical one).

3-24 DRIVE FAIL

Activates to a logical one when drive is not ready or when drive is not fully operable. This may be due to drive not being up to speed, its door is open, diskette not installed, or drive is not properly connected.

3-25 FOUND DELETED ADDRESS MARK

This bit goes low if DDAM preceeds a Read command. It is reset by the Clear Error Flags command.

SECTION III

INTERFACING REQUIREMENTS

3-1 SIGNAL LEVELS

All signals pertaining to the Controller/Formatter are standard TTL-compatible negative true. Positive true is available as an option for an additional cost.

3-2 CF3700 INPUT SIGNALS

Logic "0": +2.0V min to +5V max

Logic "1": +0.0V min to +0.8V max

Standard load is a TTL gate (7404 plus 680 ohms to +5V). The other two input load variations shown, are extra cost options designed to accomodate special system requirements.

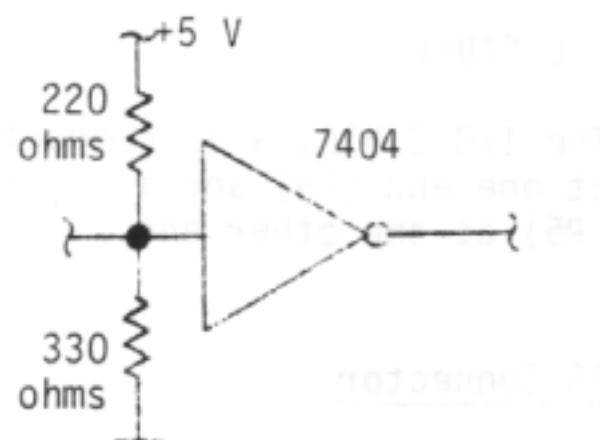
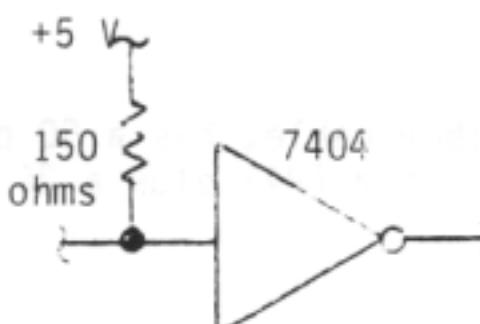
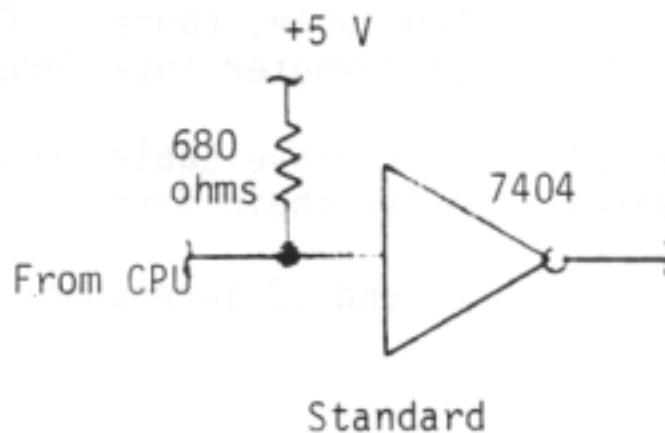


Figure 3-1. Input Receivers

3-3 CF3700 OUTPUT SIGNALS

Logic "0": +2.4V min
Logic "1": +0.0V min to +0.4V max

Current sink at logic "1" is 32 ma

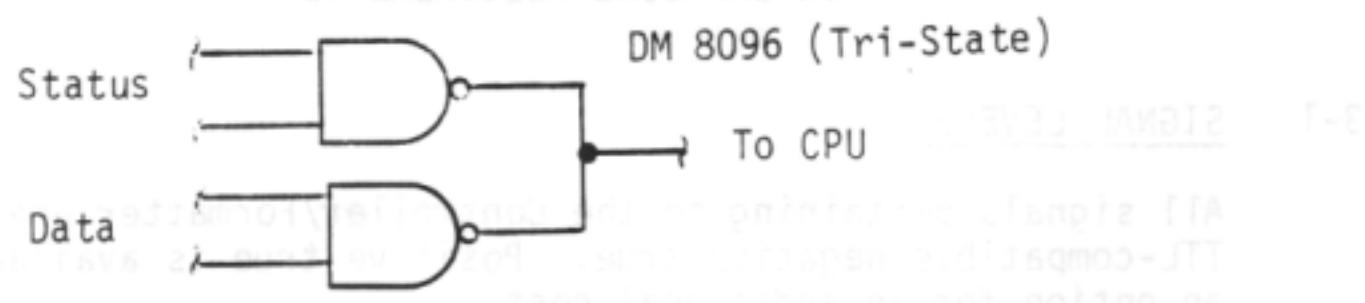


Figure 3-2. Output Drivers

3-4 CABLE INSTALLATION

Four 3M flat cables interconnect the floppy disk system:

P/N	Description
360-27	I/O Cable, connects Controller/Formatter to computer interface.
370-22 (Dual Drive) 370-24 (Four Drive)	Disk Drive Cable, connects drives in a daisy chain fashion to Controller/Formatter.
360-26 and 360-25	Z1 and Z2 Interconnection Cables.

3-5 I/O CABLE

The I/O Cable, a 50 conductor ribbon cable, has a 50 pin connector at one end (P9) and a 20 pin connector (P4) plus a 26 pin connector (P5) at the other end.

3-6 P9 Connector

The 50 pin connector is designated P9 and connects to the interface or CPU of the computer.

The Cable connector is 3M P/N 3433-2002 (solder tail) or 3M P/N 3233-4005 (wire wrap) or equivalent.

The key for this connector is pin 29.

3-9 DISK DRIVE CABLE

The Disk Drive Cable connects the CF3700 to the floppy disk drive. The Controller/Formatter connector is designated P1, and connects to its respective connector on Z2.

Table 3-3. Disk Drive Cable

Pin	Signal	Definition	I/O
1	SEL0*	Drive Select 0	0
2	SEL1*	Drive Select 1	0
3	SEL2*	Drive Select 2	0
5	SEL3*	Drive Select 3	0
6	PHLOD*	Head Load	0
11	WRENA*	Write Enable	0
15	1HIGH*	High Head Current	0
17	1WDATA	Write Data	0
19	1STIN	Step In	0
21	1STOUT	Step Out	0
31	DRFAL	Drive Fail	I
32	TR0*	Track Zero	I
33	WP	Write 1	I
45	INDEX	Index	I
46	RDO*	Read Data	I

Pin 43 is keyed

3-10 Z1 AND Z2 INTERCONNECTING CABLES

These cables connect the two printed circuit boards, Z1 and Z2, of the Controller/Formatter, CF3700. The P2 cable has 40 conductors, while the P3 cable has 50 conductors.

These interconnecting cables provide for signal transfer between the two sections of the Controller/Formatter.

3-11 P8 CONNECTOR

The printed circuit board edge connector, P8, provides for DC power inputs as well as routing the write protect and the status features to the Z2 board. Table 3-4 furnishes the pin and assigned signal information for P8.

Table 3-4. P8 Connector

<u>Pin</u>	<u>Signal</u>	<u>Description</u>	<u>I/O</u>
1	WP3*	Drive 3 Write Protect	I
A	WP2*	Drive 2 Write Protect	I
2	WP1*	Drive 1 Write Protect	I
B	WP0*	Drive 0 Write Protect	I
3	LDRFAL	Ready	O
C	LBUSY	Busy	OS
4	LCRC	CRC Error	OE
D	LWRPR	Write Protect	SE
5	LUN1	Unit Select Bit 1	SE
E	LUN0	Unit Select Bit 0	SE
6	+5V		
F	-12V return		
7	+5V		
H	+5V return		
8	+5V		
J	-12V return		
9	+5V		
K	-12V return		
10	-12V		
L	-12 return		

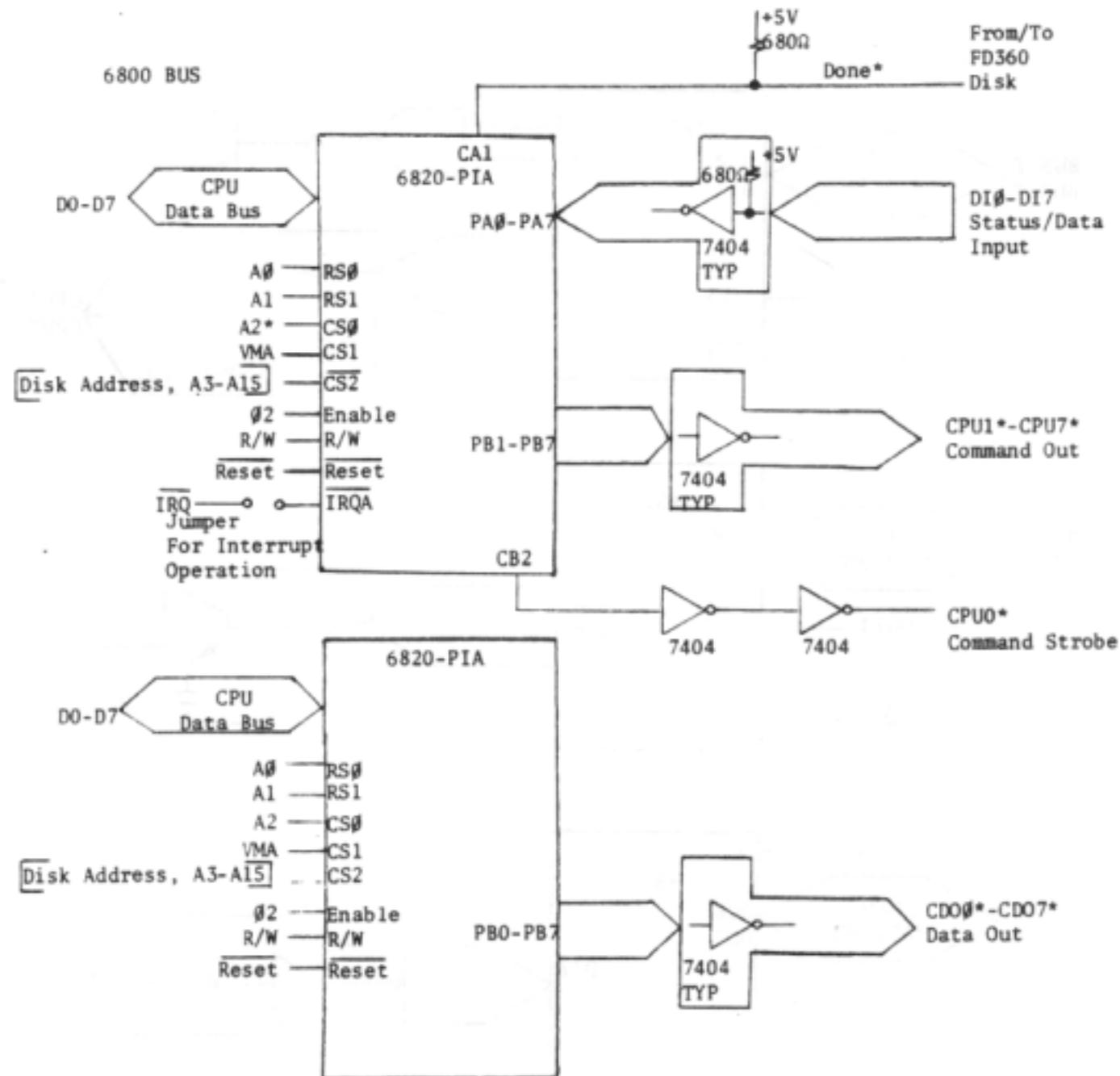


Figure 4-3. 6800 Interfacing Diagram

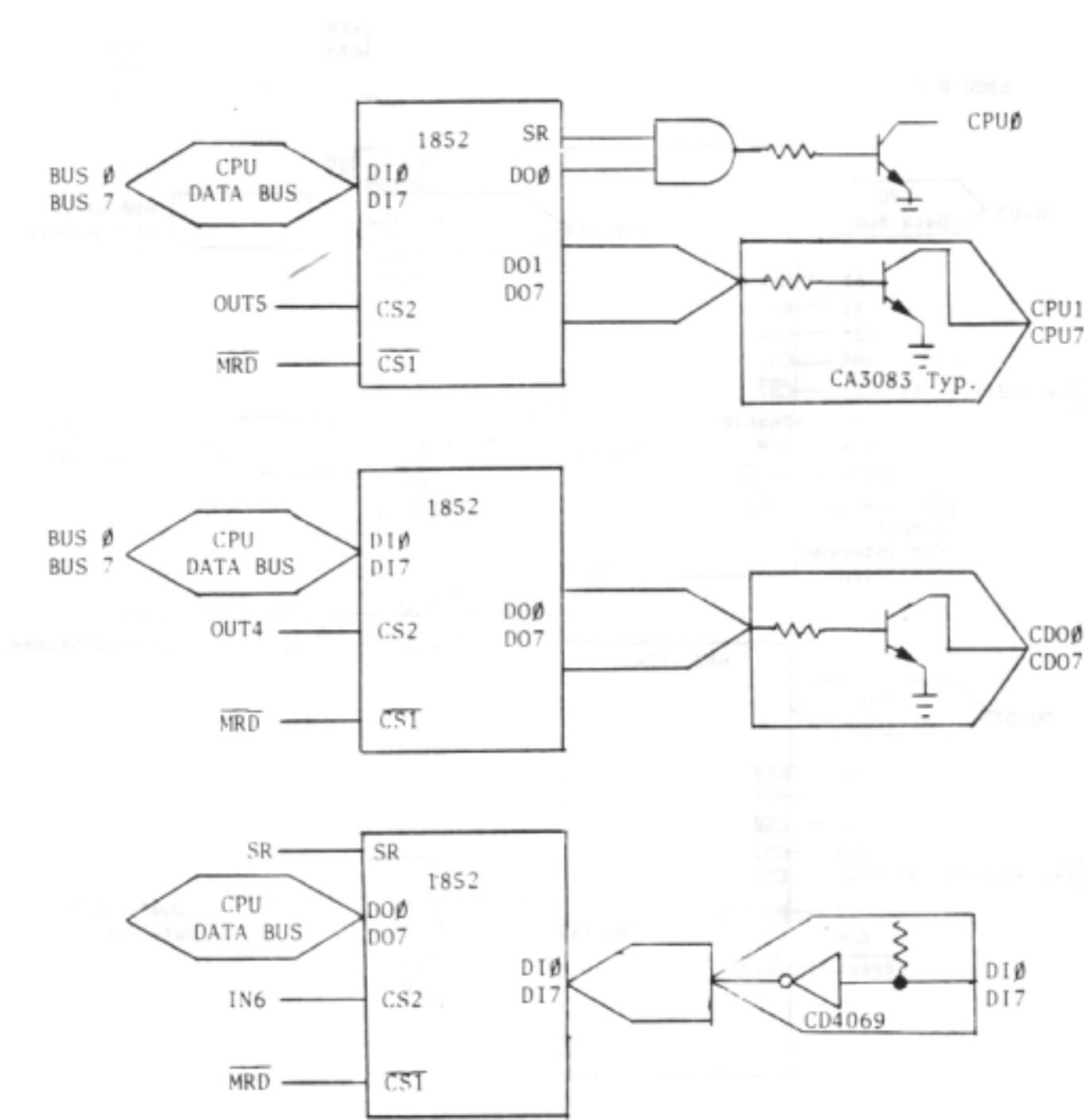


Figure 4-4. RCA 1800 Interfacing Diagram

SECTION IV INTERFACING DIAGRAMS

This section offers typical diagrams for the interfacing requirements for various microprocessors. Figure 4-1 depicts the interface for iCOM's FDOS-II OEM software. Figure 4-2 shows a possible hardware arrangement for custom designed systems. This is, however, not compatible with iCOM's FDOS-II software. A 6800 interface hook-up is given in figure 4-3. And an interface to a RCA 1800 microprocessor is shown by figure 4-4.



Figure 4-1 iCOM's FDOS-II OEM software



Figure 4-2 General hardware arrangement

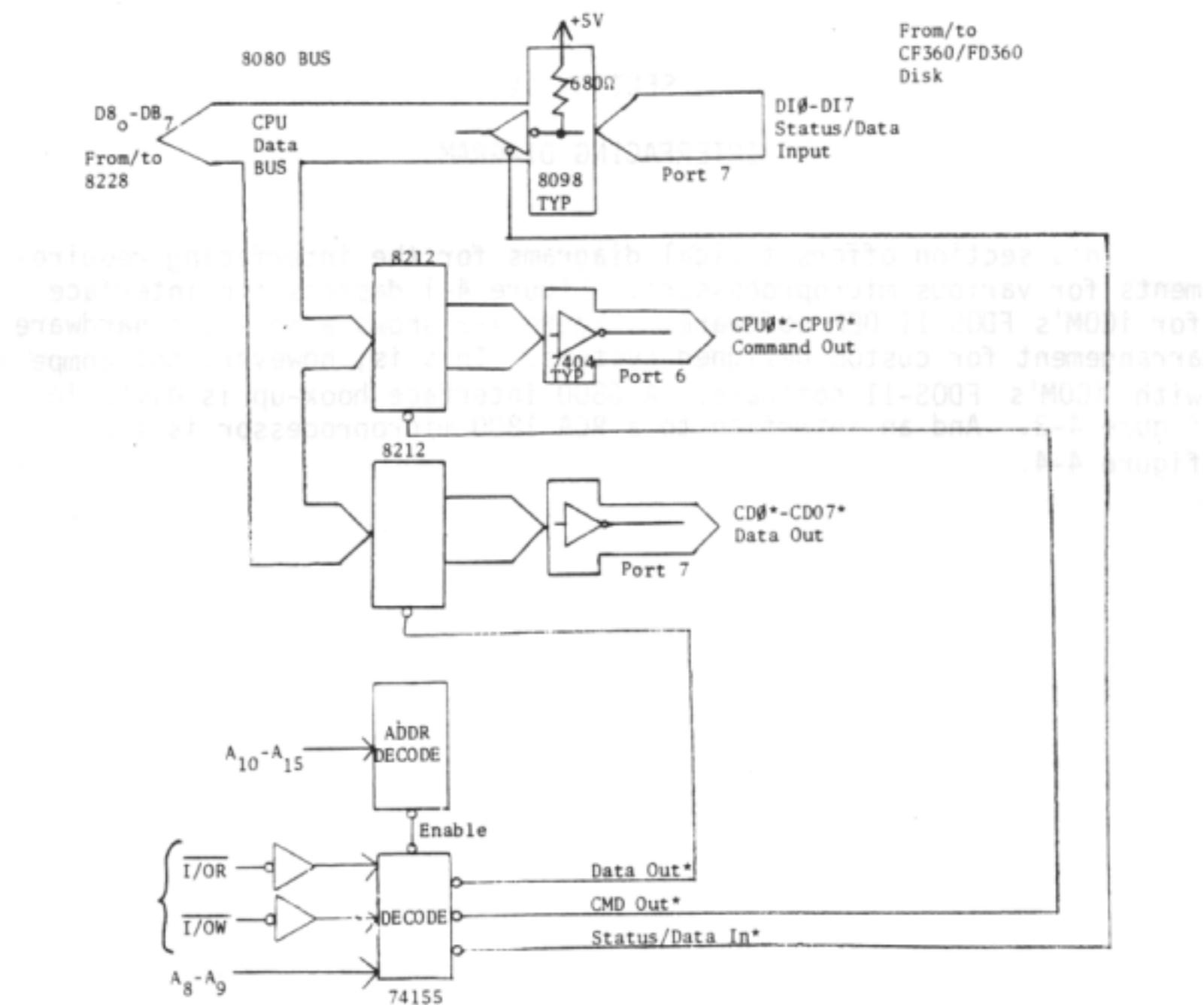


Figure 4-1. 8080 Interfacing Diagram

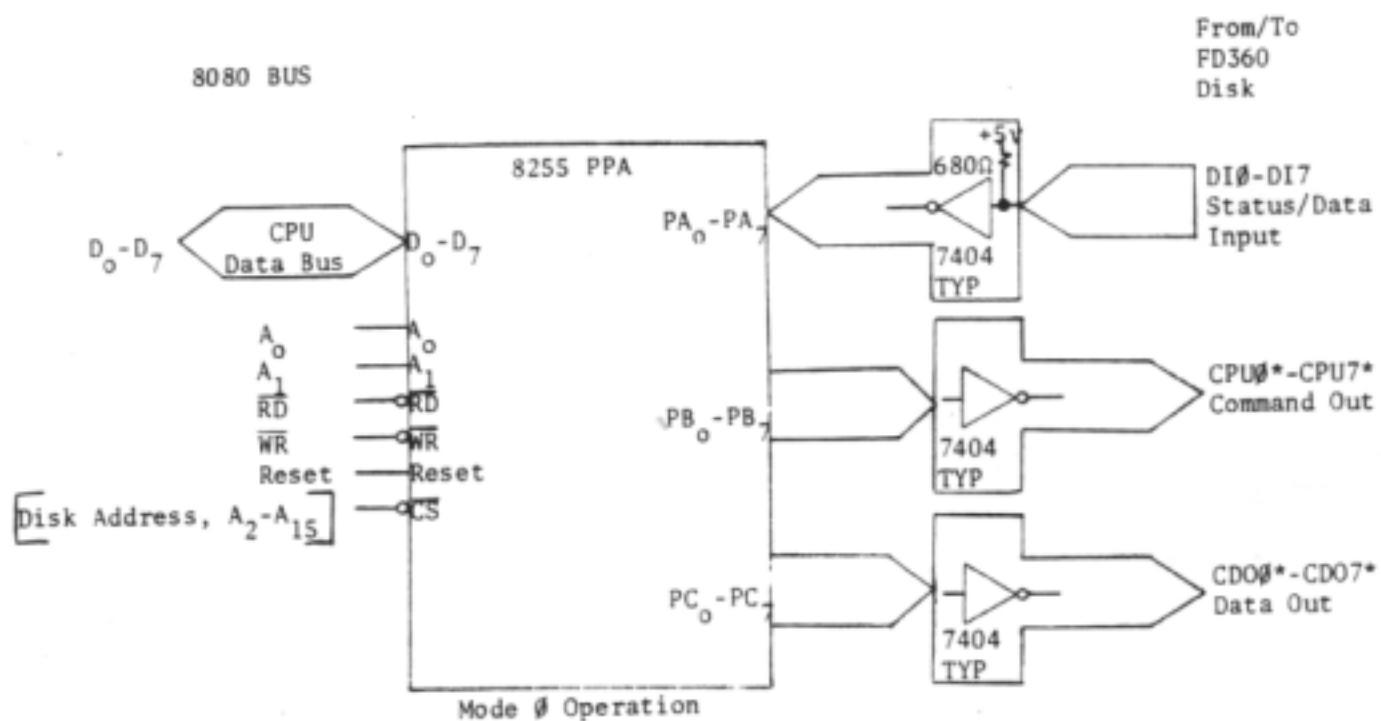


Figure 4-2. Alternate 8080 Interfacing Diagram

3-12 INTERCONNECTION DIAGRAM

MADE BY: [unclear] JAN 12 1985

A typical floppy disk system interconnection diagram is given by figure 3-3. Take special note of the fact that drive 0 is at the terminus of the disk drive cable.

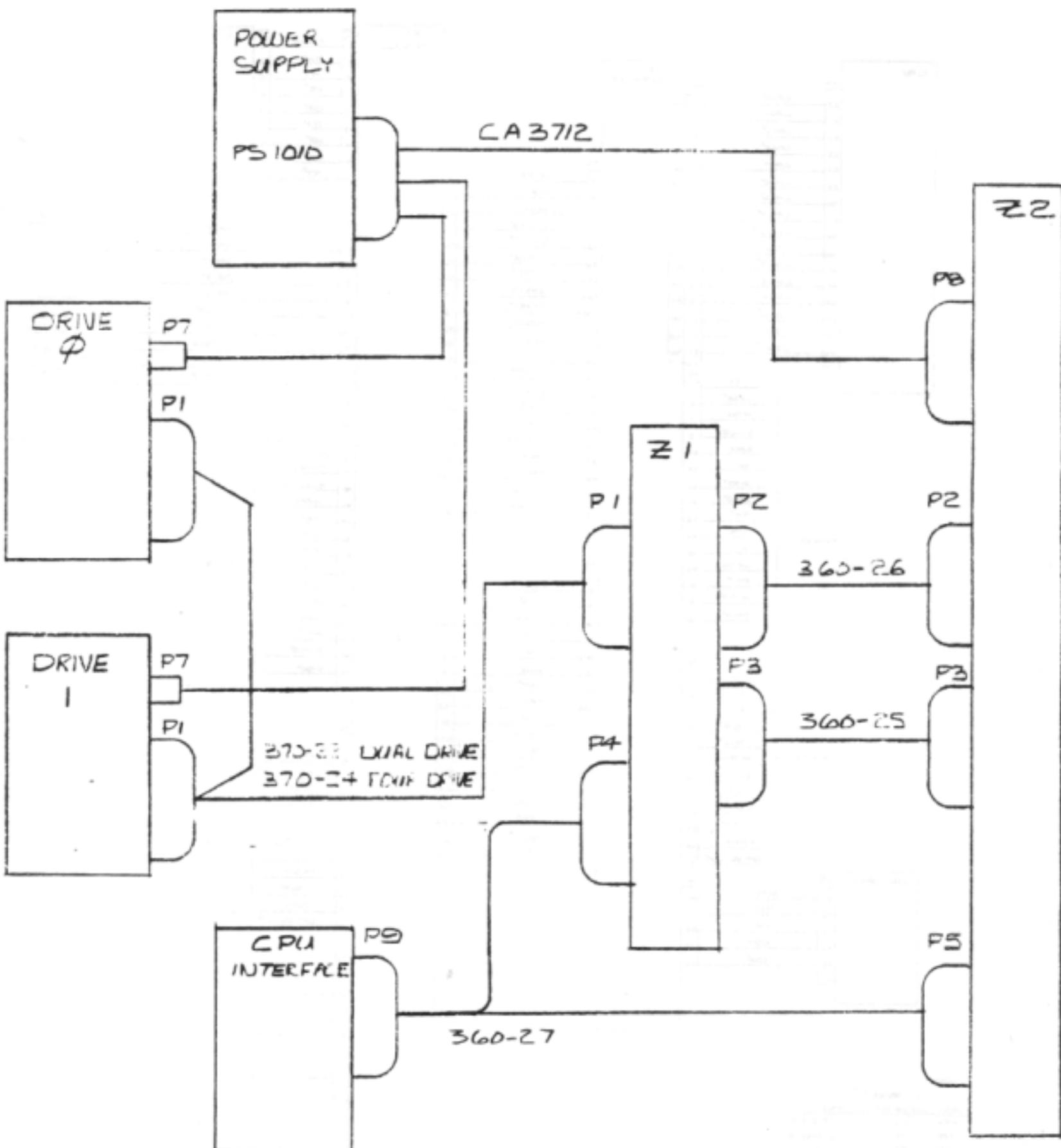


Figure 3-3. Interconnection Diagram

3-13 SIGNAL FLOW DIAGRAM

3-13 SIGNAL FLOW DIAGRAM

Figure 3-4 presents the signal flow of a disk drive system, and the CF3700 in particular. Signal flow is from left to right. Thus, an item may appear more than once to illustrate input and output signals. The outputs of Z2 are referenced back to Z1 as inputs.

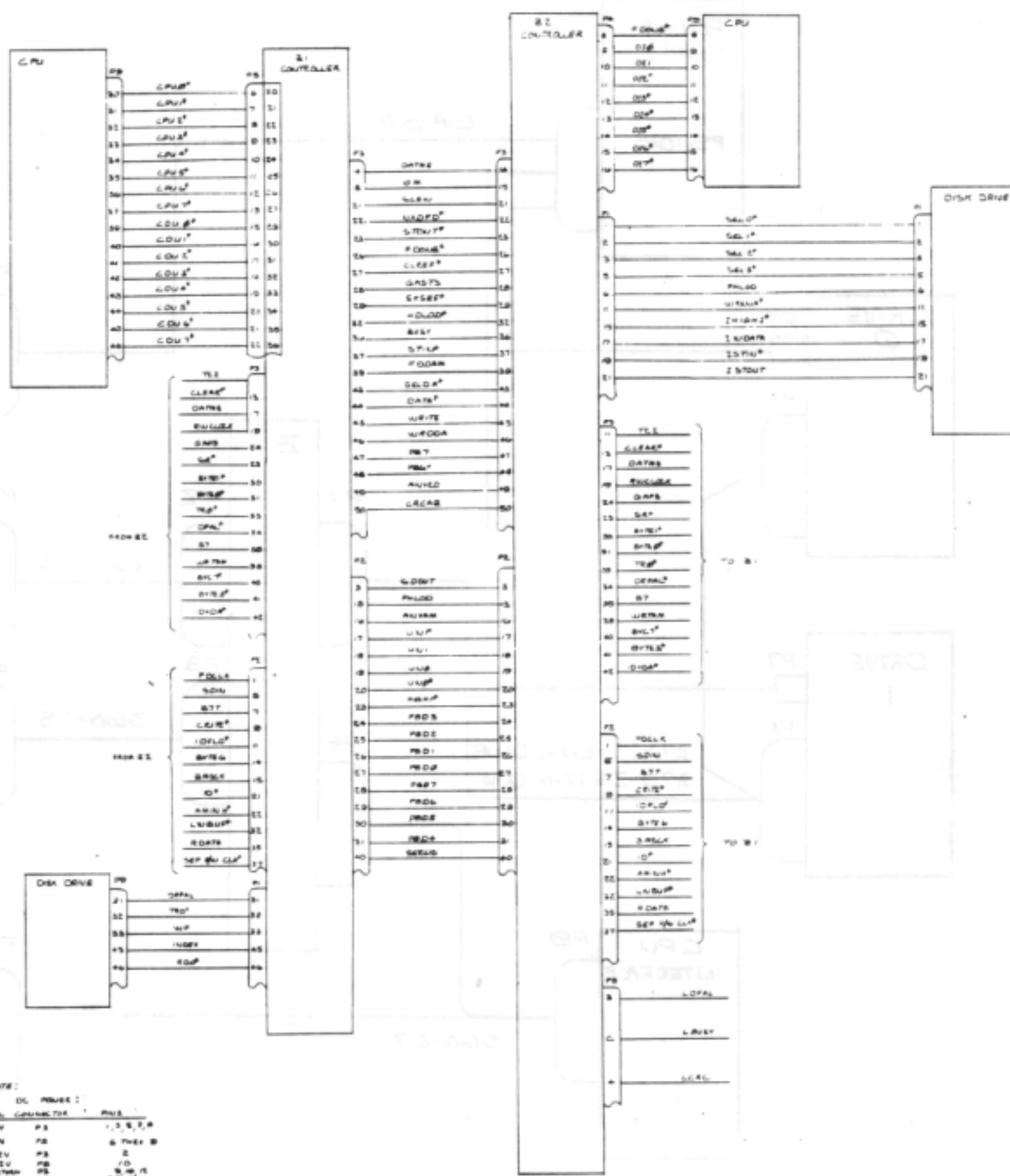


Figure 3-4. Signal Flow Diagram



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CORRECTION SHEET

Publication: Interfacing Guide for
iCOM Model FD3700 Series Floppy Disk Systems
iCOM Frugal FloppyTM Model FF37
and
Controller/Formatter Model CF3700

Dated: April, 1977

Instructions: Replace pages 17 and 18 with revised pages.

3-12 INTERCONNECTION DIAGRAM

A typical floppy disk system interconnection diagram is given by figure 3-3. Take special note of the fact that drive 0 is at the terminus of the disk drive cable.

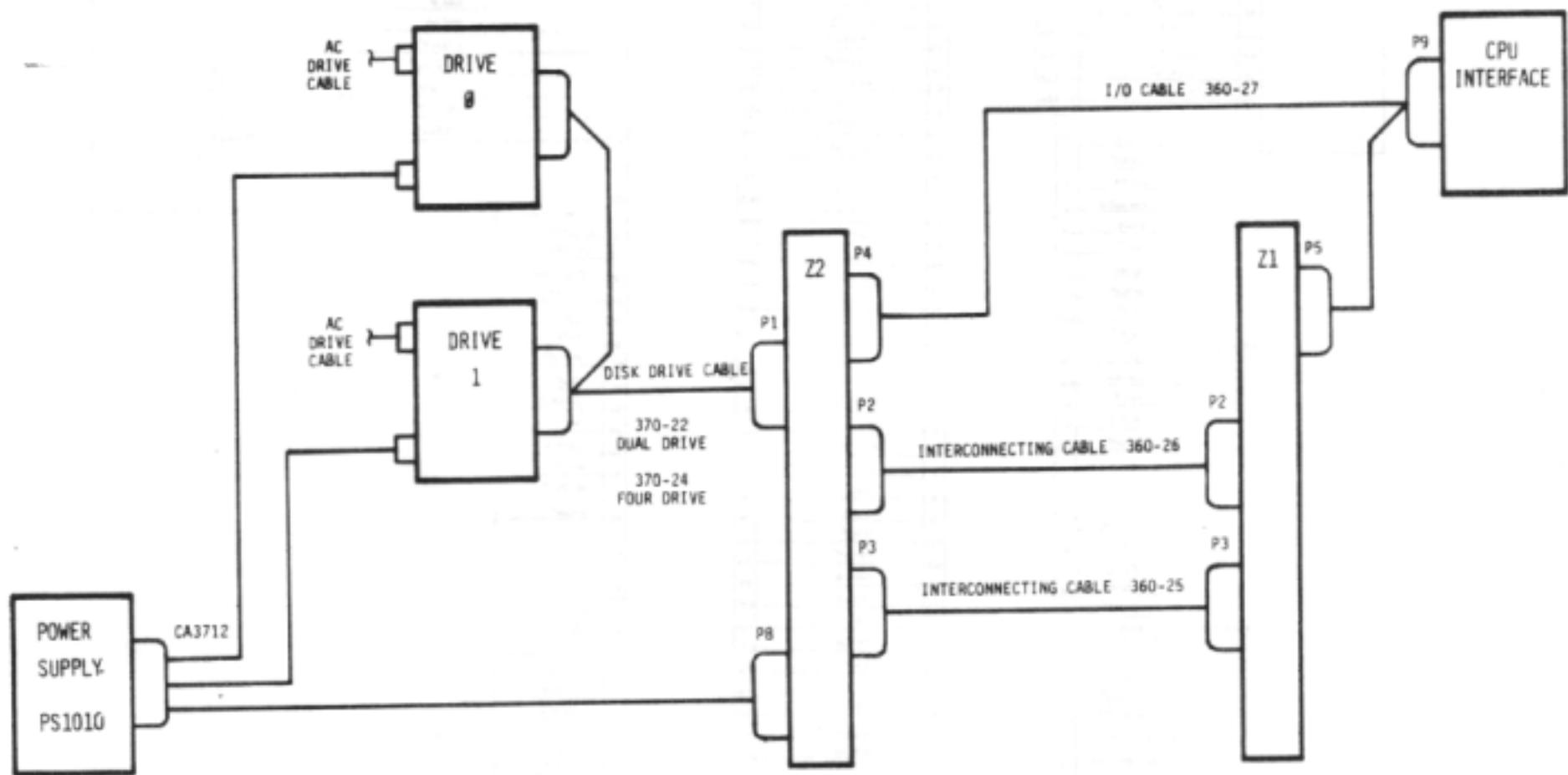


Figure 3-3. Interconnection Diagram